

FIG. 1

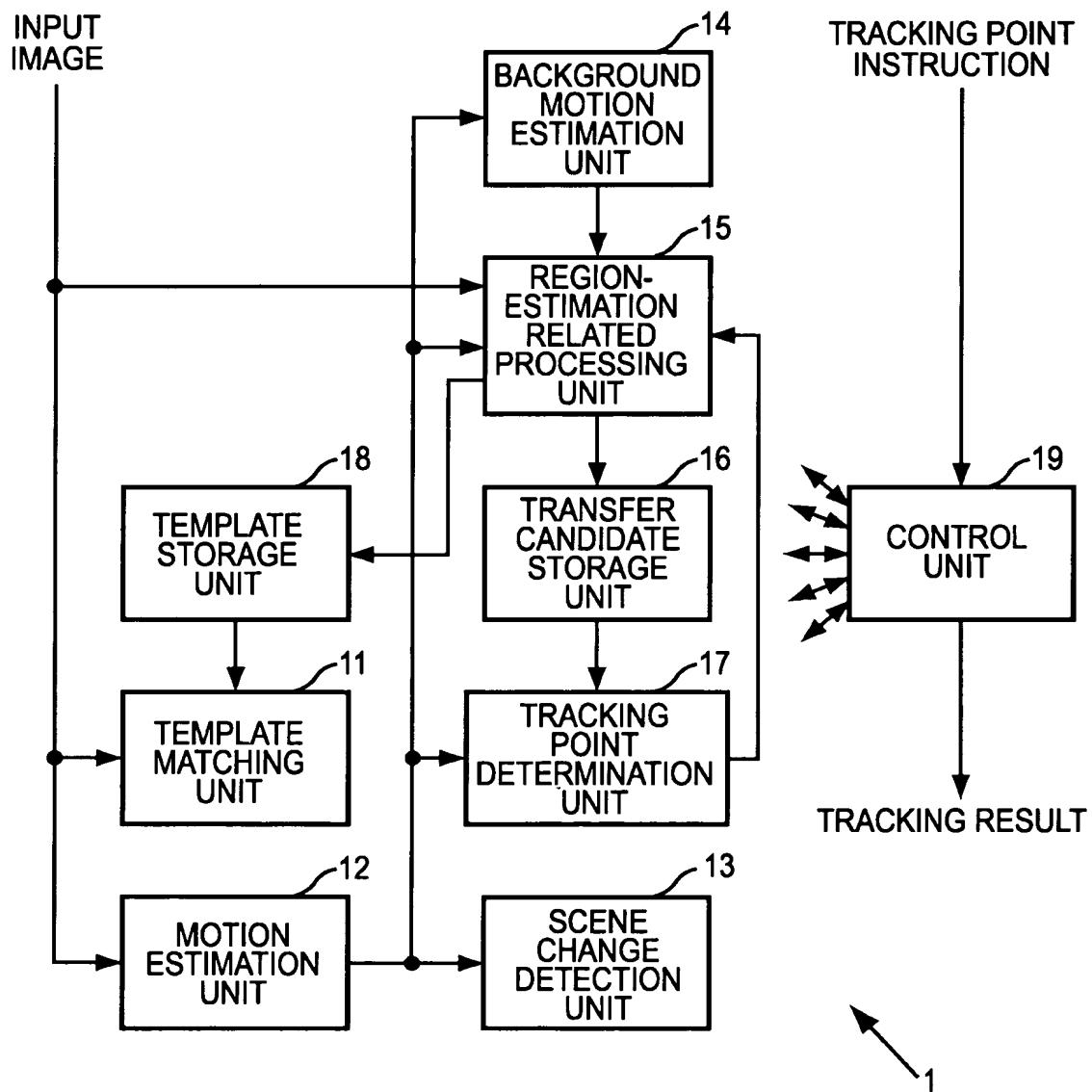


FIG. 2

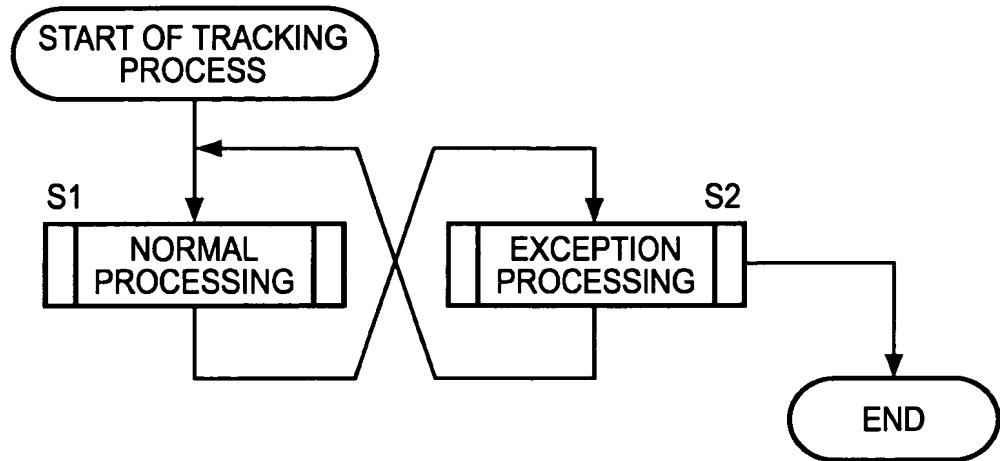


FIG. 3

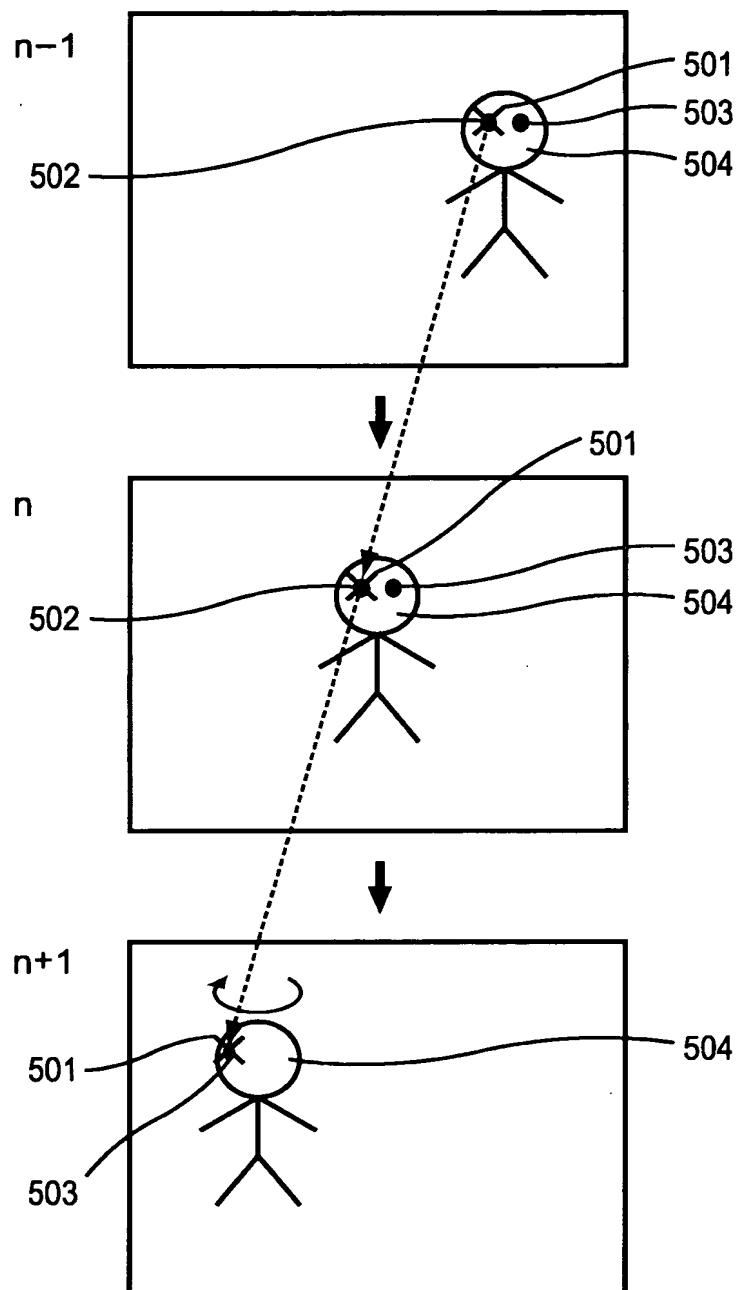


FIG. 4

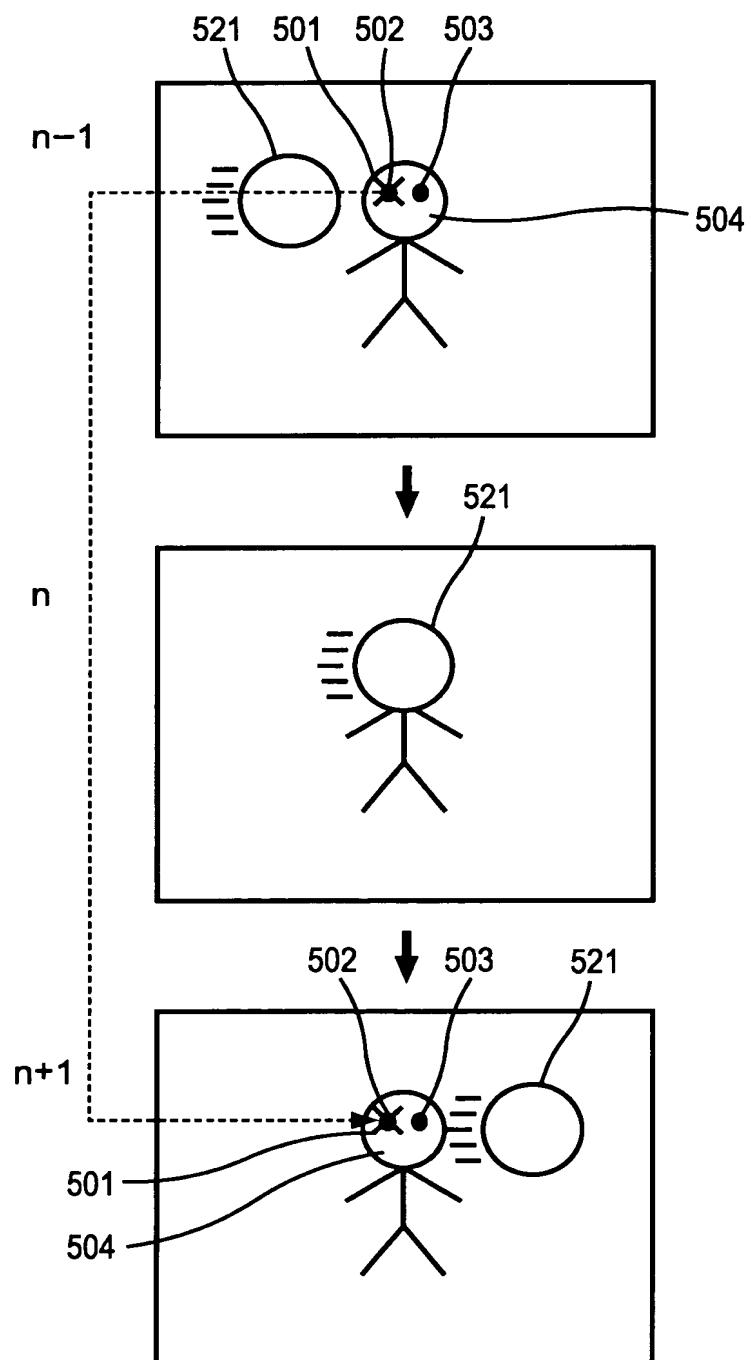


FIG. 5

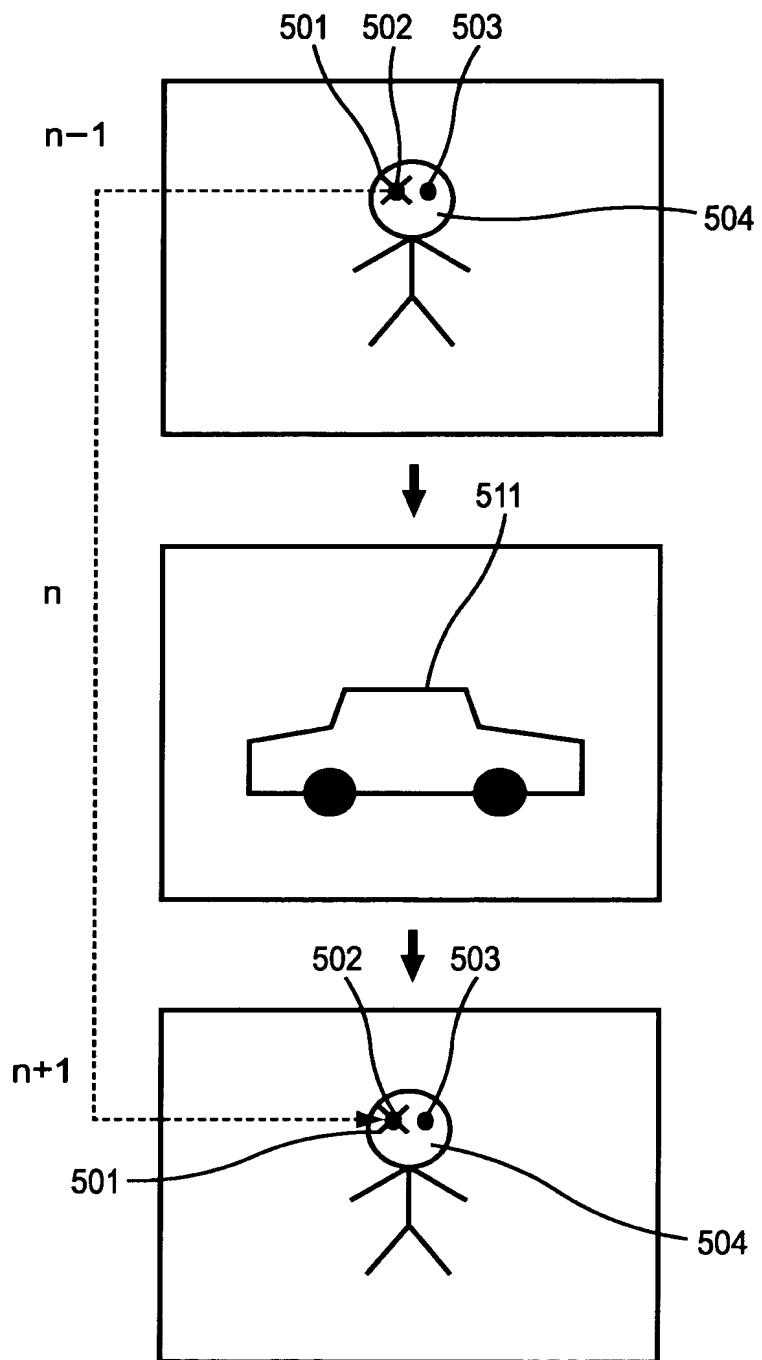


FIG. 6

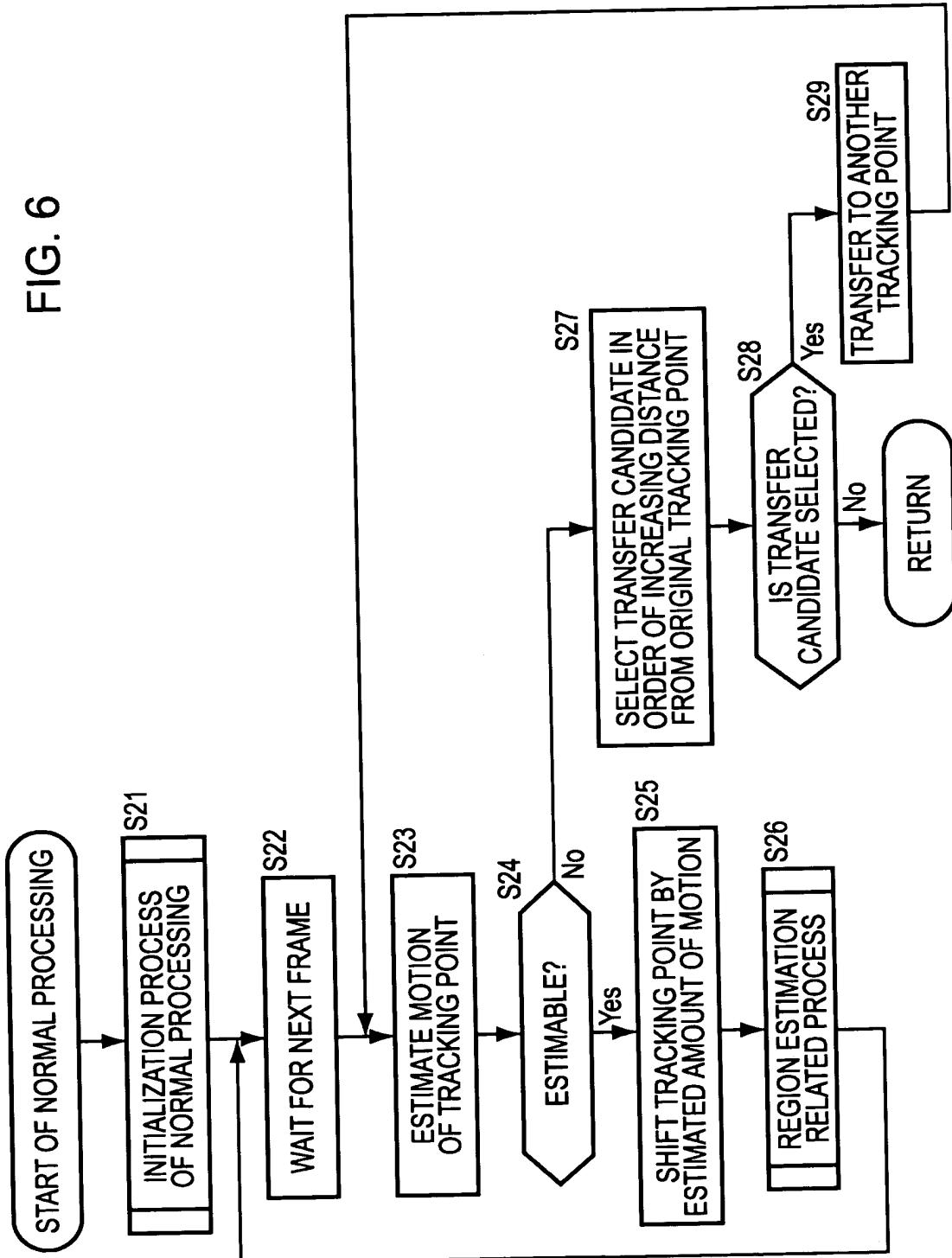


FIG. 7

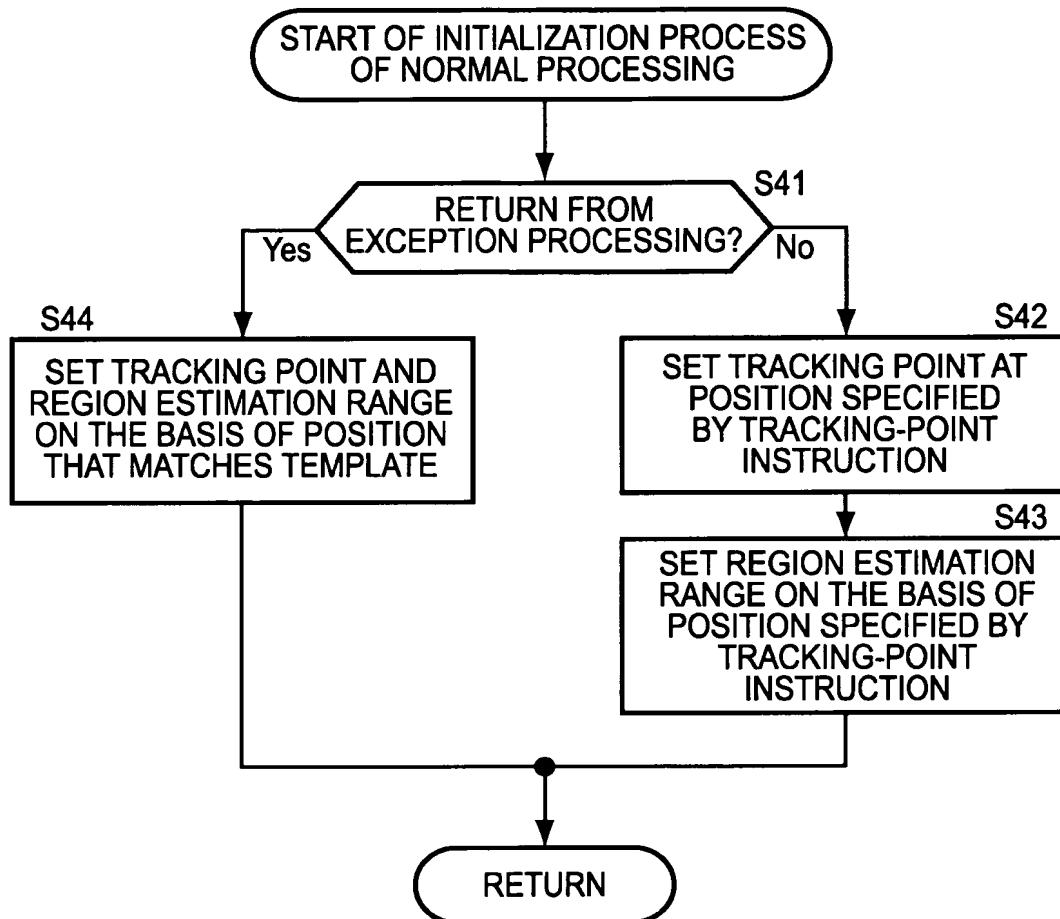


FIG. 8

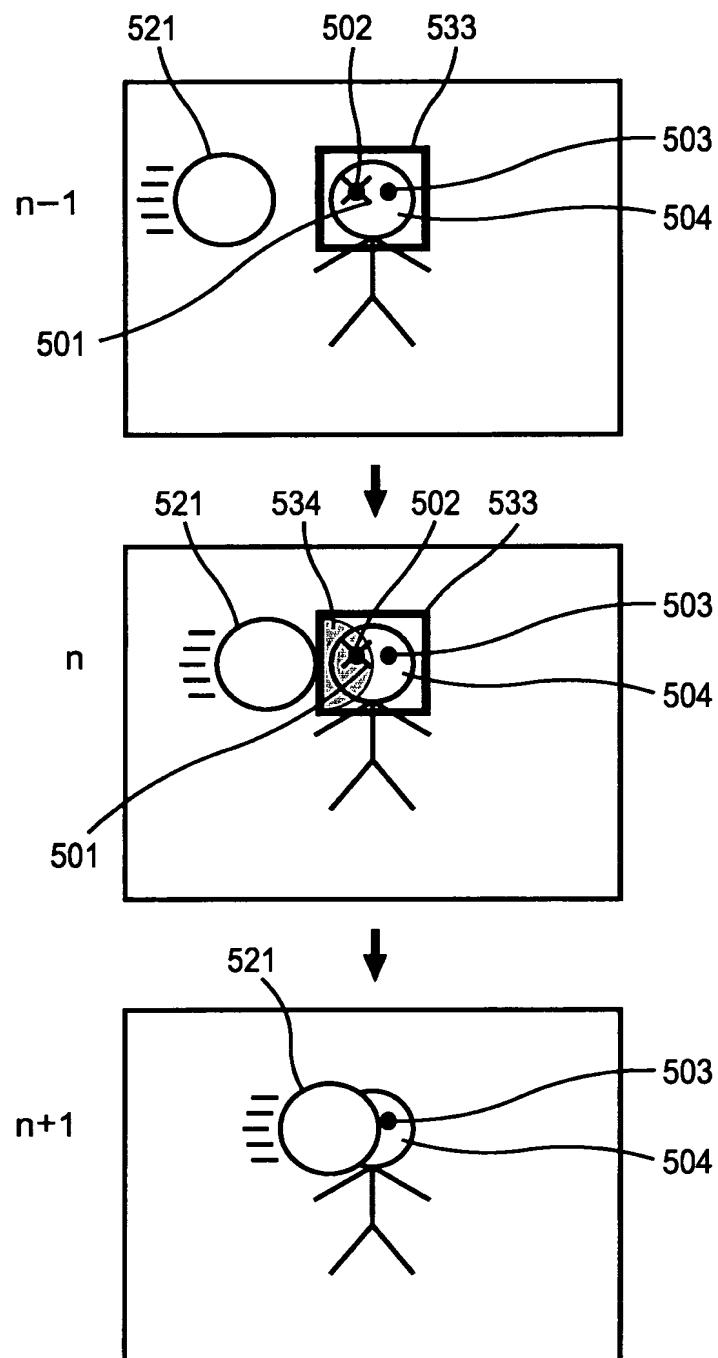
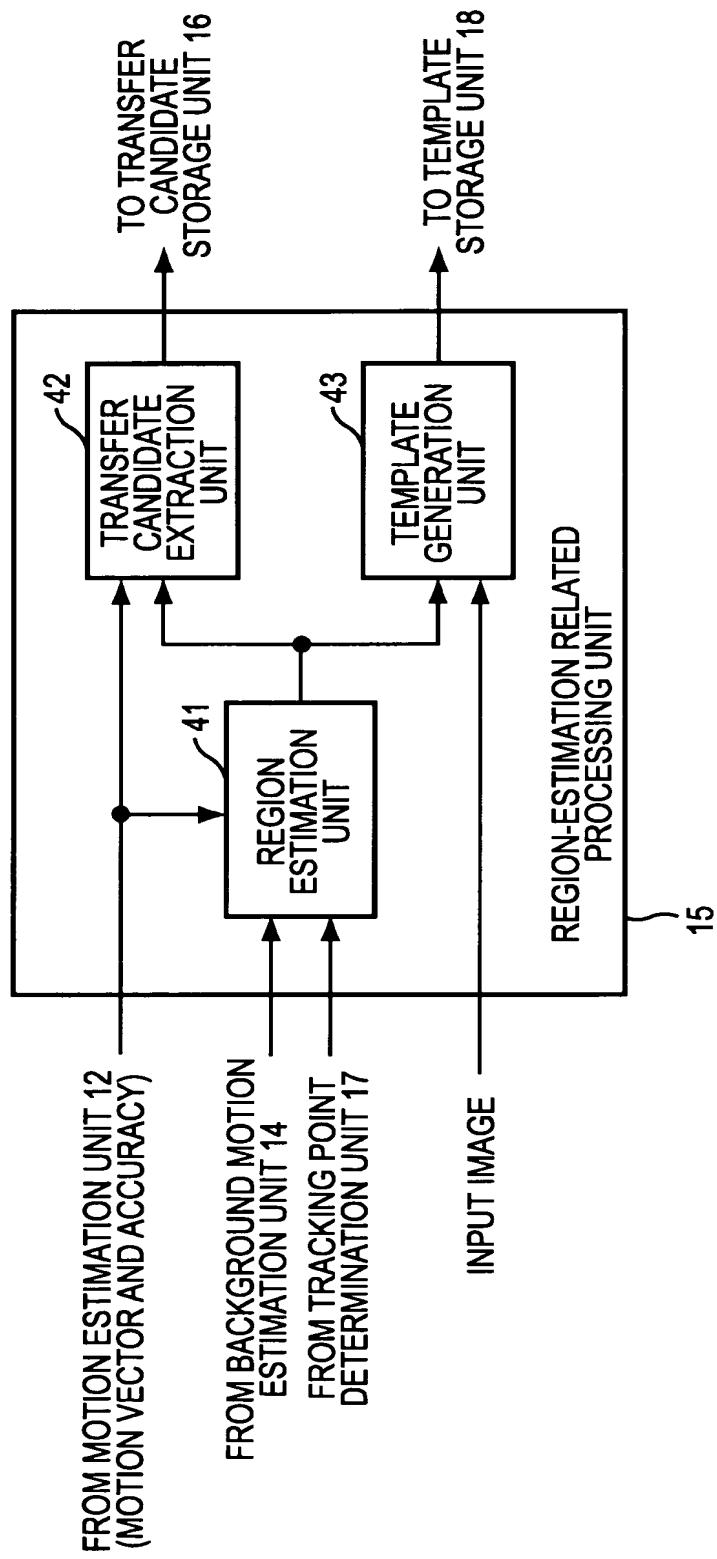
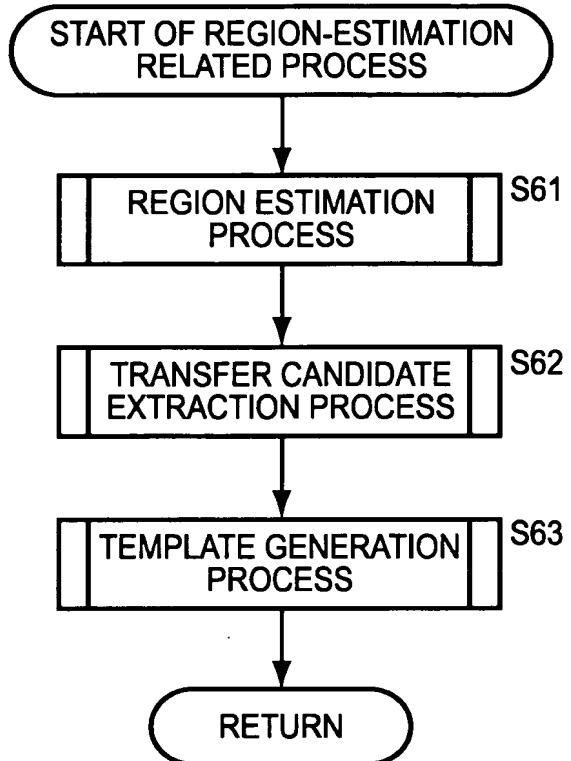


FIG. 9



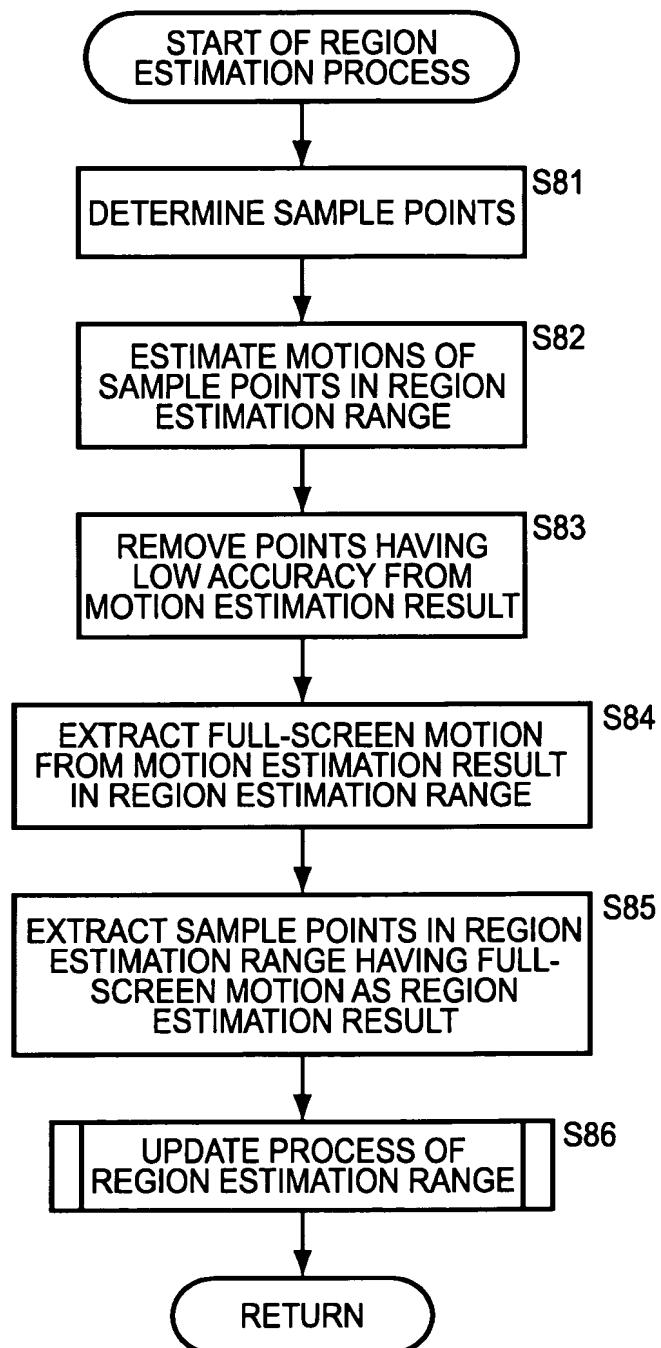
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FIG. 10



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FIG. 11



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FIG. 12A

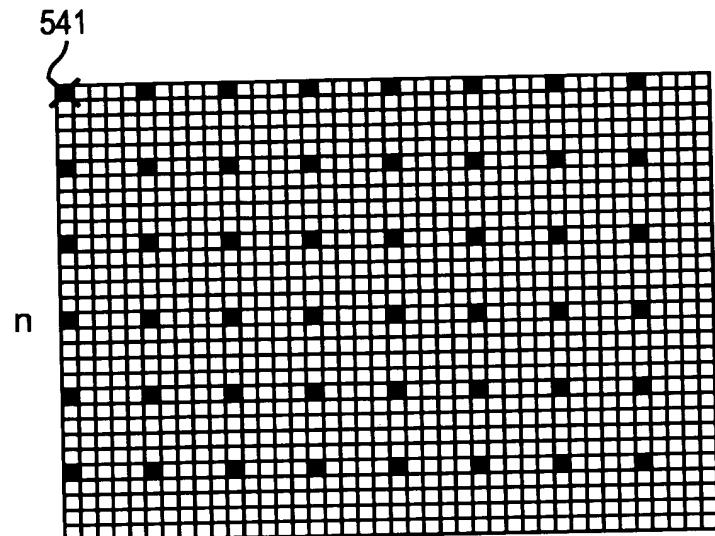


FIG. 12B

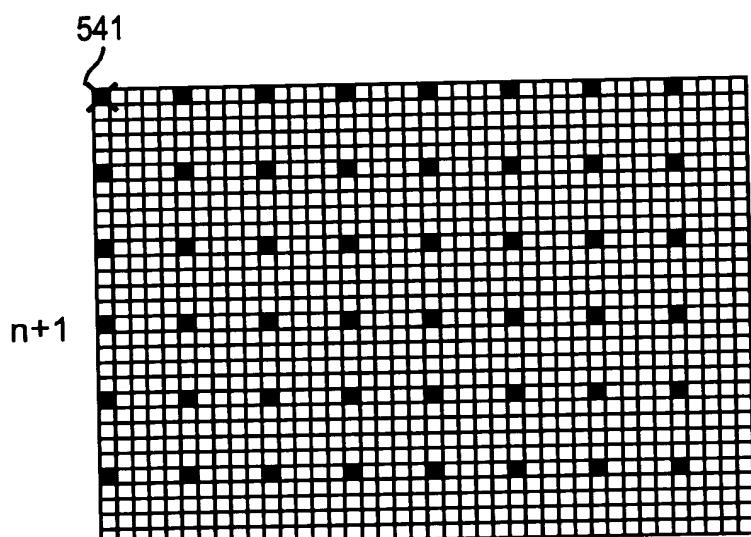


FIG. 13A

n

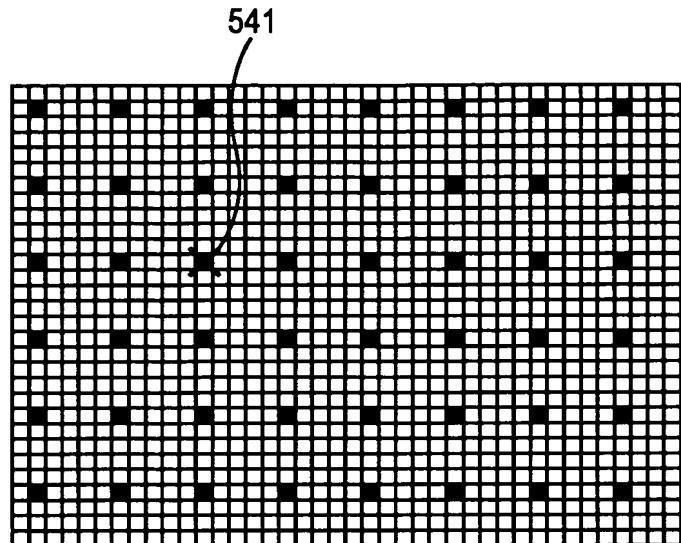
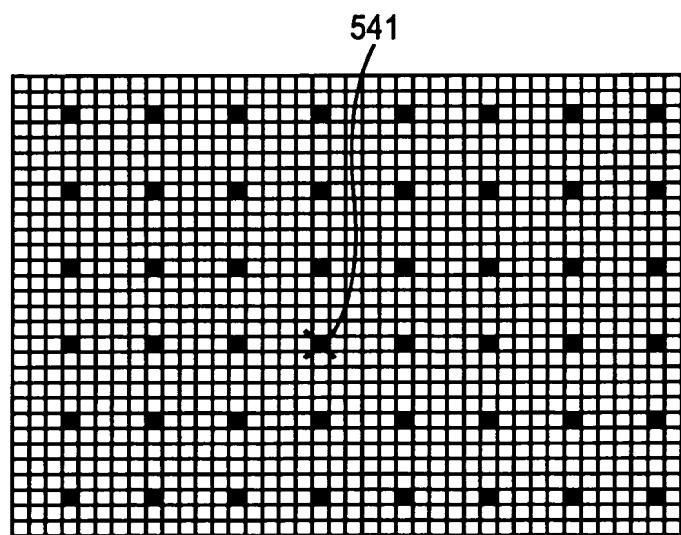


FIG. 13B

$n+1$



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FIG. 14A

n

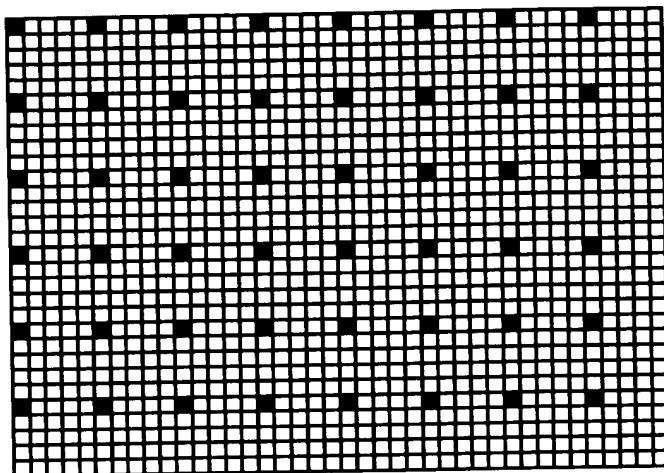


FIG. 14B

$n+1$

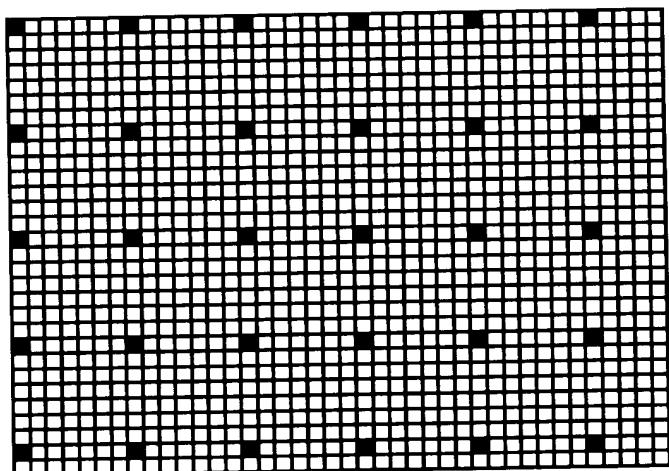
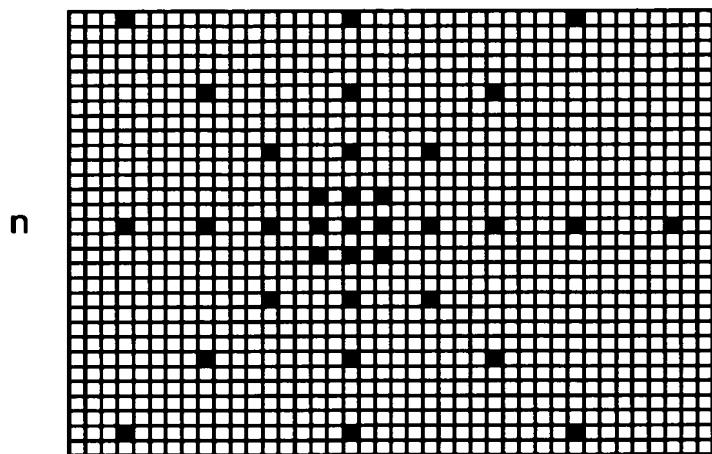
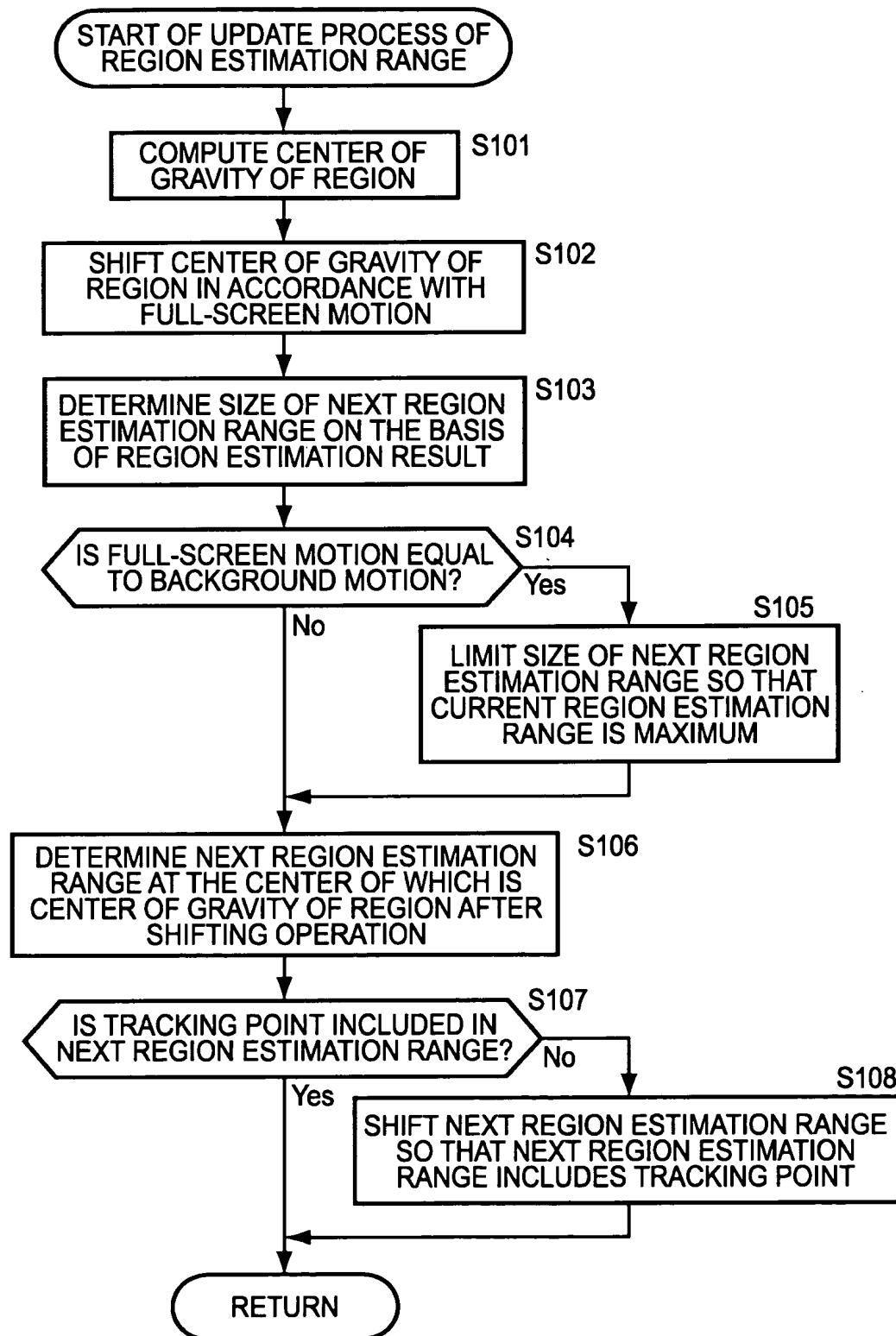


FIG. 15



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FIG. 16



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FIG. 17A

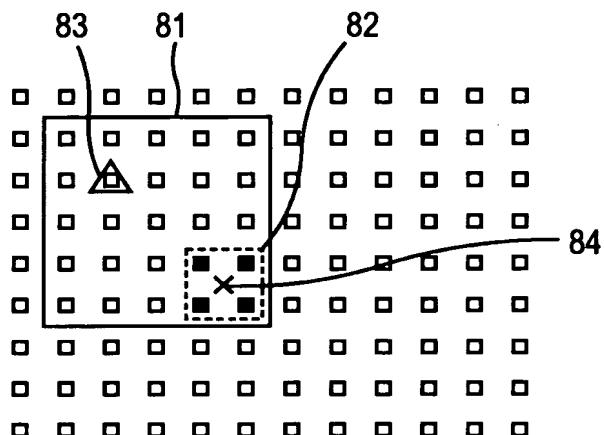


FIG. 17B

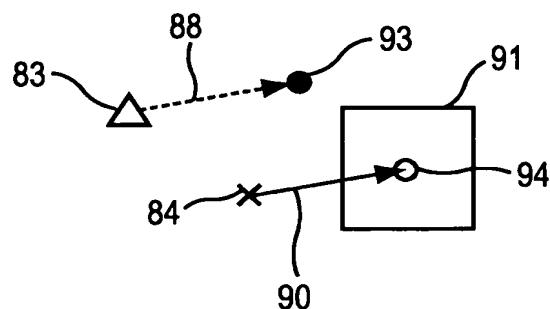
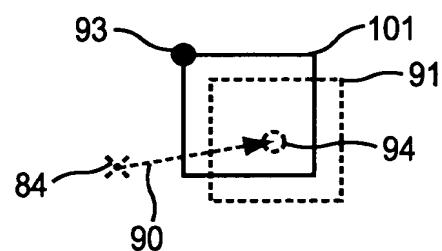


FIG. 17C



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FIG. 18A

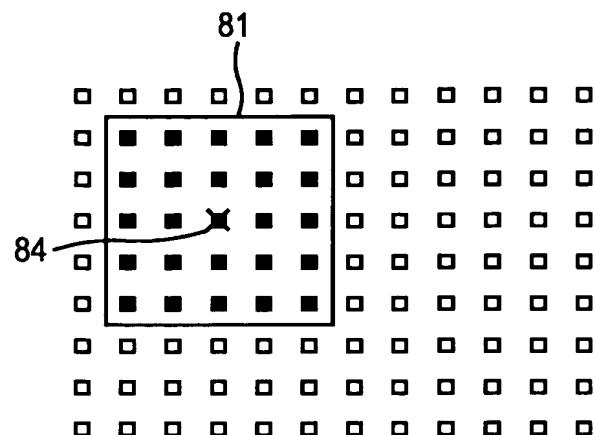


FIG. 18B

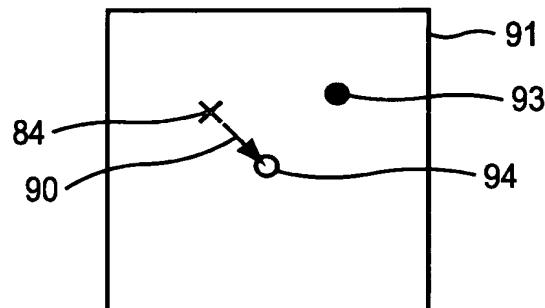
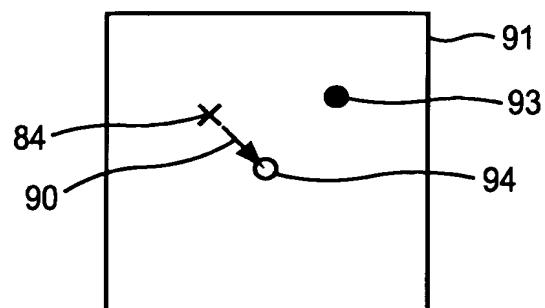


FIG. 18C



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FIG. 19A

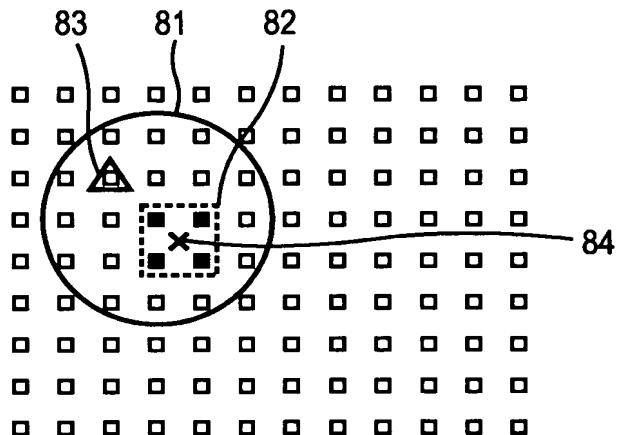


FIG. 19B

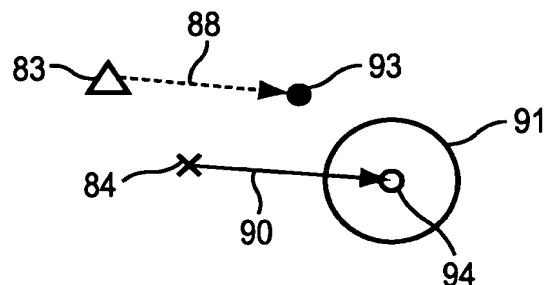
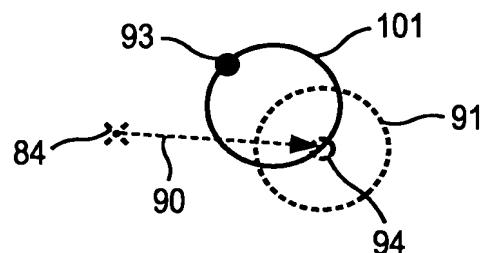


FIG. 19C



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FIG. 20A

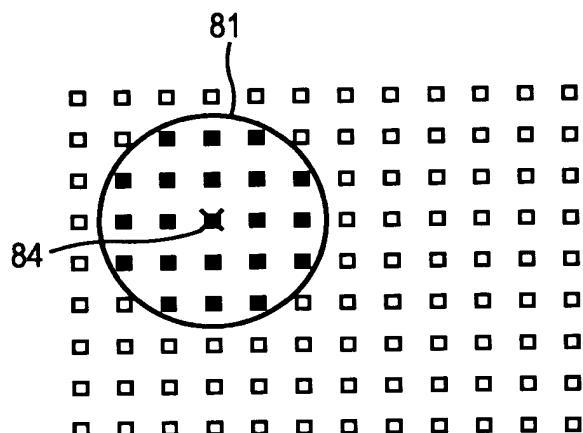


FIG. 20B

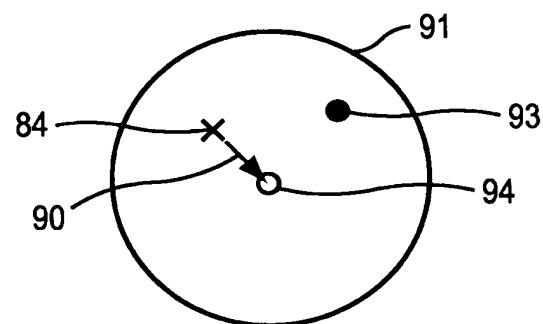


FIG. 20C

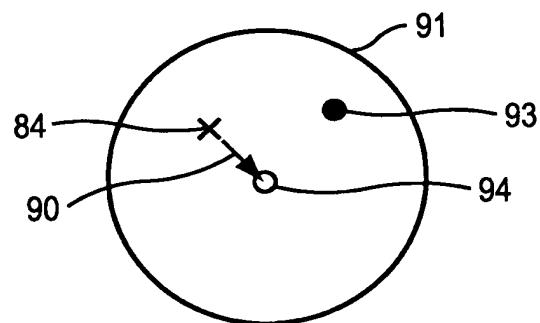


FIG. 21

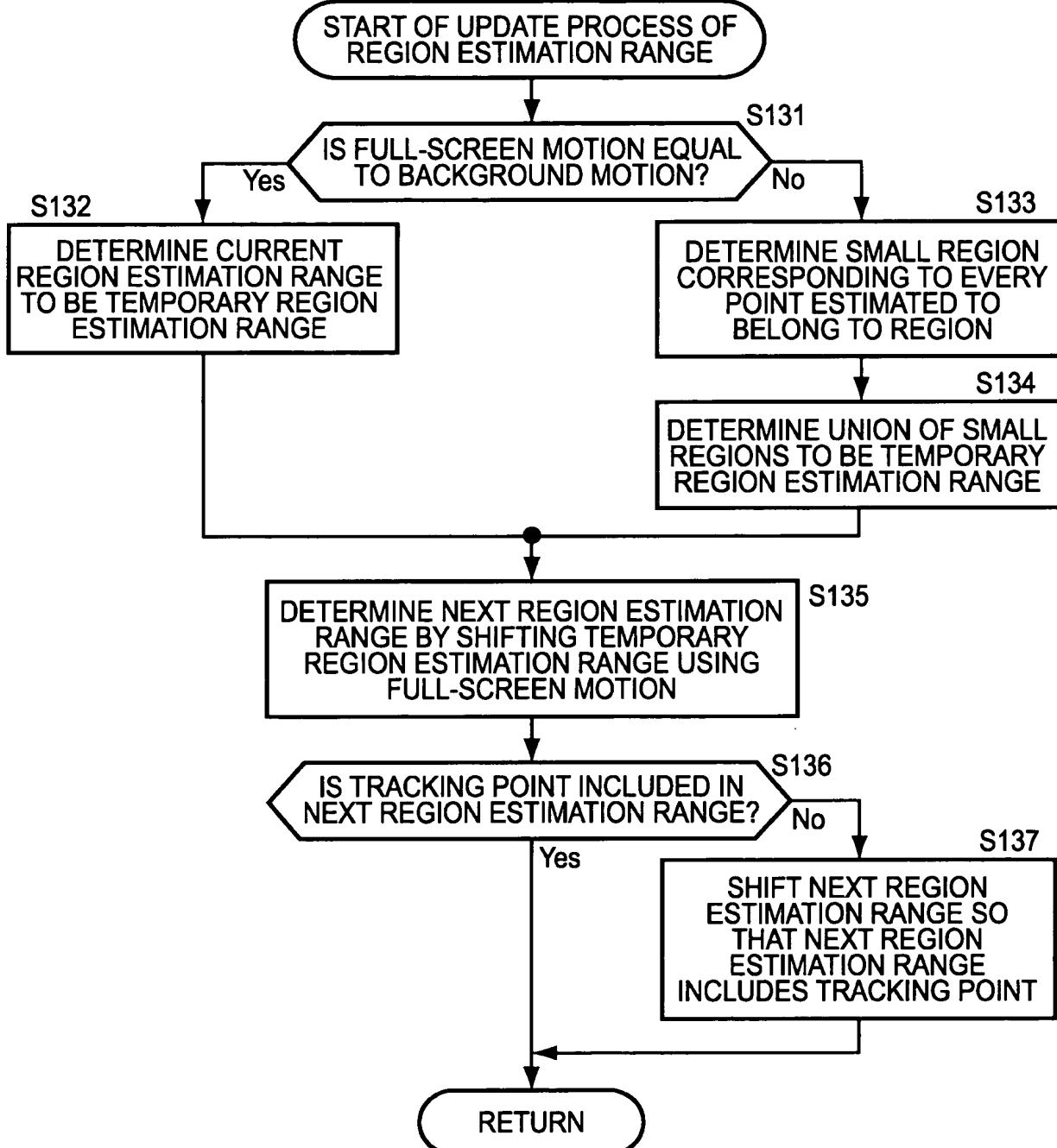


FIG. 22A

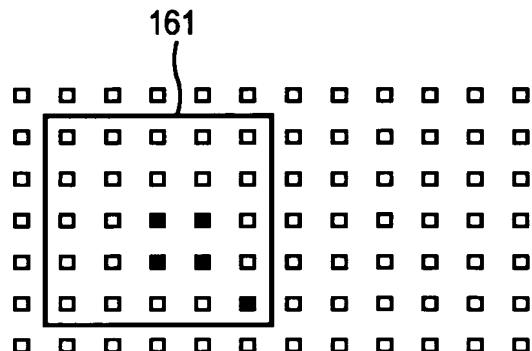


FIG. 22B

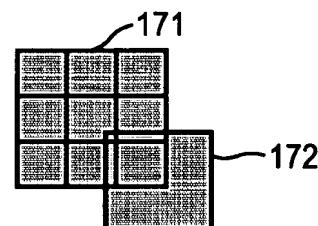


FIG. 22C

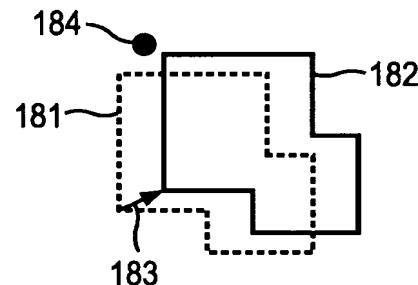
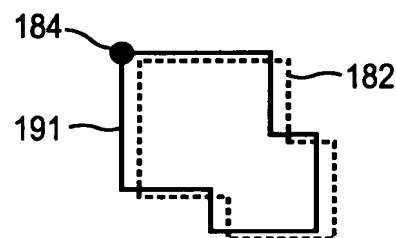


FIG. 22D



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FIG. 23

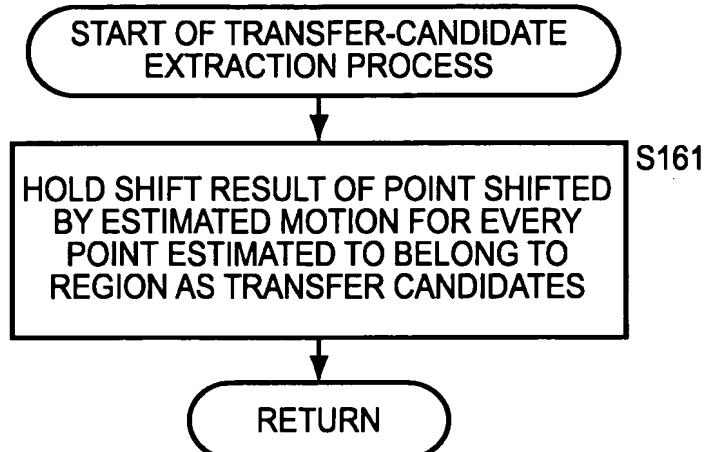


FIG. 24

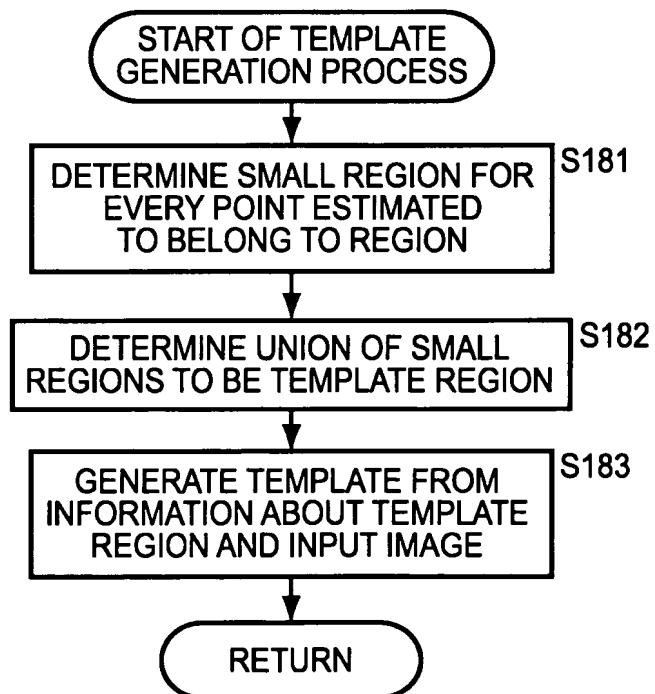


FIG. 25

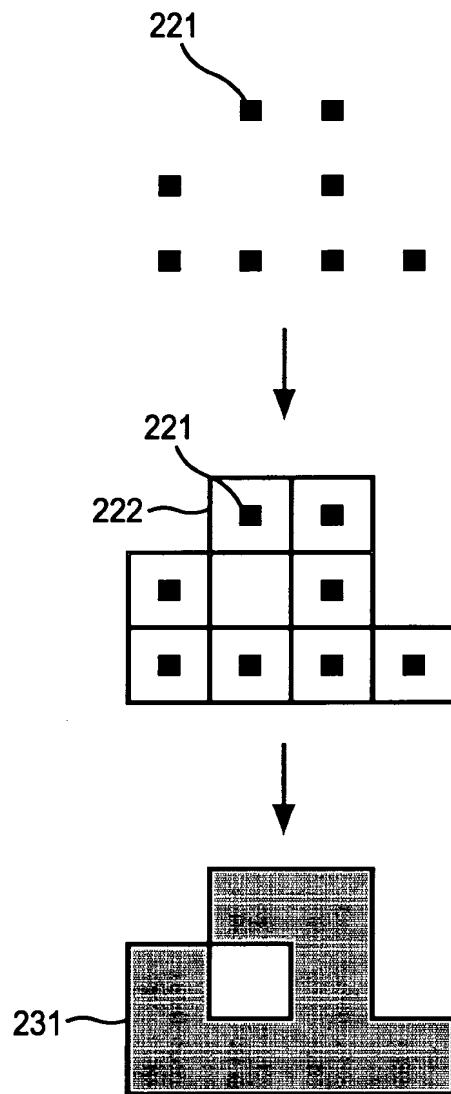


FIG. 26

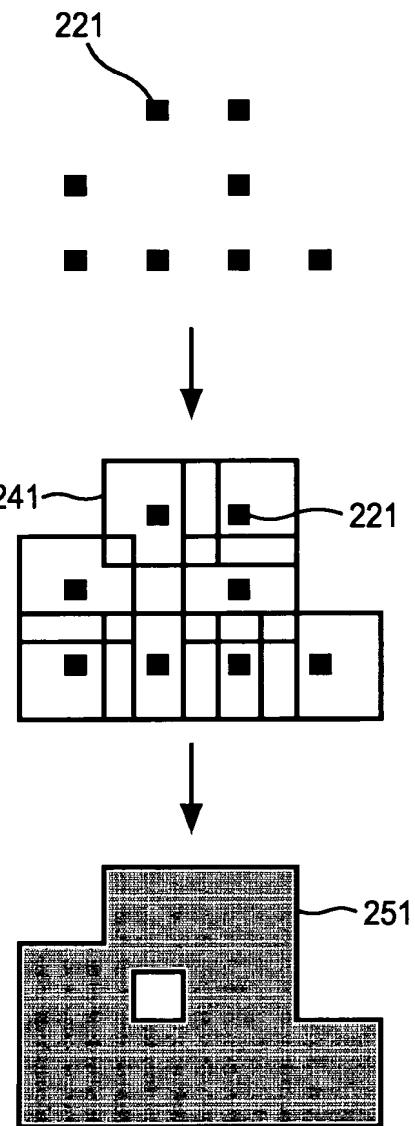


FIG. 27

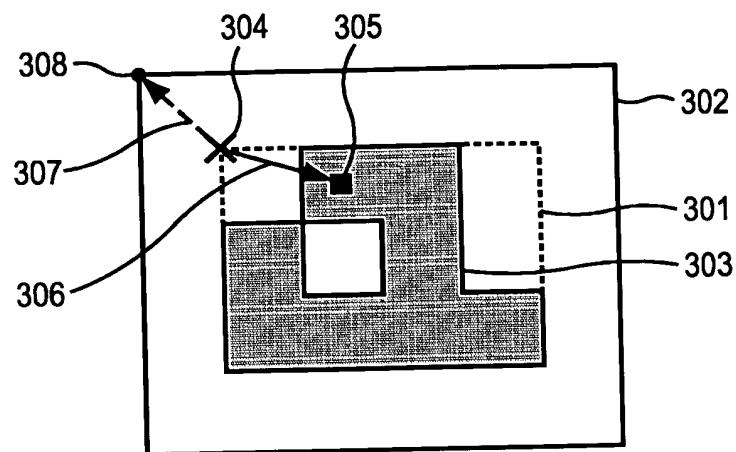


FIG. 28

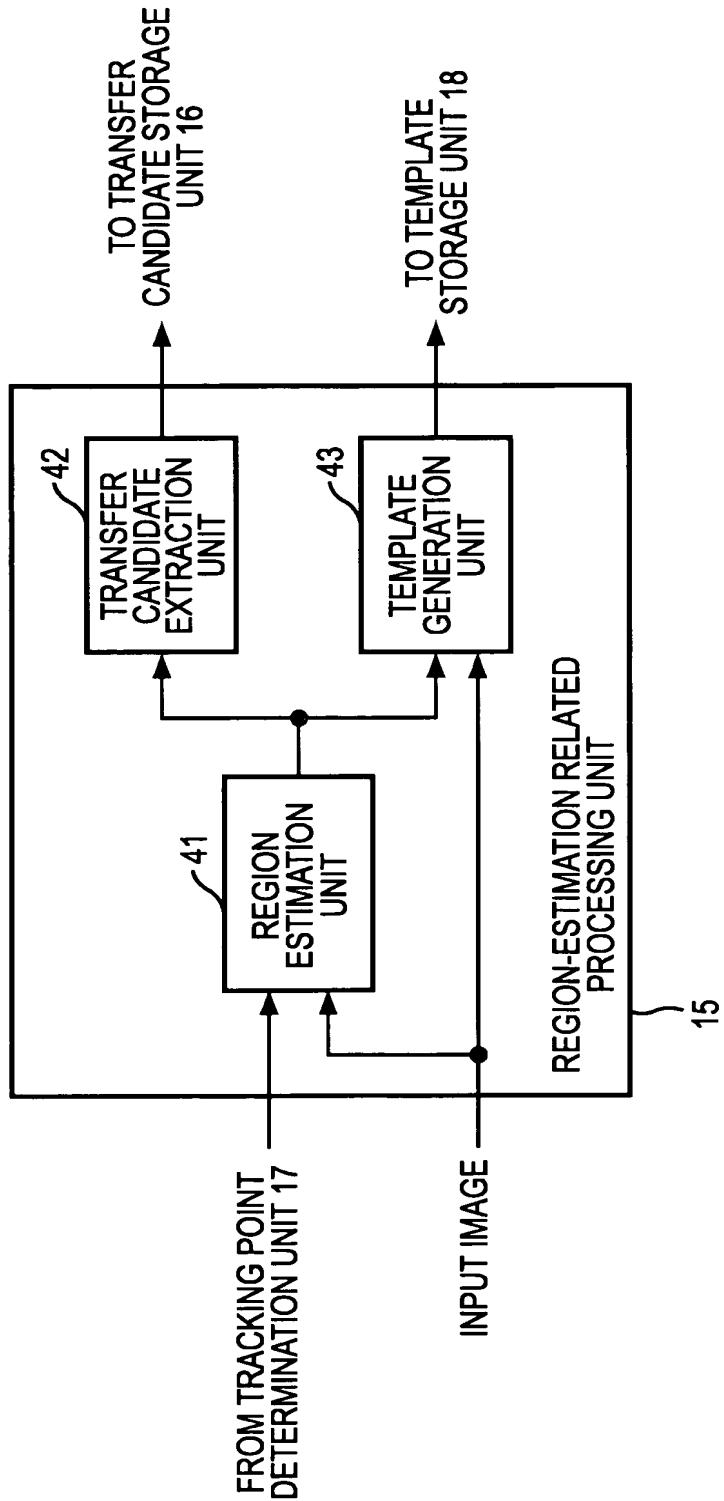
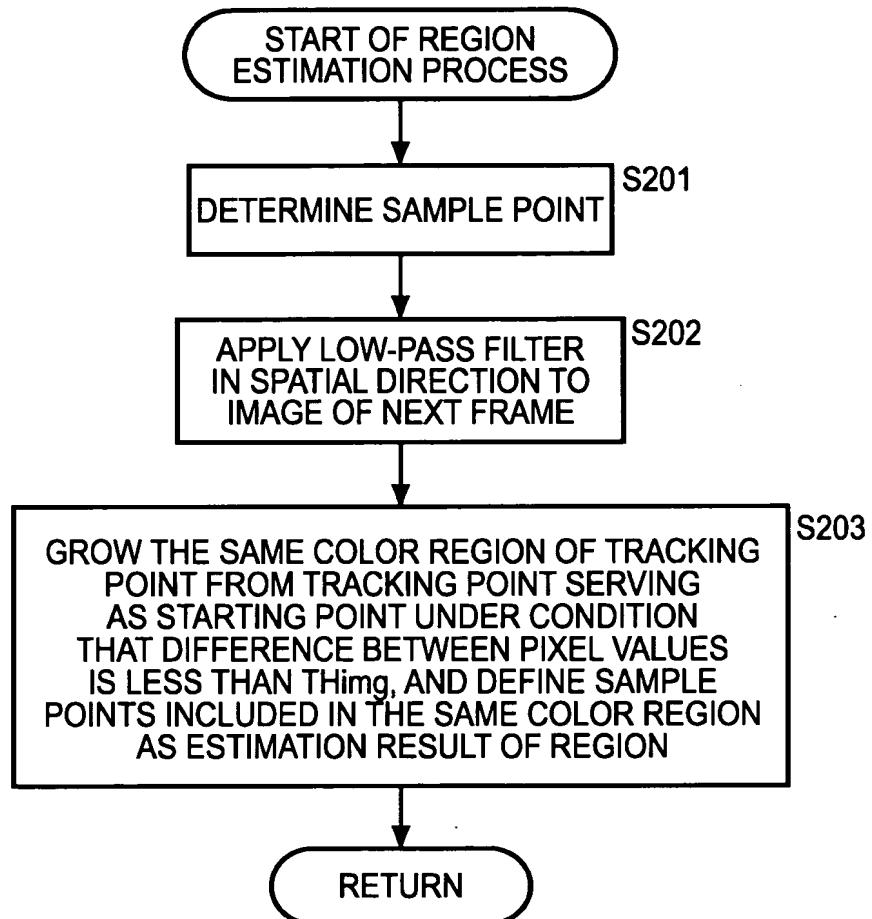


FIG. 29



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FIG. 30A

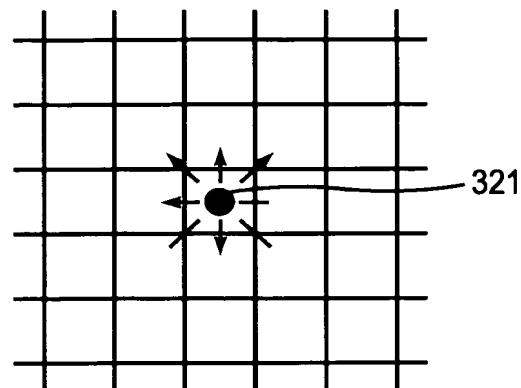


FIG. 30B

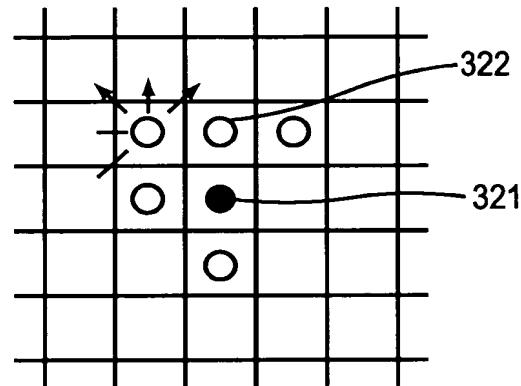


FIG. 30C

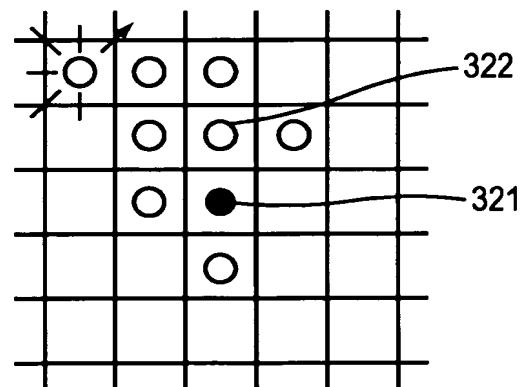


FIG. 31

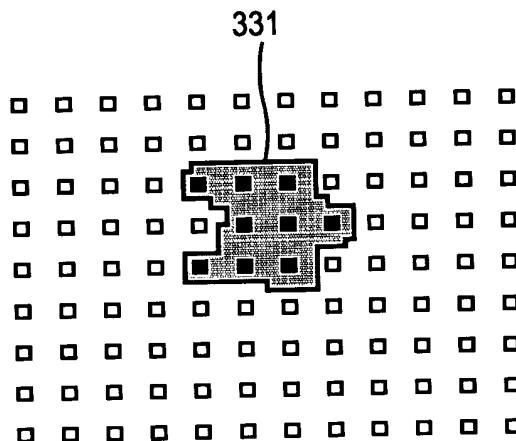
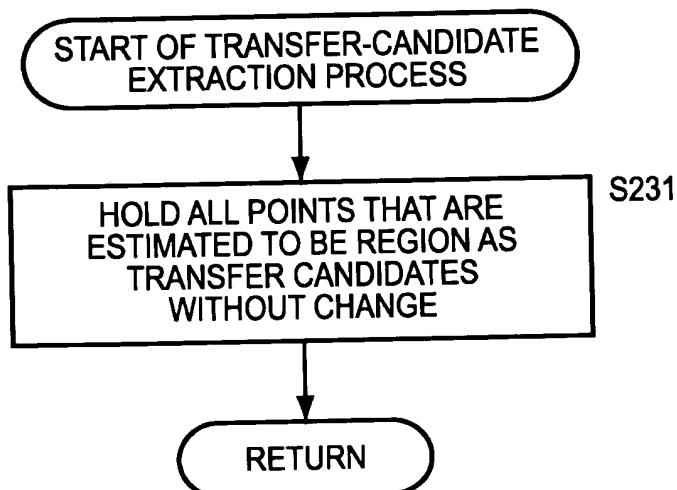


FIG. 32



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FIG. 33

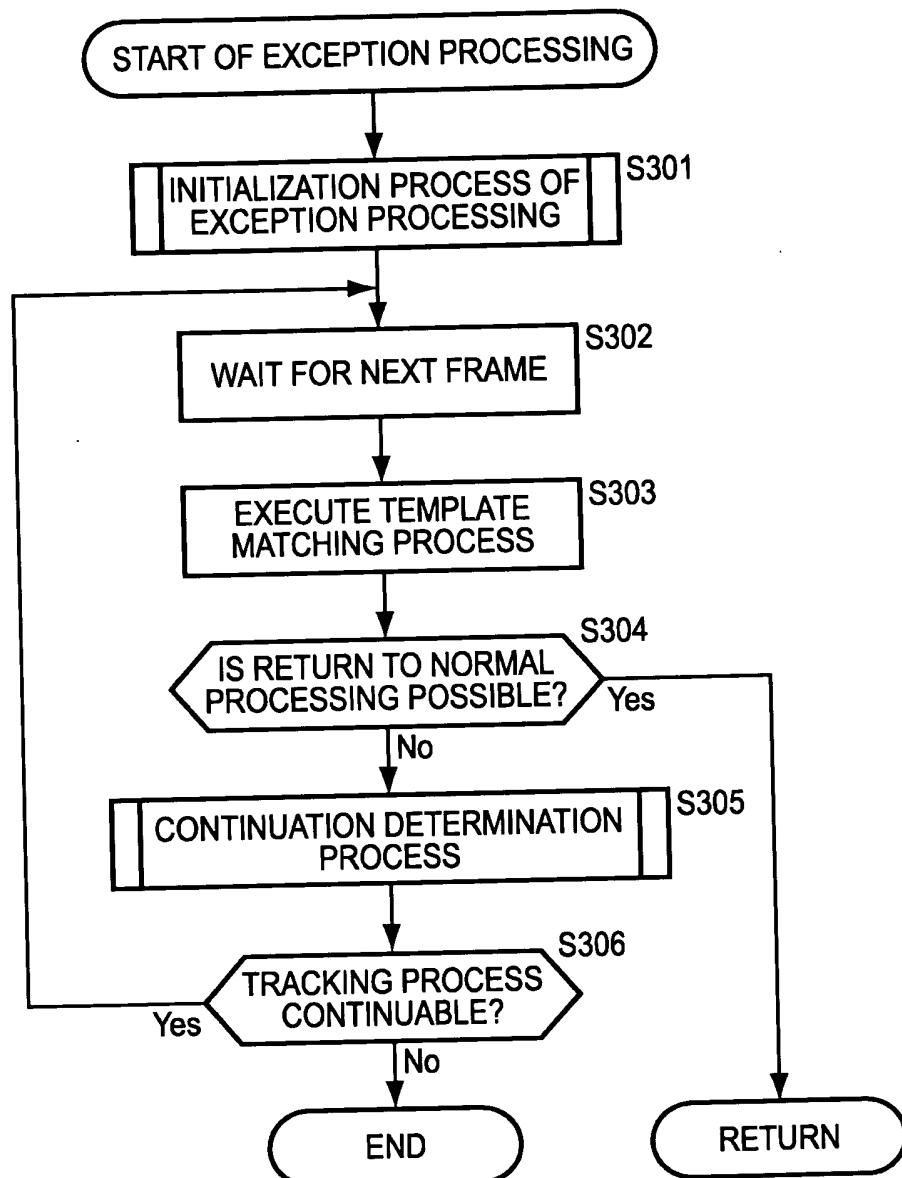


FIG. 34

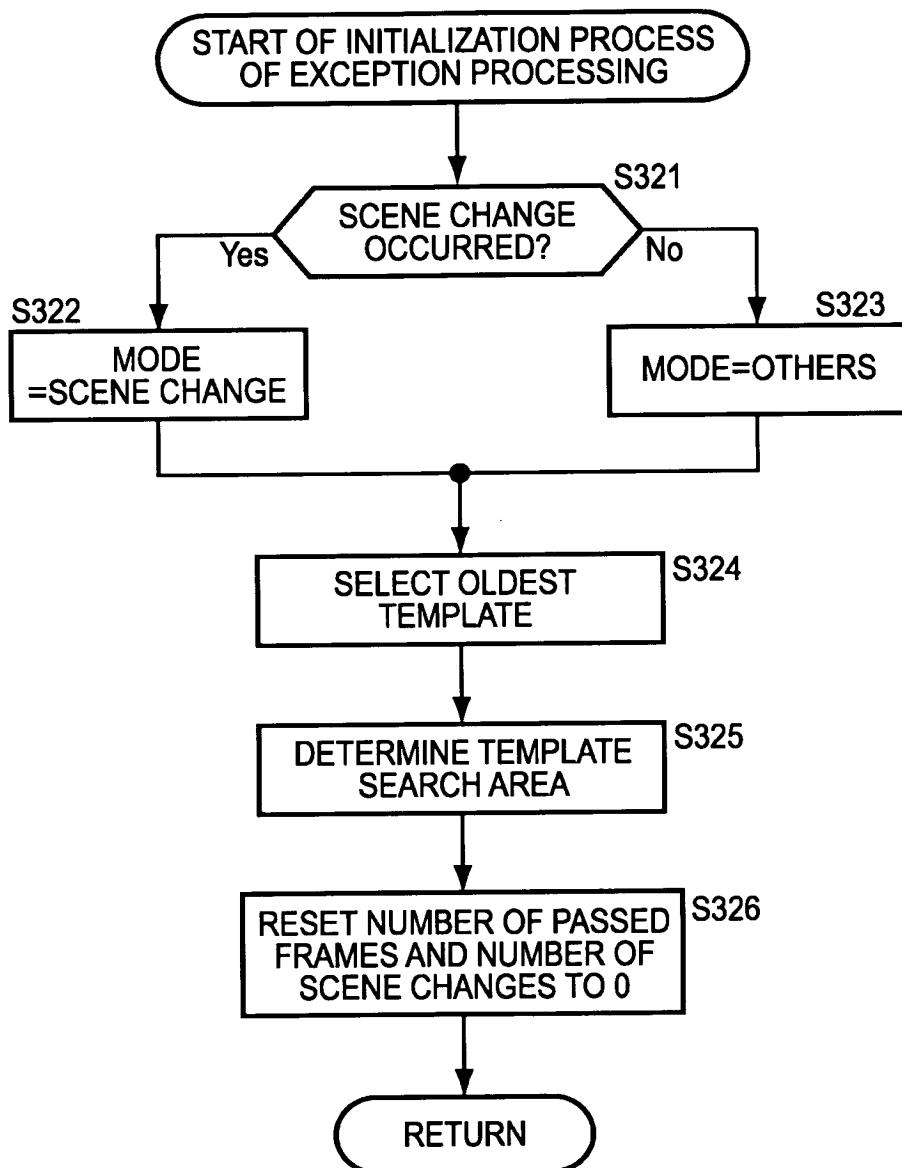


FIG. 35

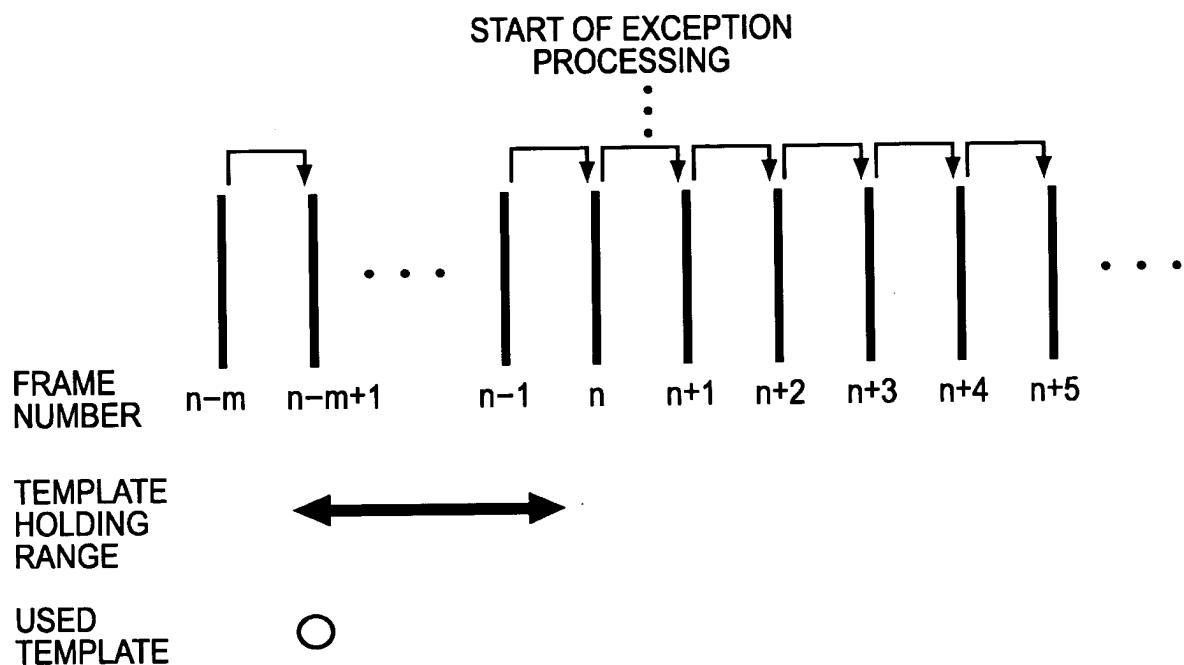


FIG. 36

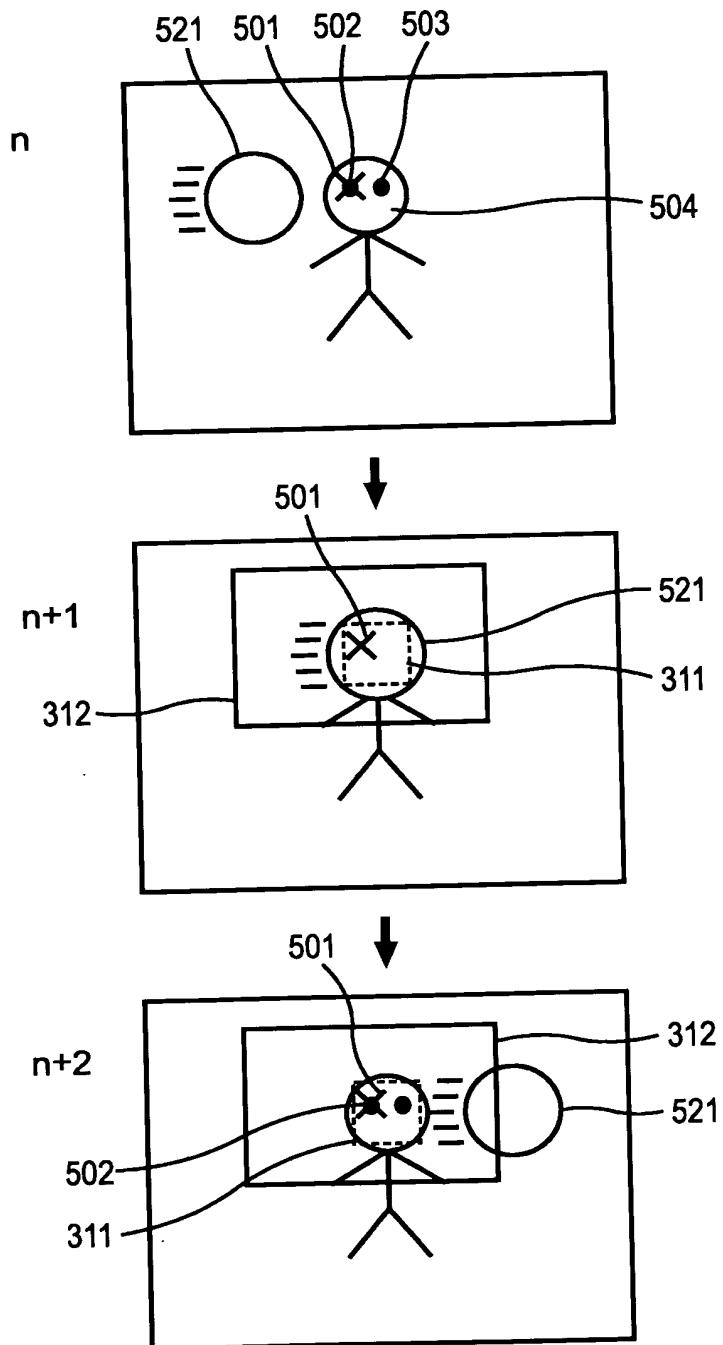


FIG. 37

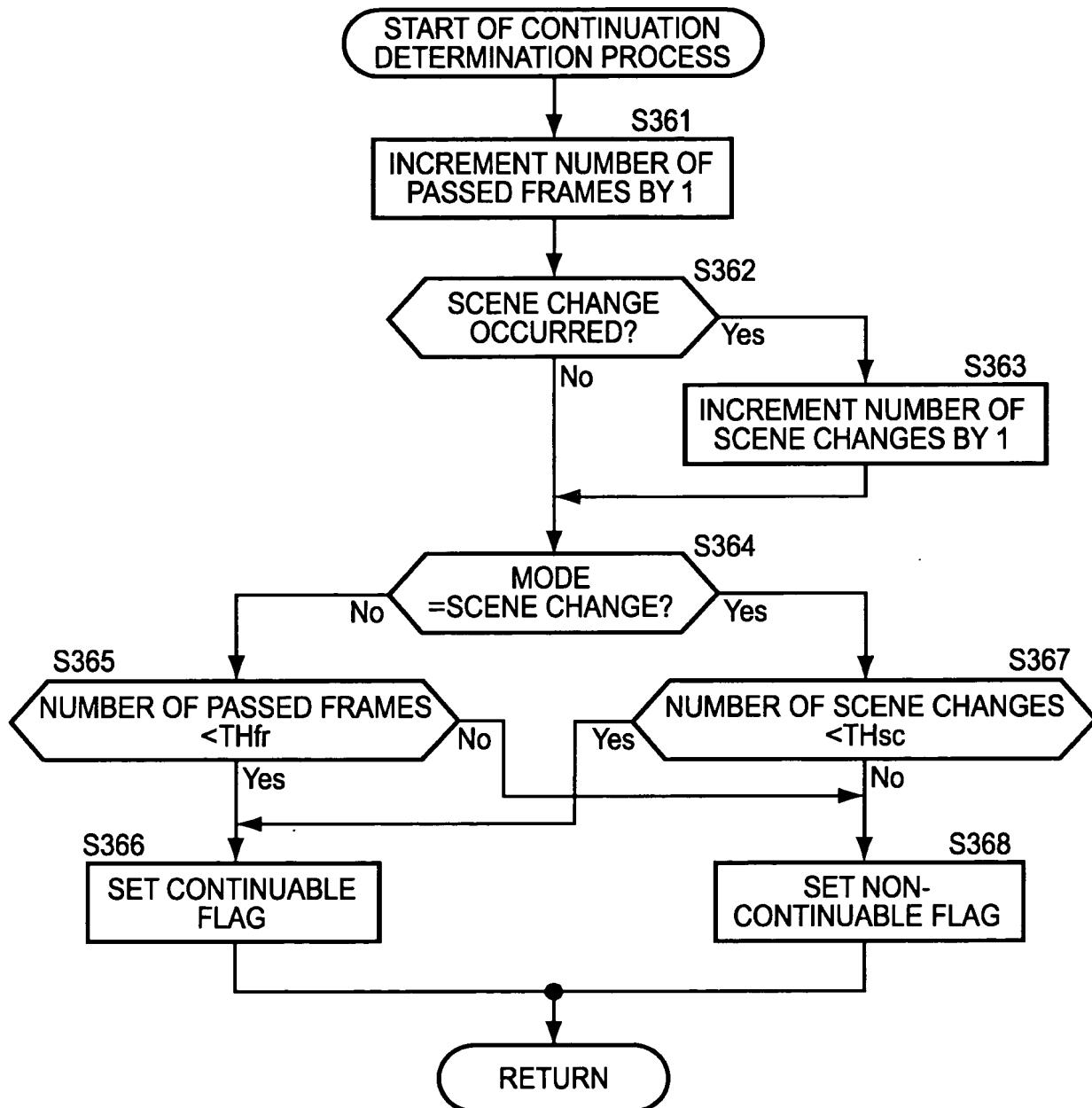


FIG. 38

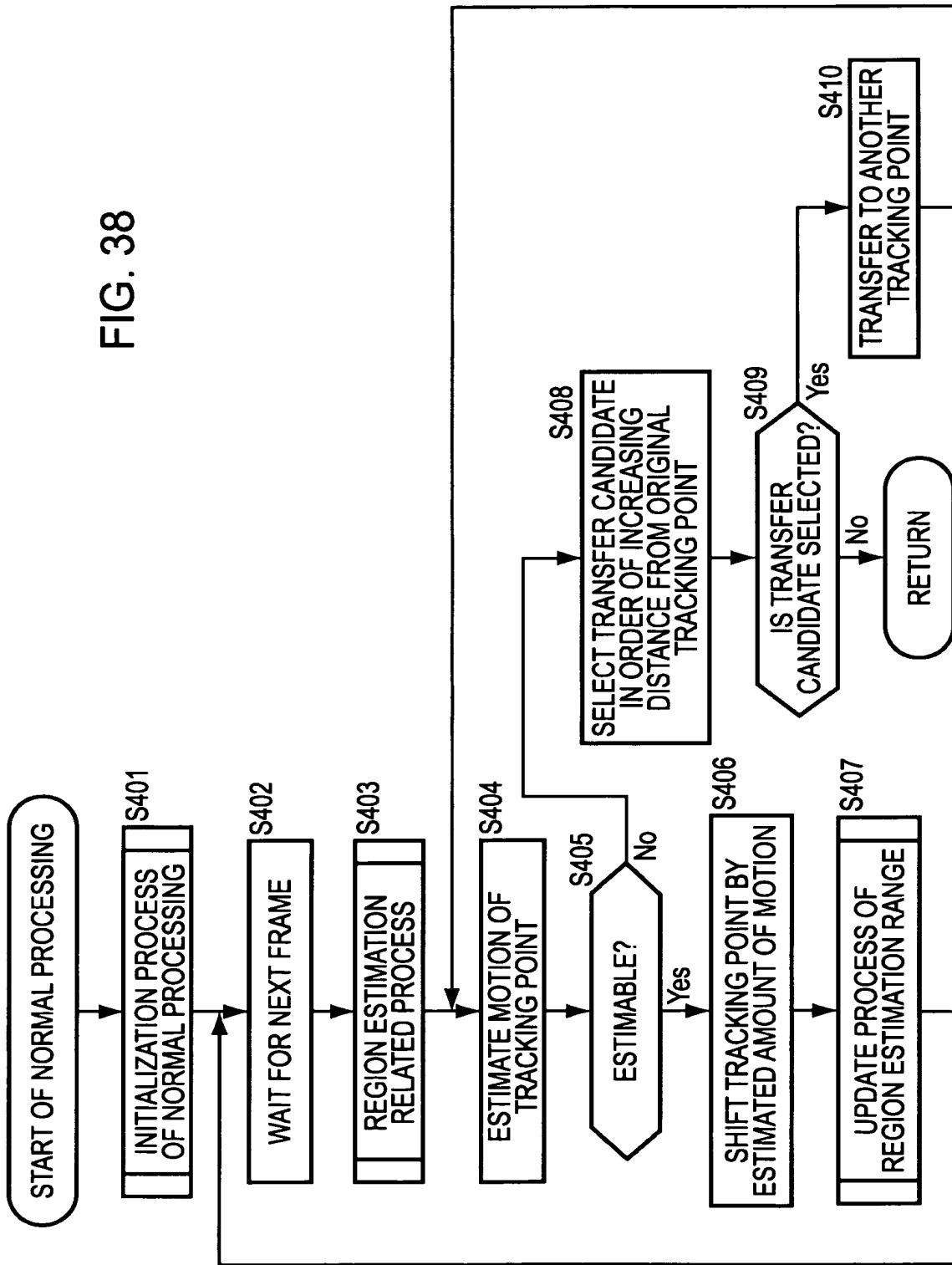
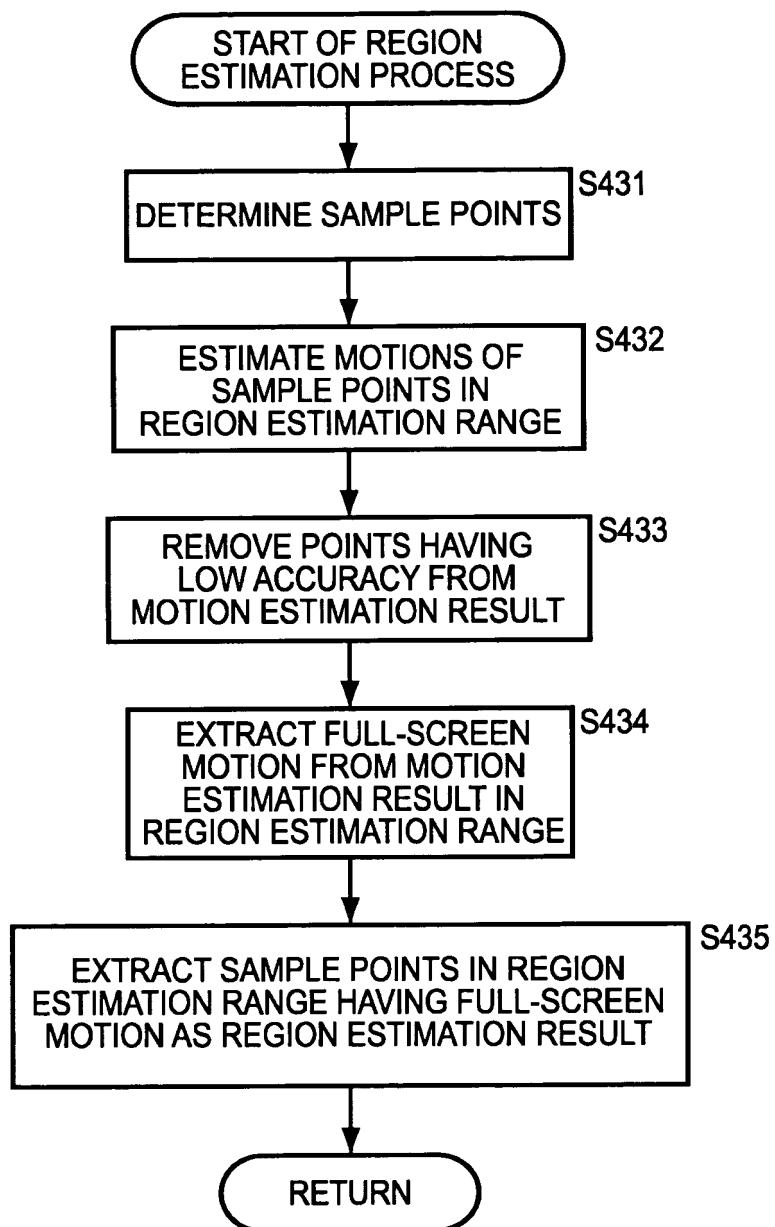


FIG. 39



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FIG. 40

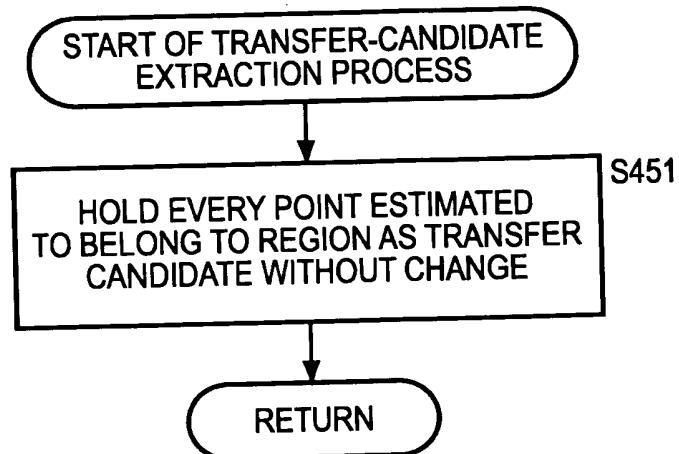


FIG. 41

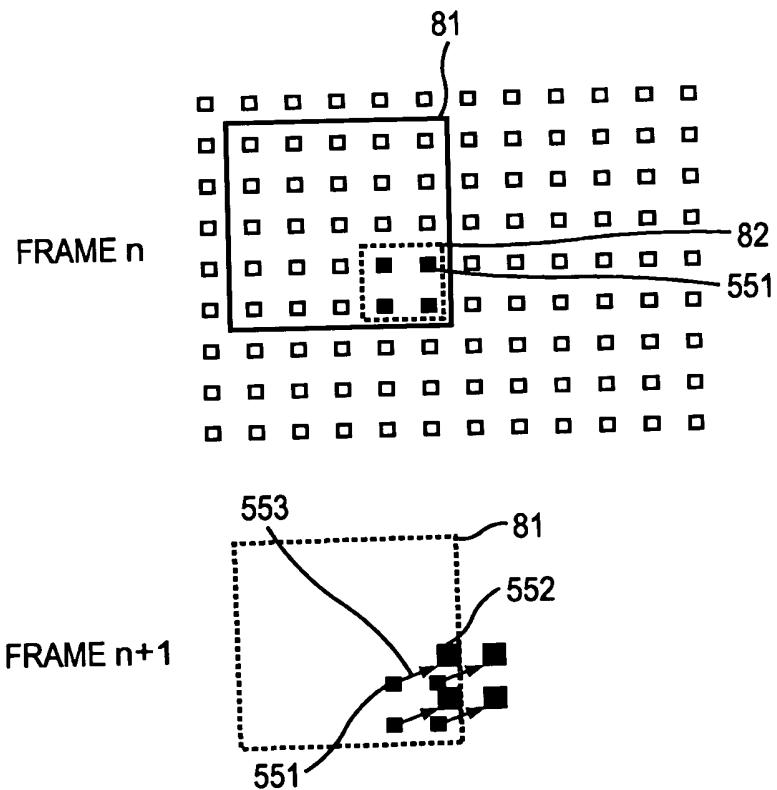


FIG. 42

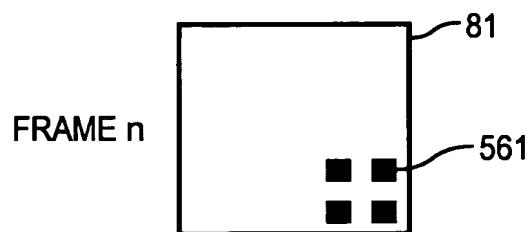


FIG. 43

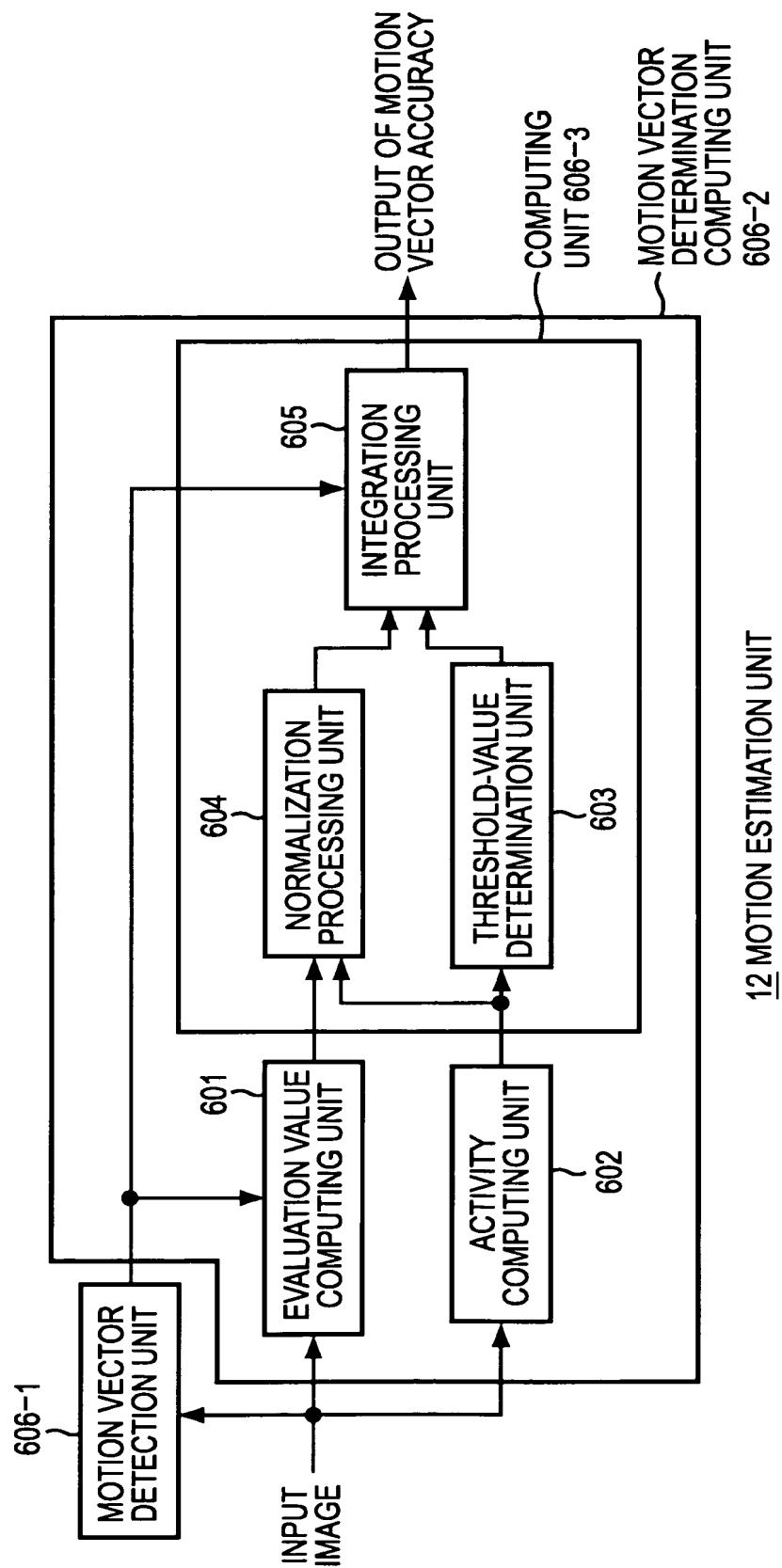
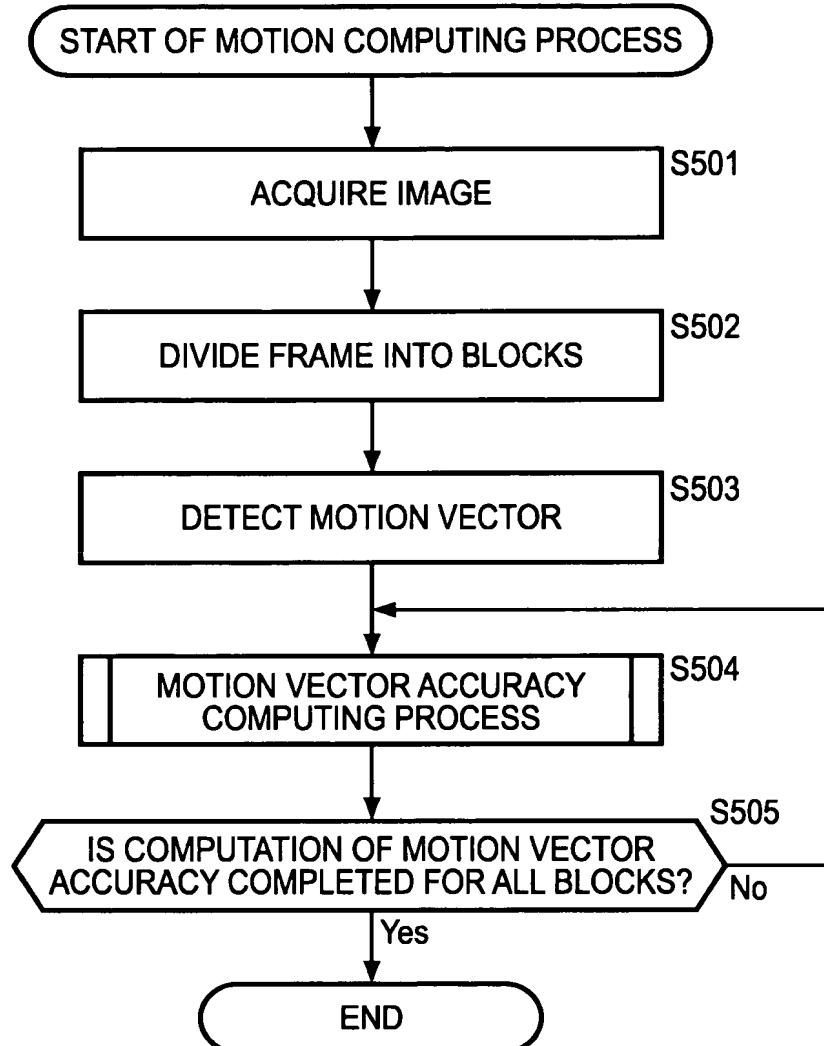


FIG. 44



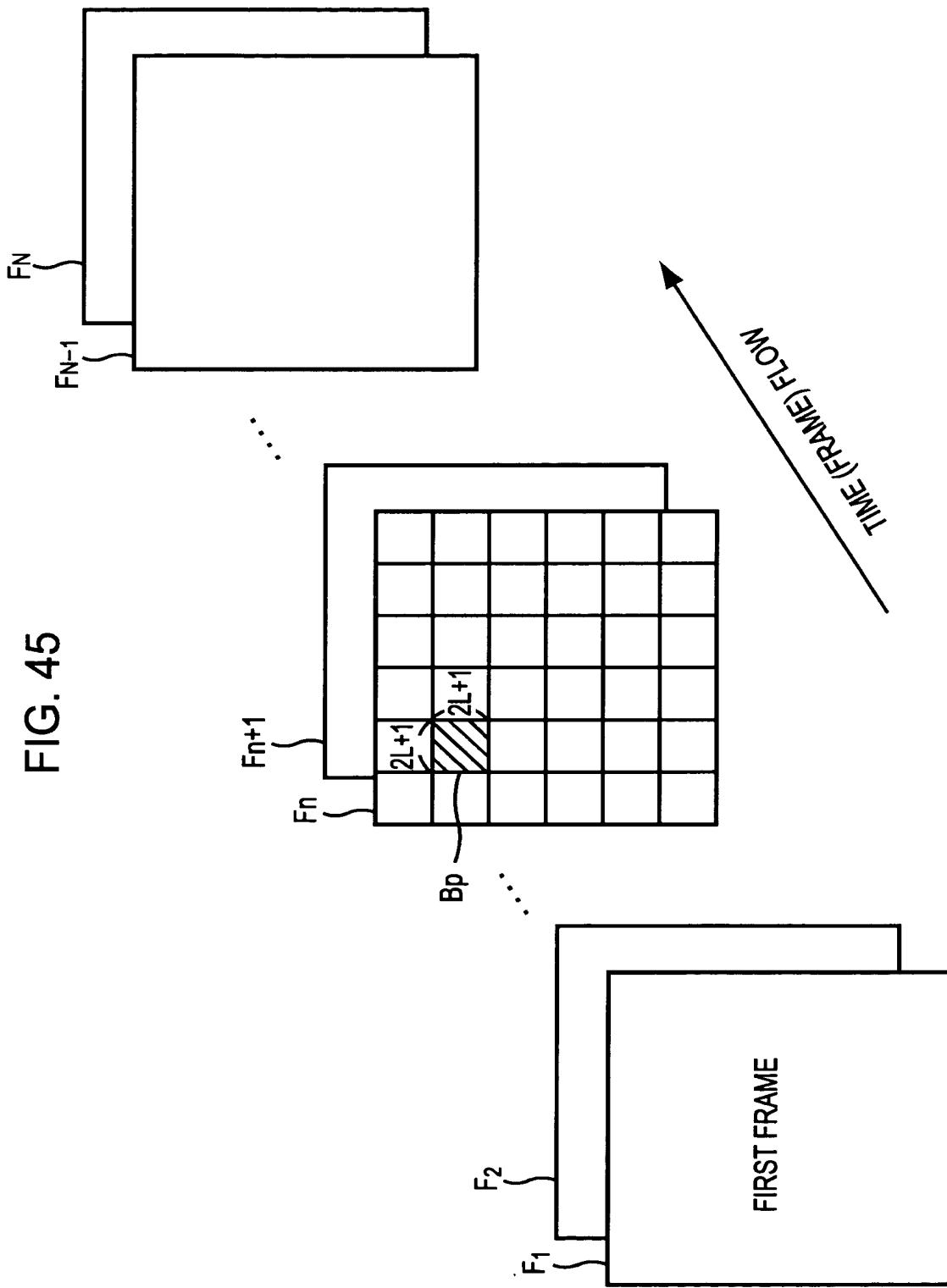


FIG. 46

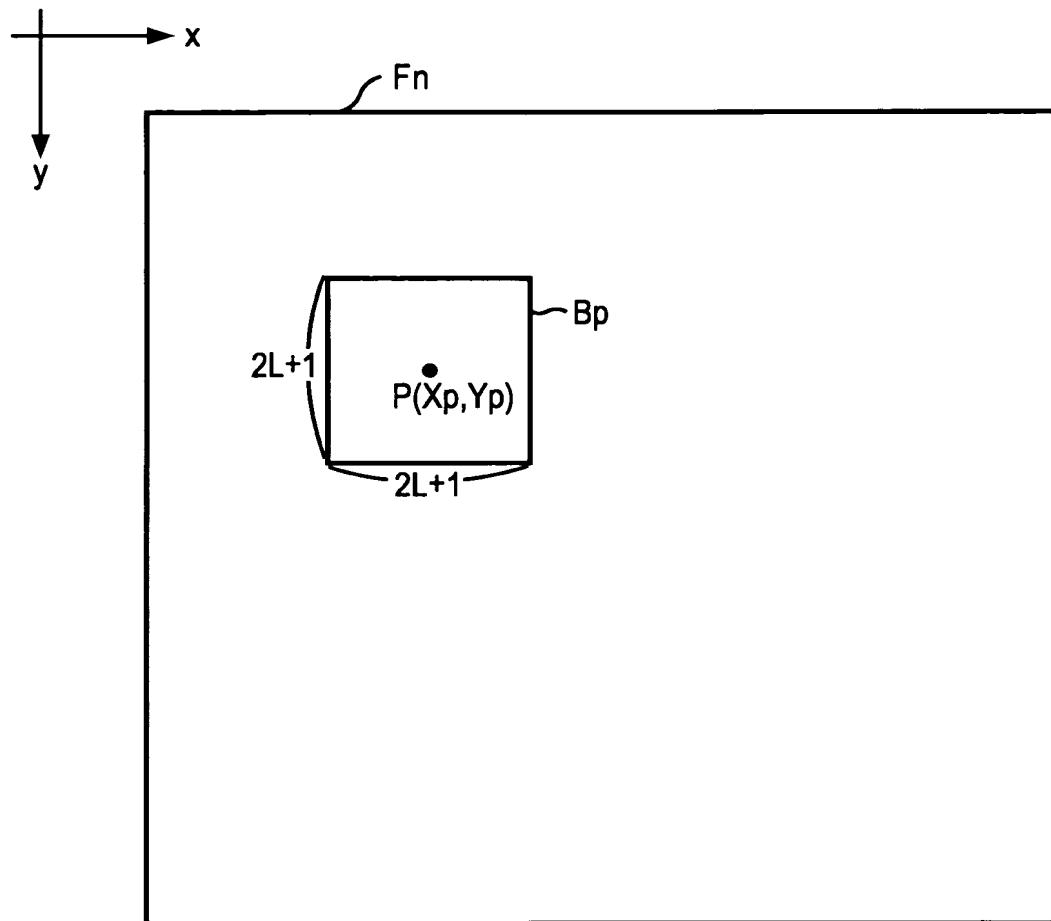
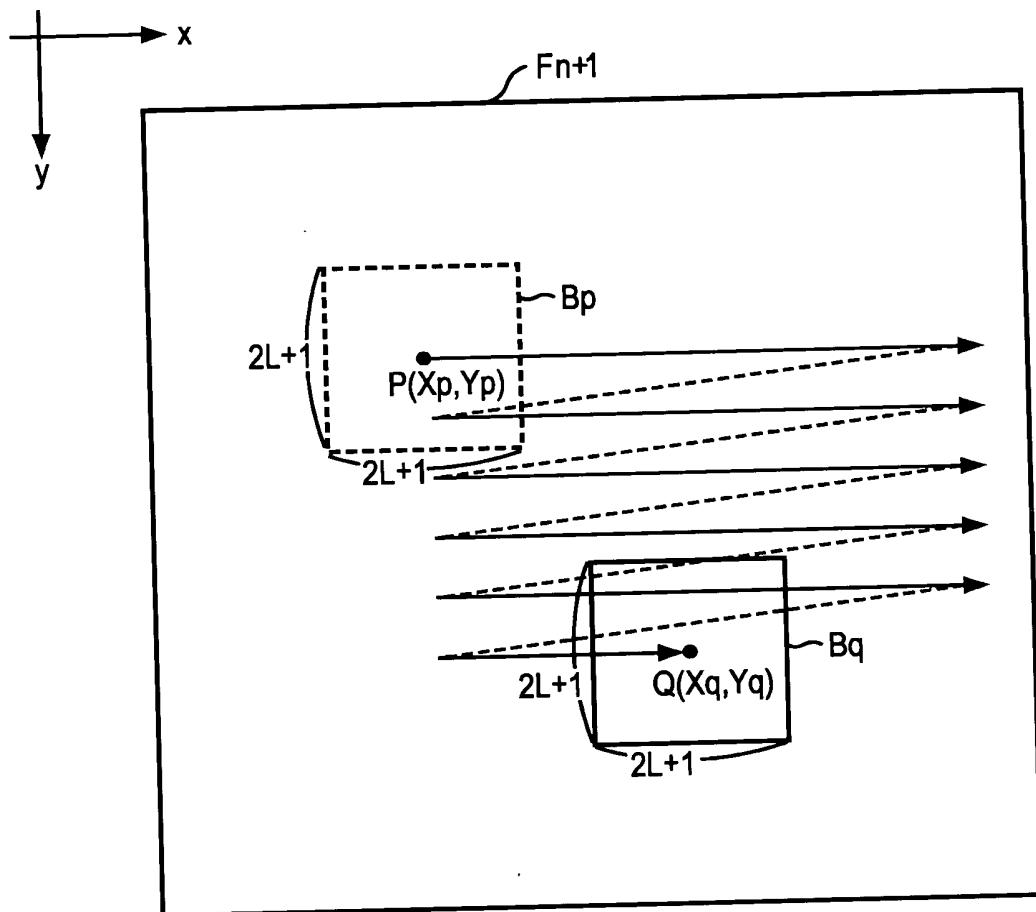


FIG. 47



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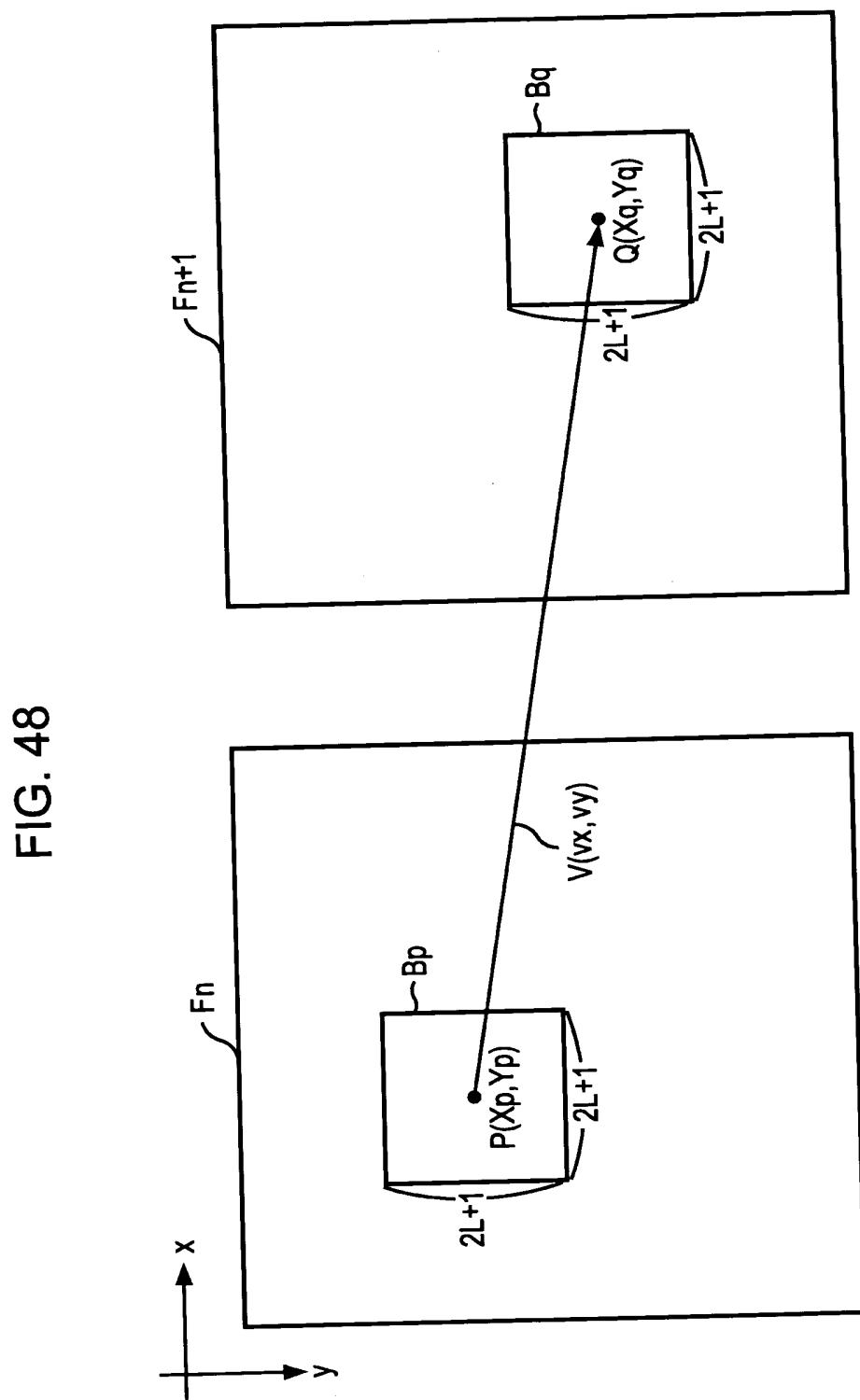


FIG. 49

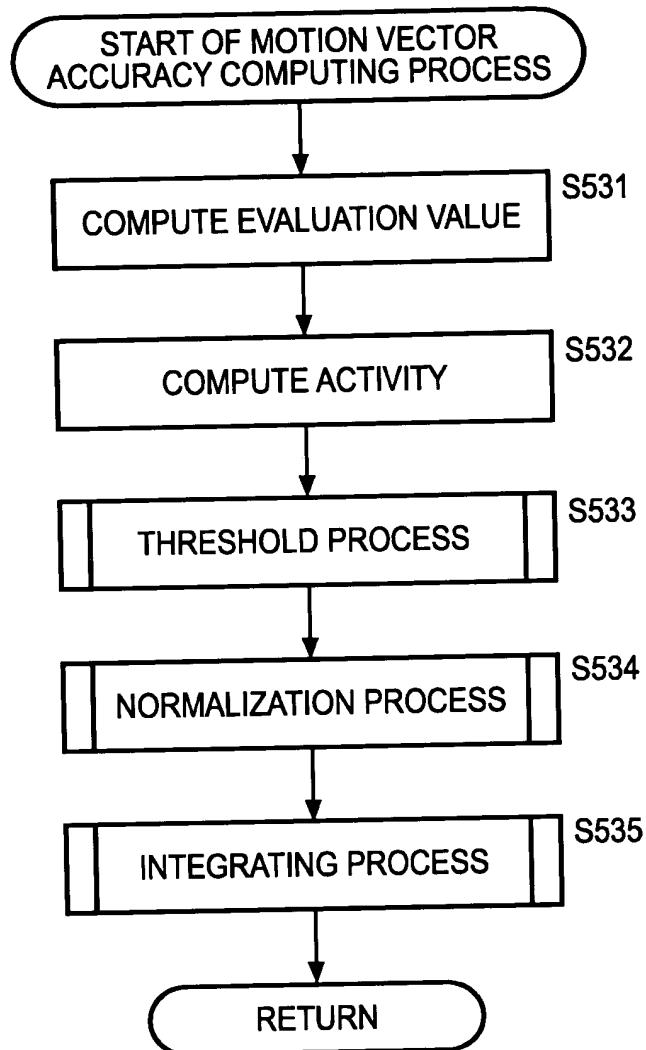


FIG. 50

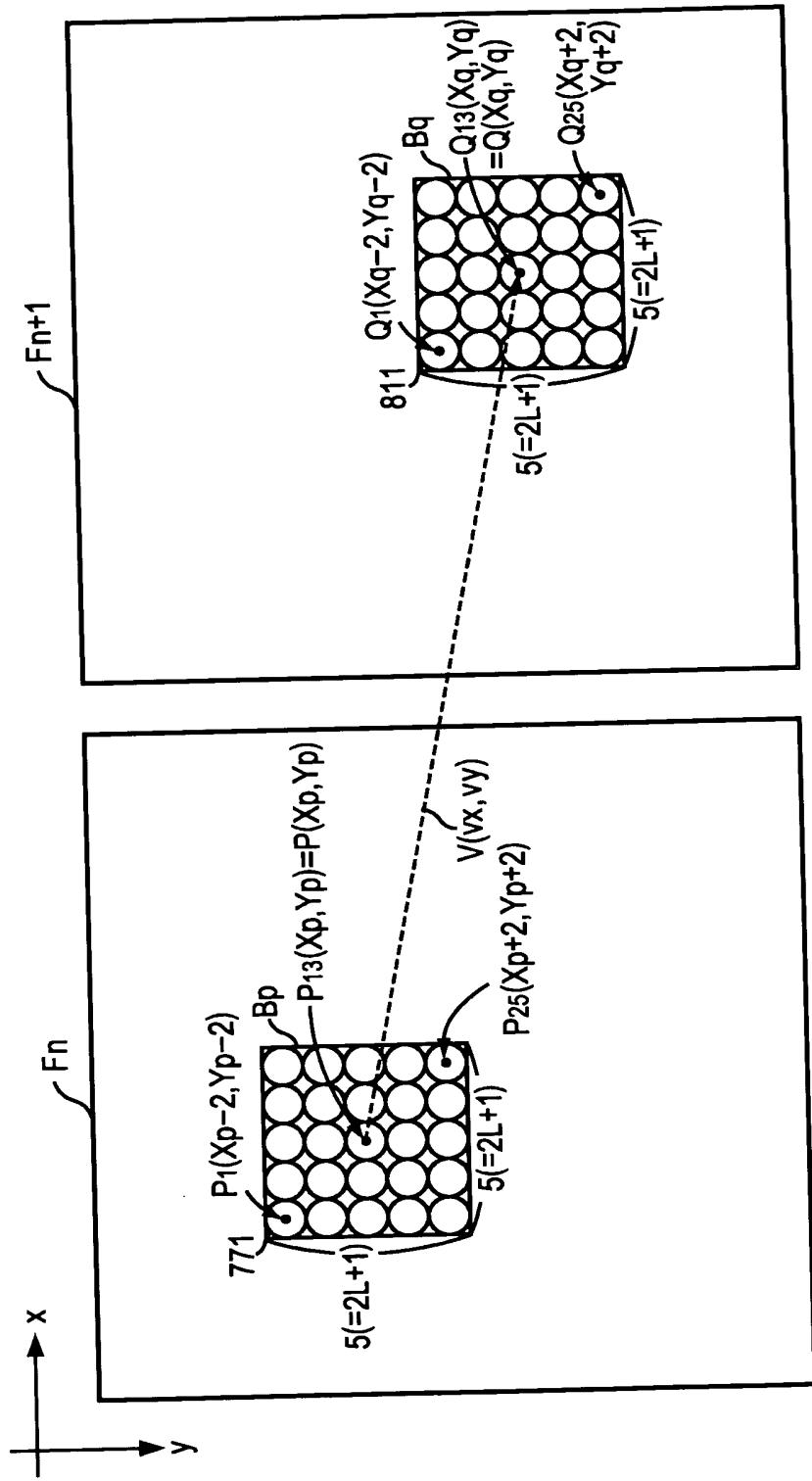


FIG. 51

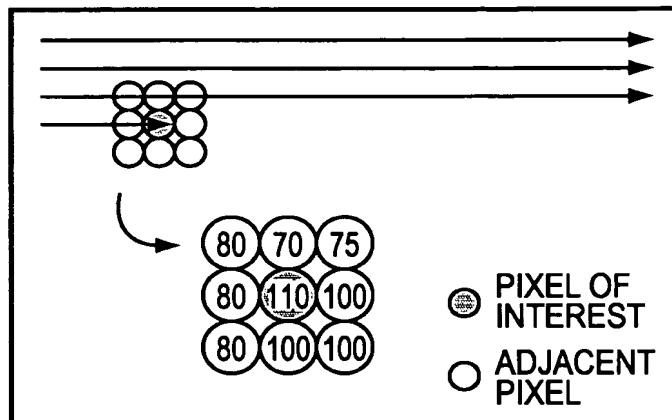


FIG. 52

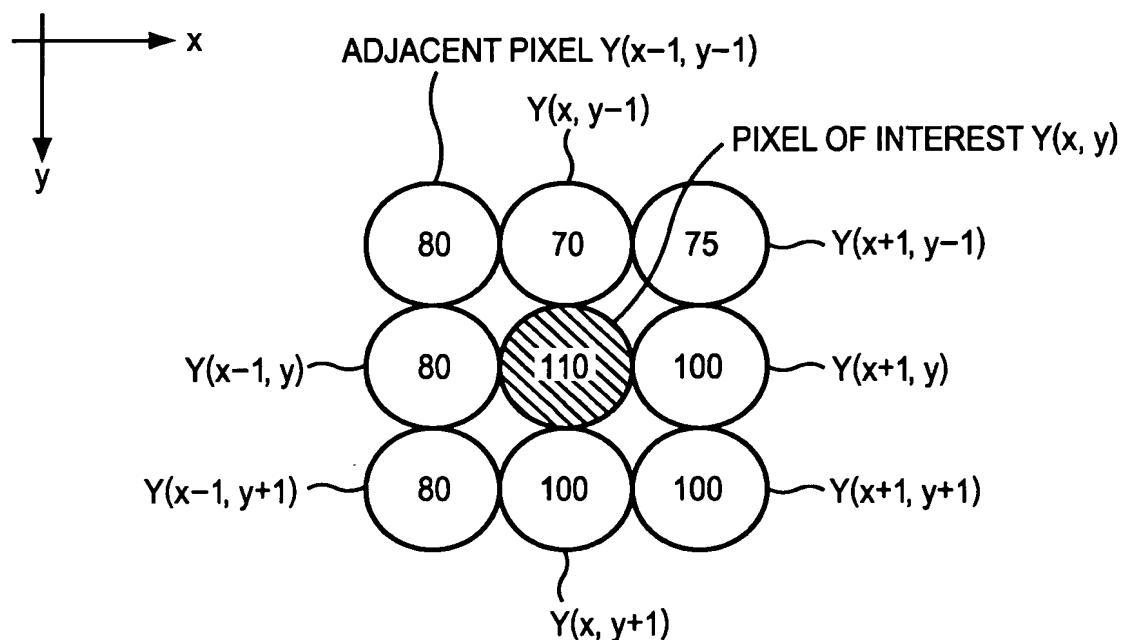


FIG. 53A

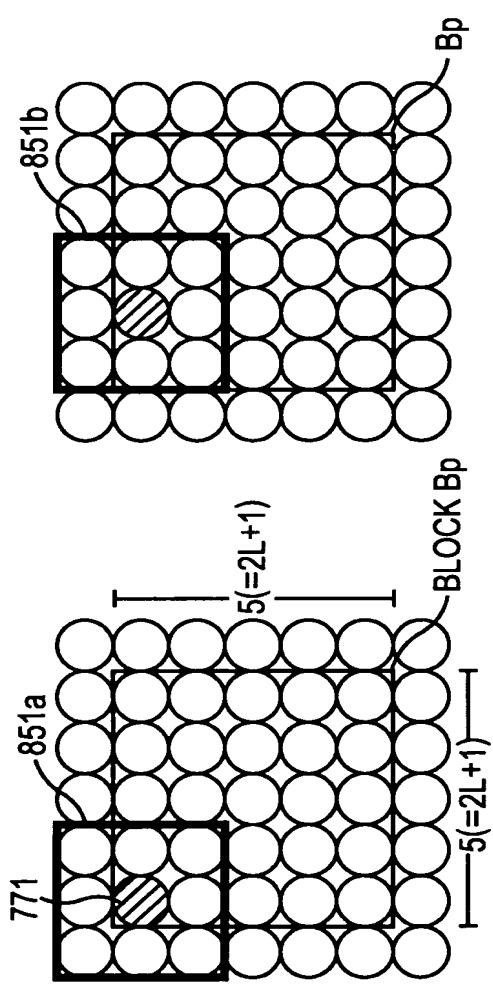


FIG. 53B

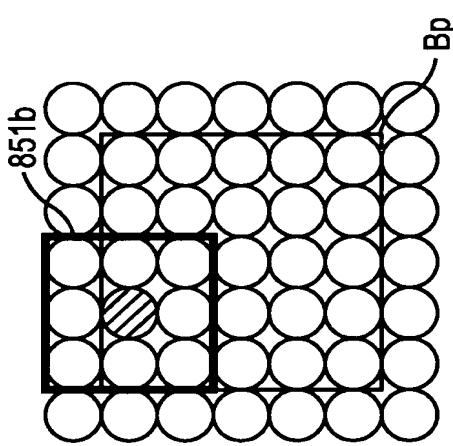


FIG. 53C

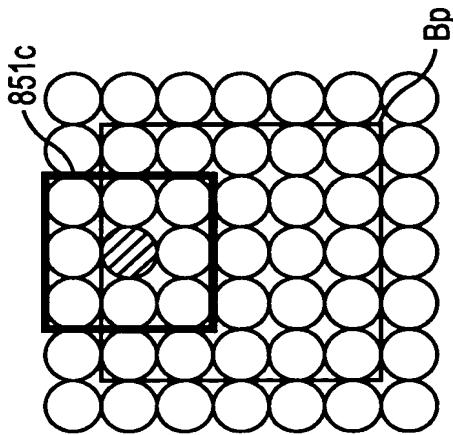


FIG. 53D

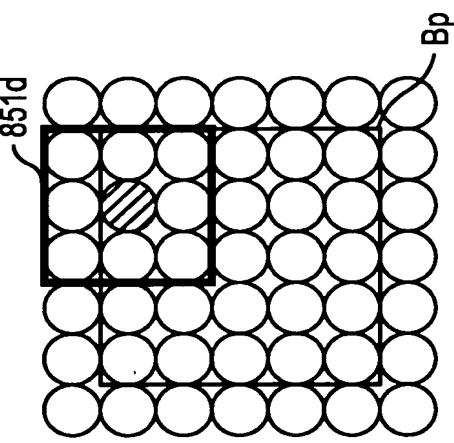


FIG. 53E

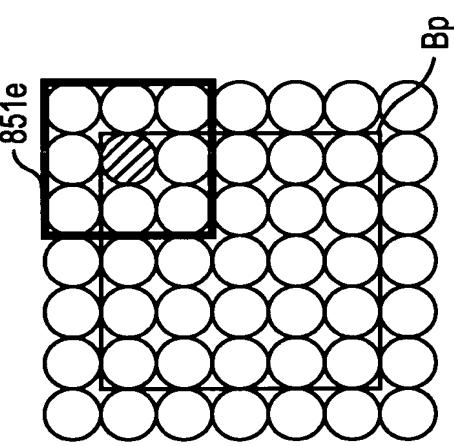
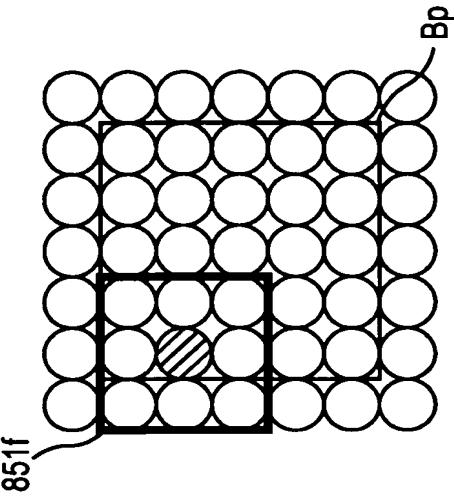


FIG. 53F



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FIG. 54

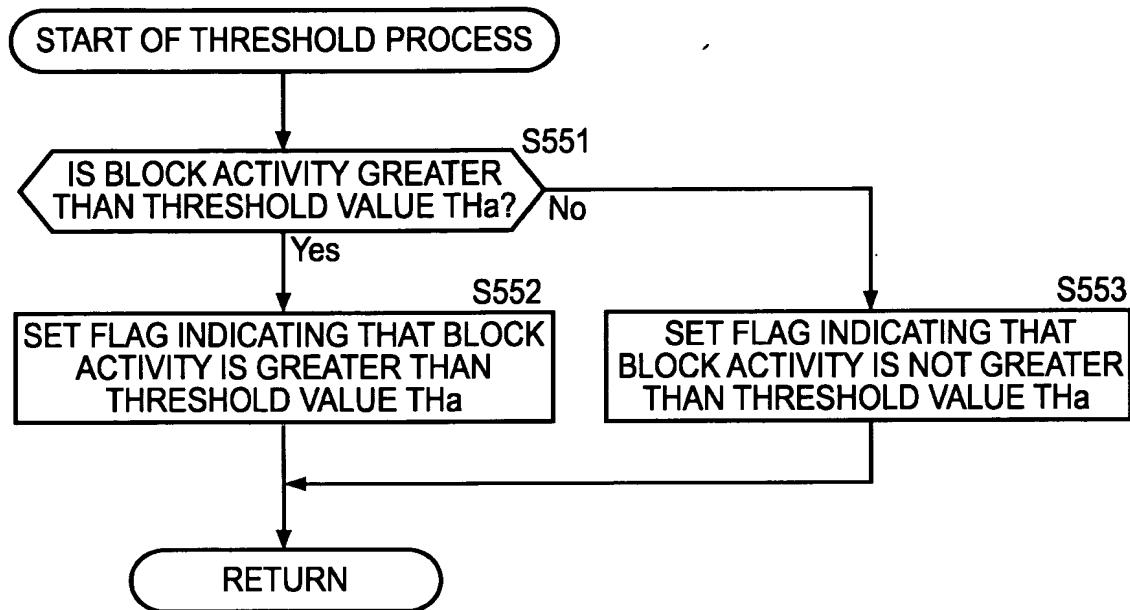
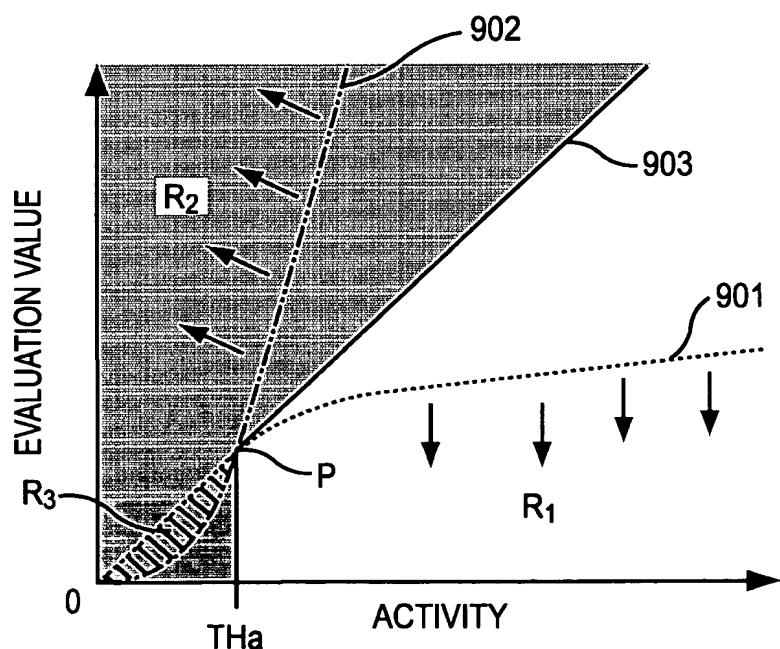


FIG. 55



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FIG. 56

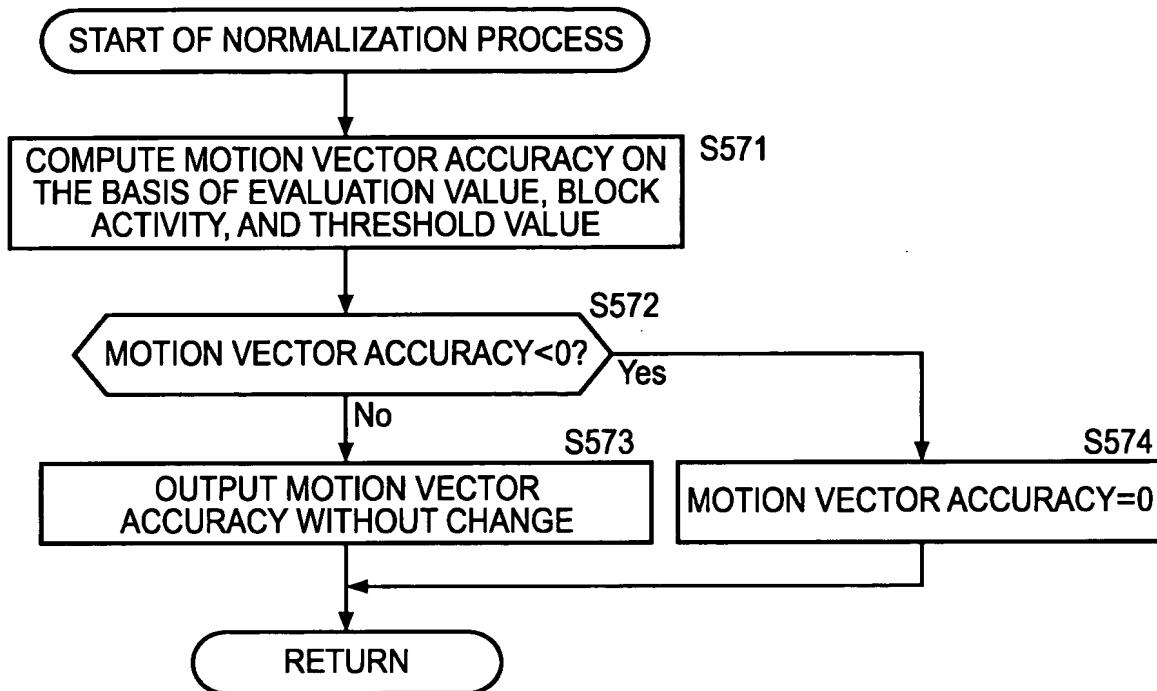
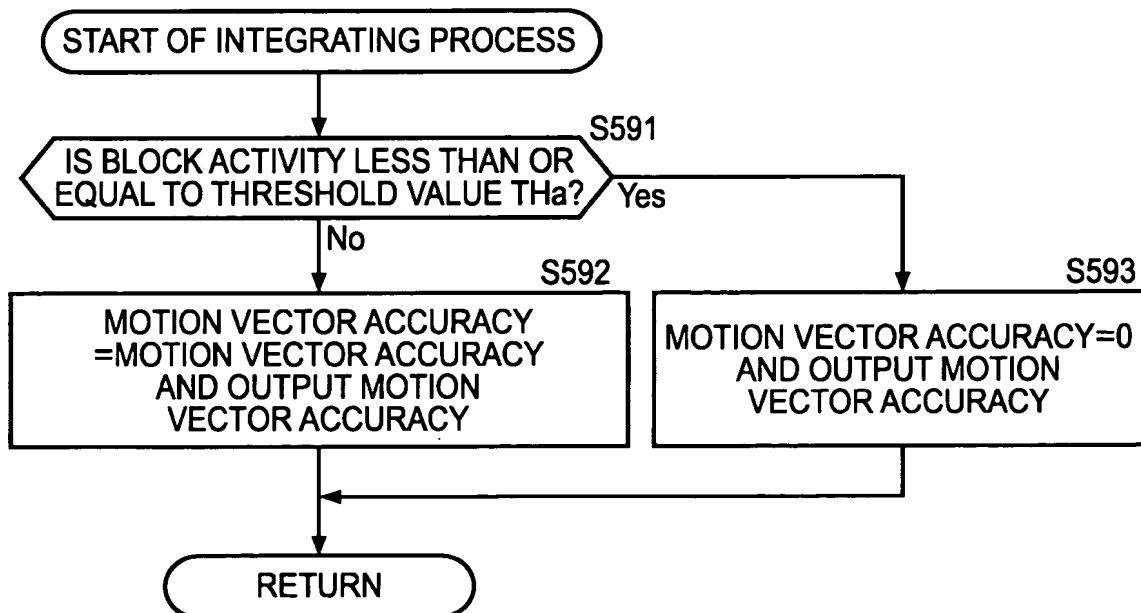


FIG. 57



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FIG. 58

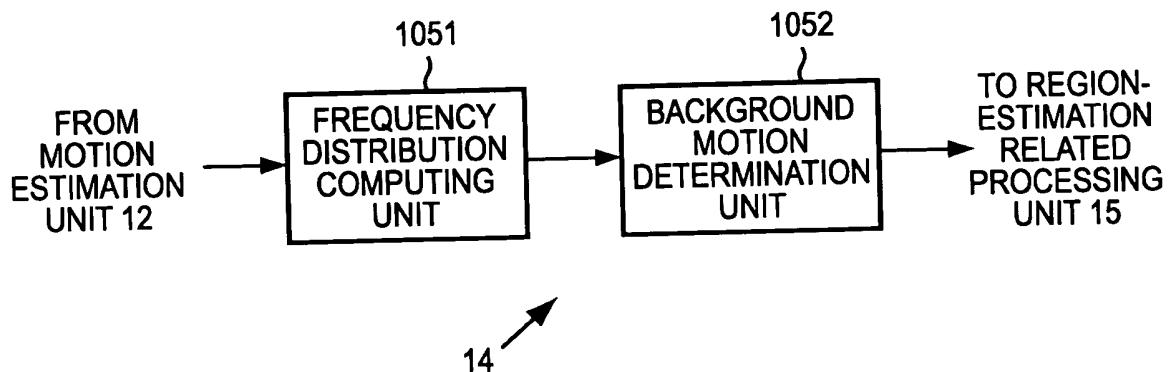


FIG. 59

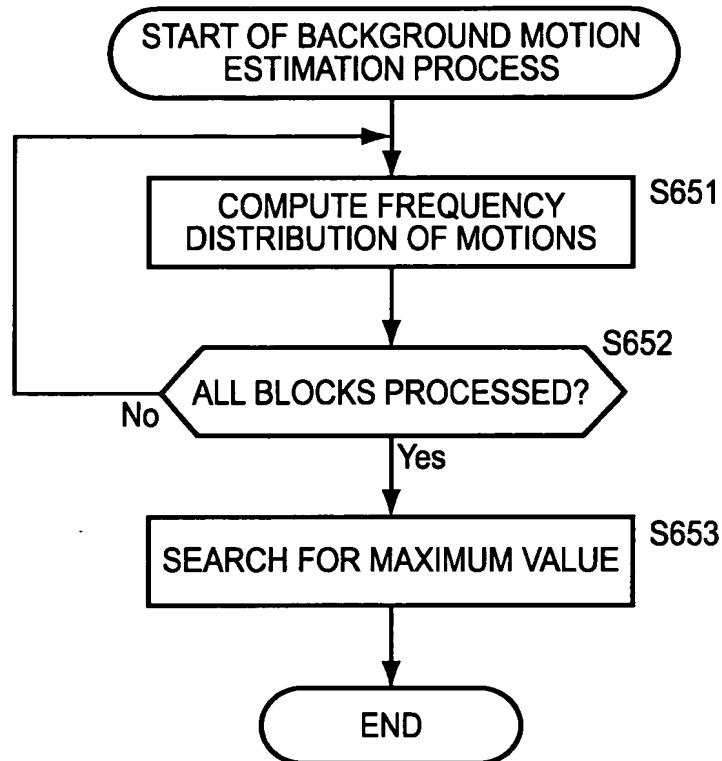


FIG. 60

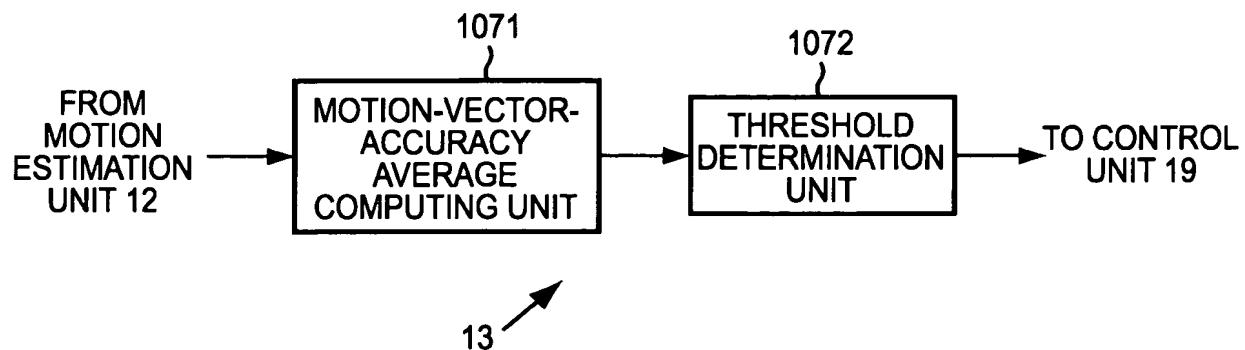


FIG. 61

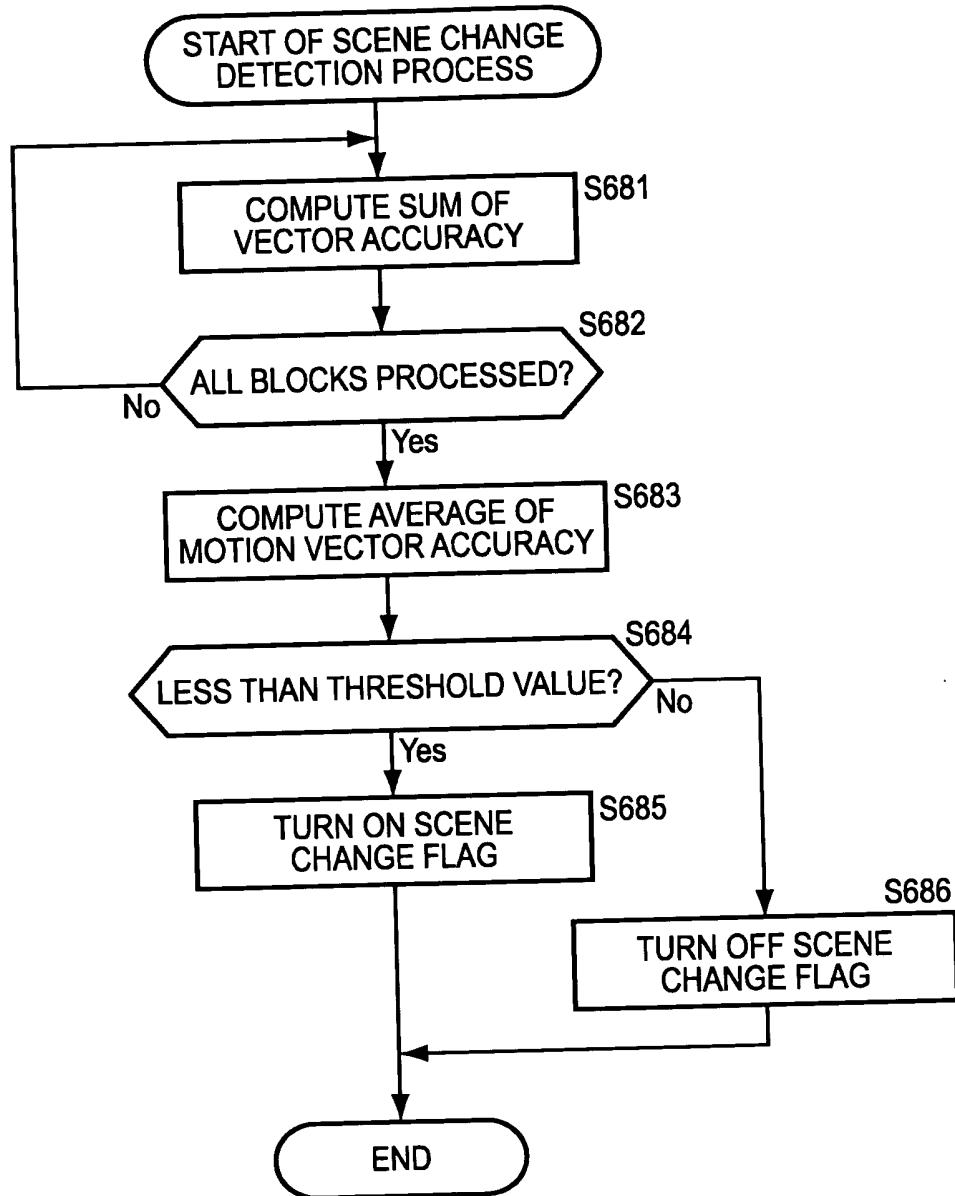


FIG. 62

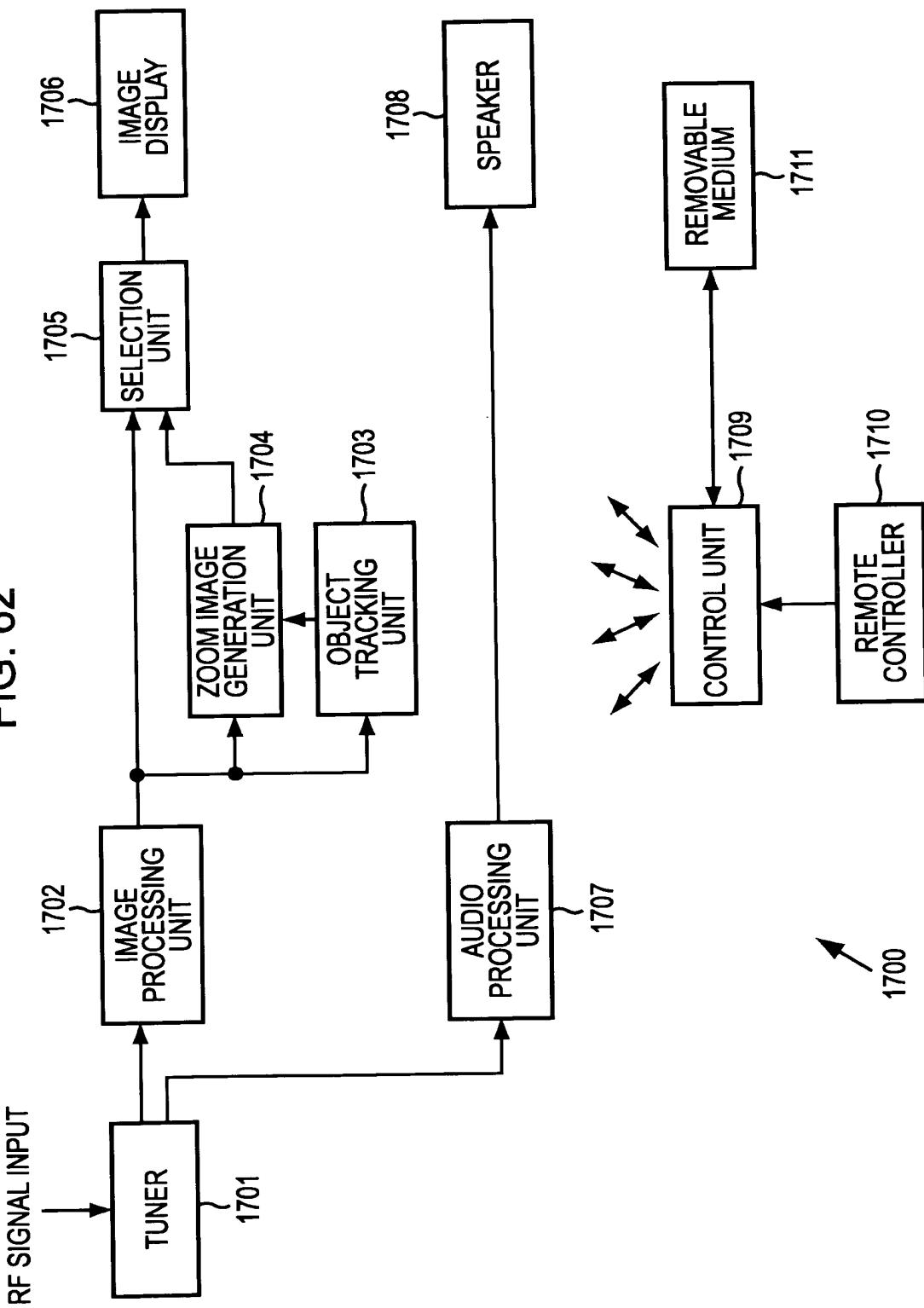


FIG. 63

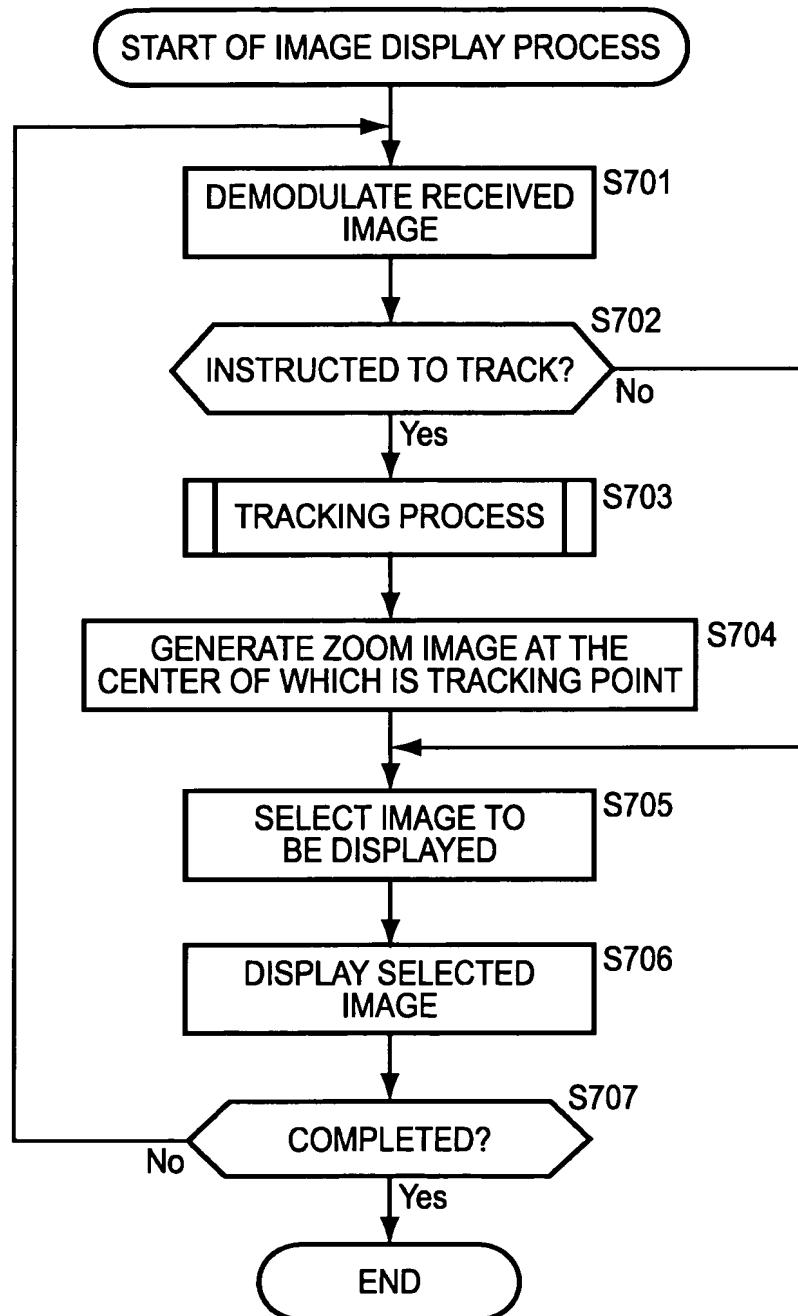


FIG. 64

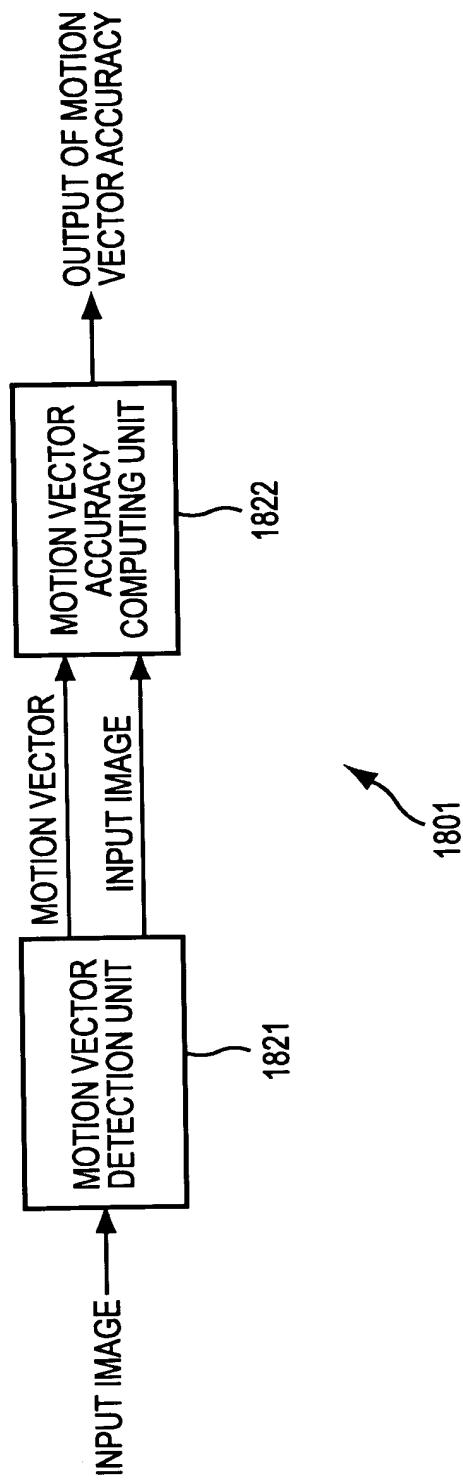


FIG. 65

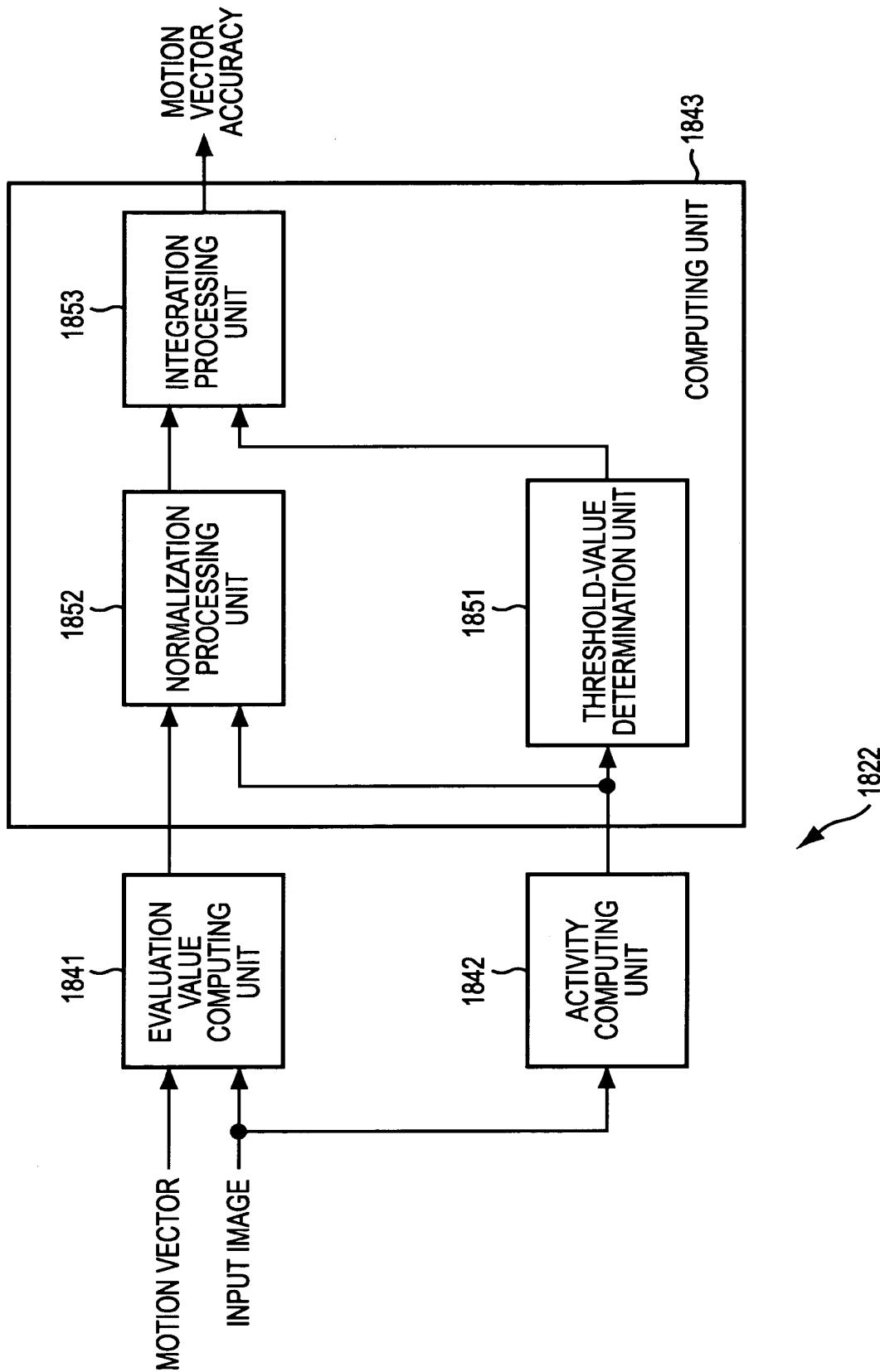


FIG. 66

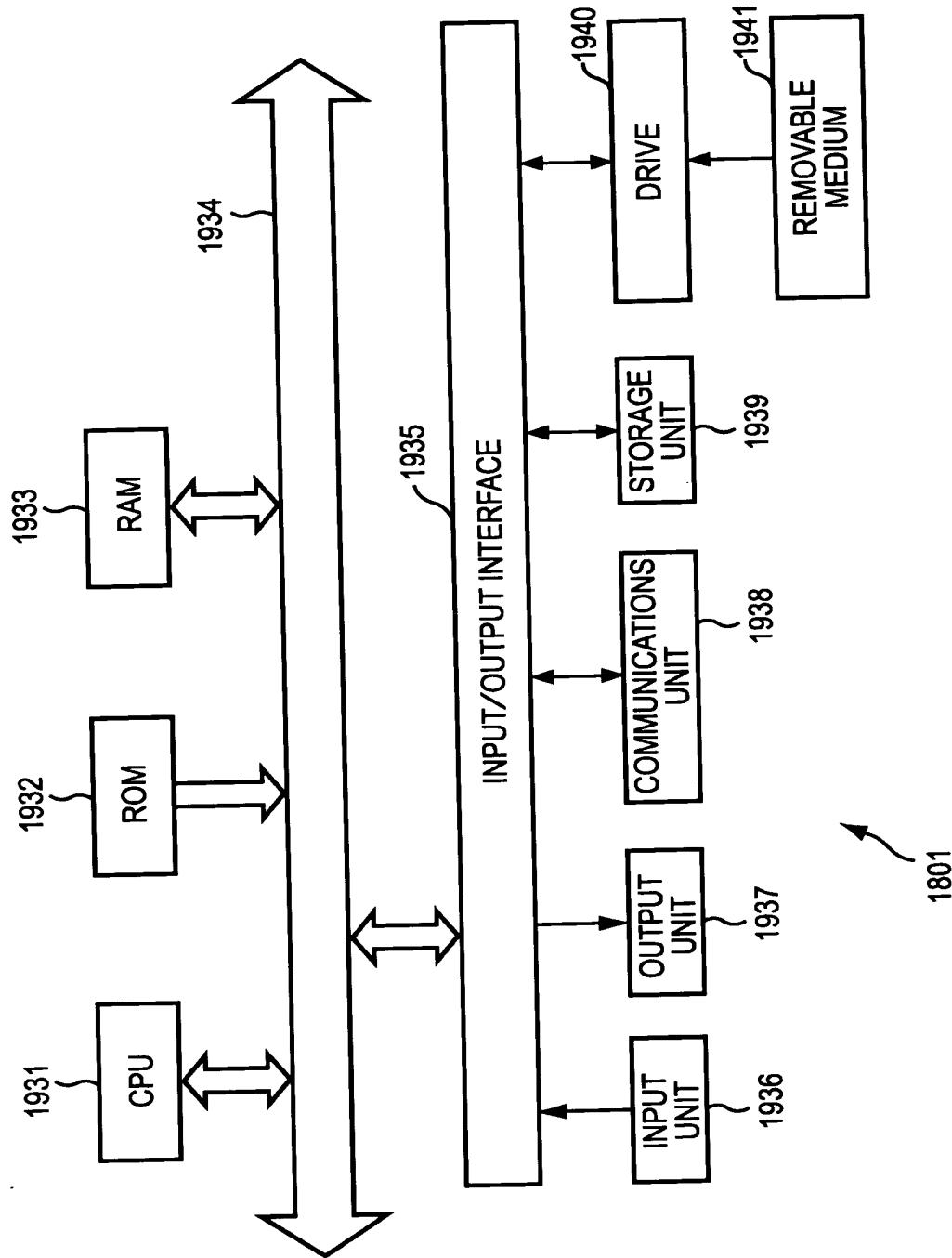


FIG. 67

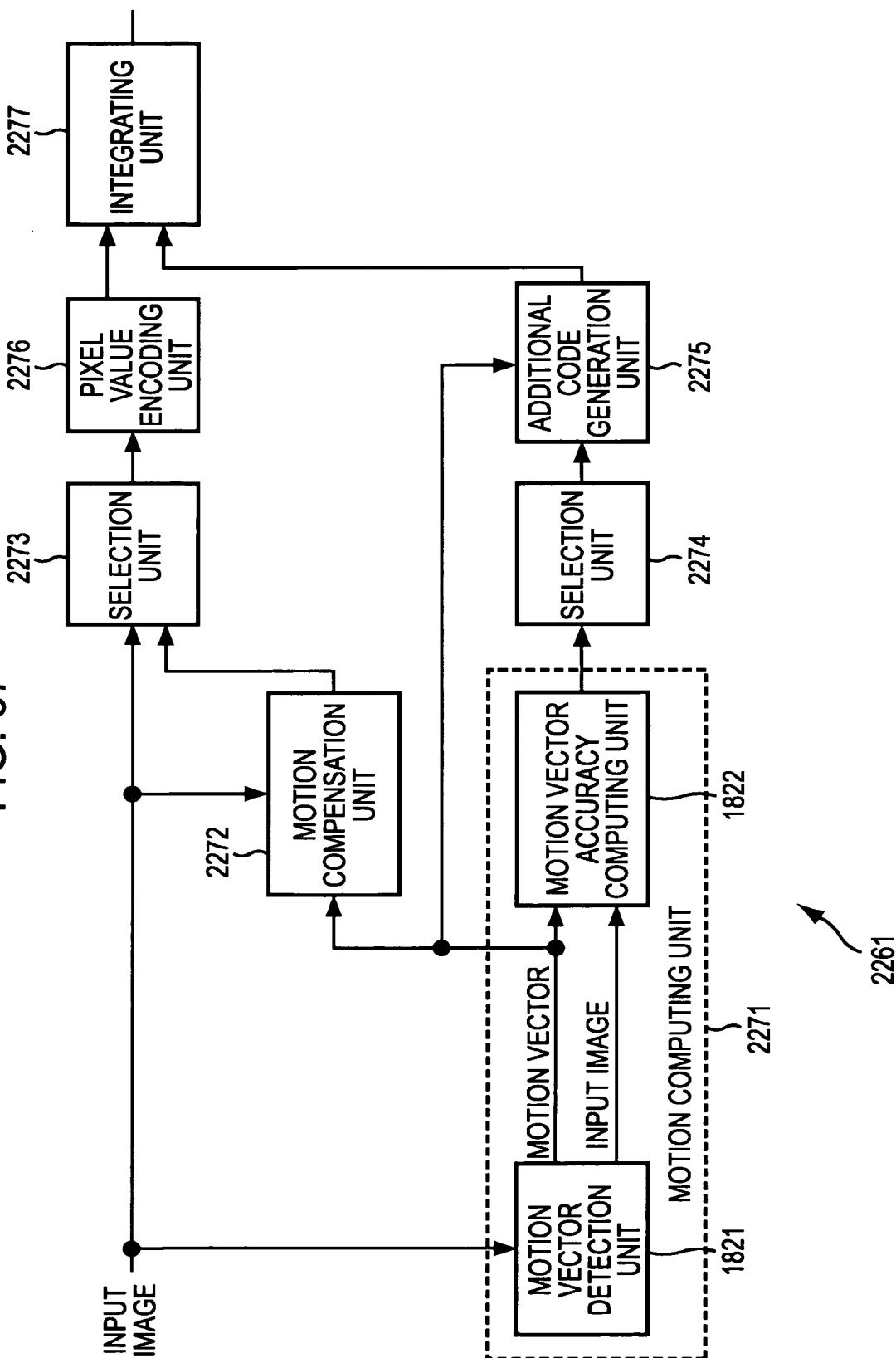
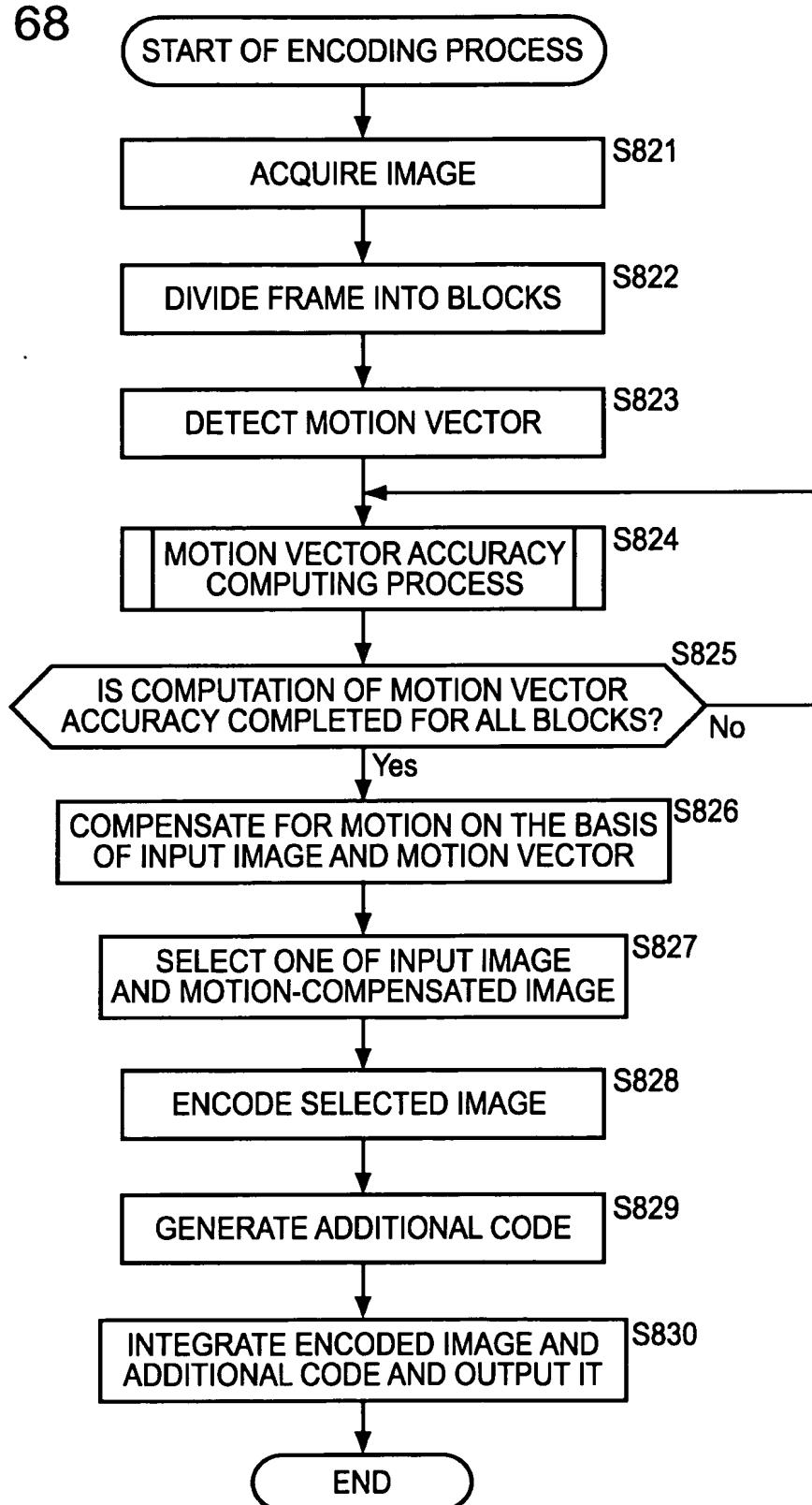
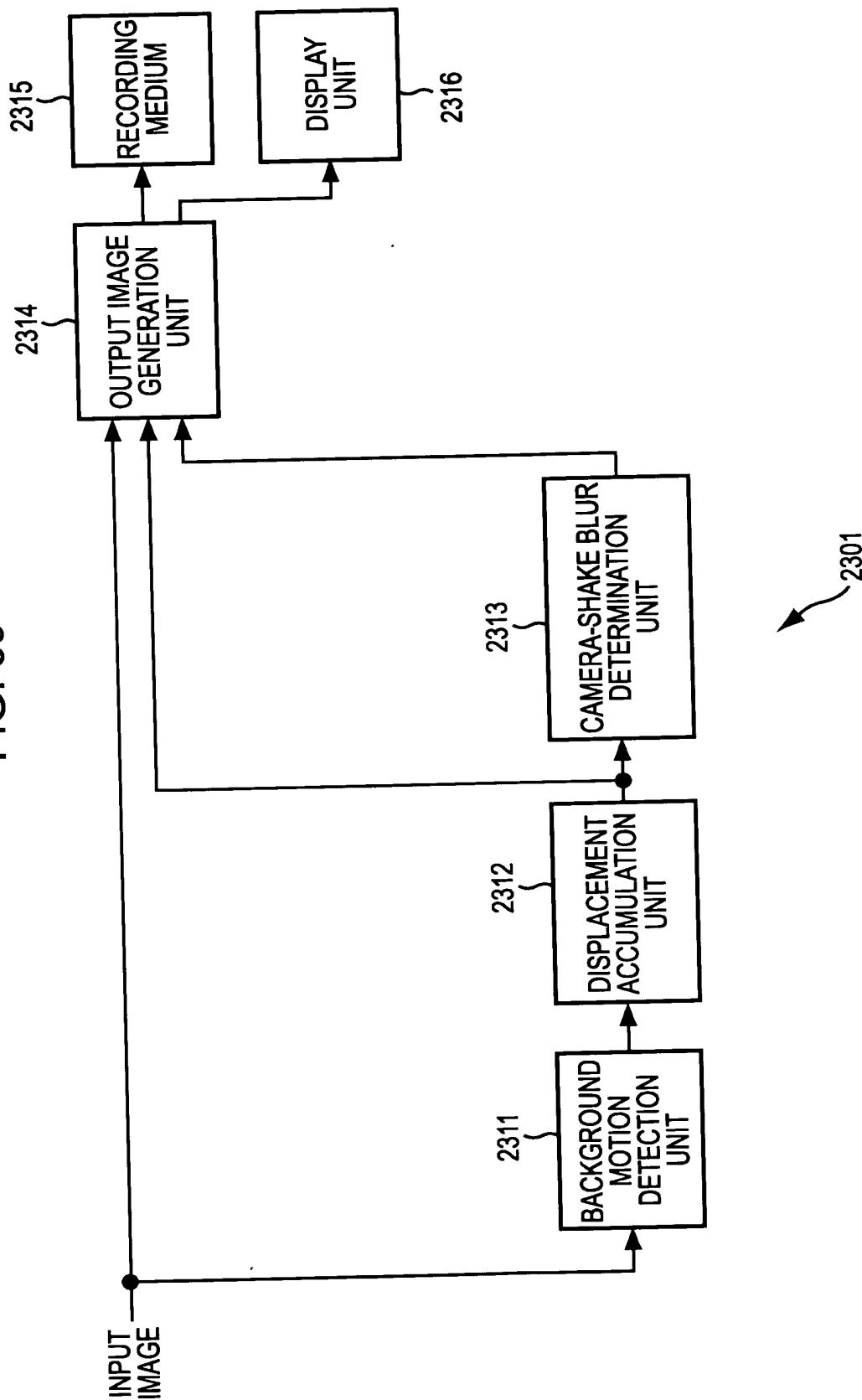


FIG. 68



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FIG. 69



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FIG. 70

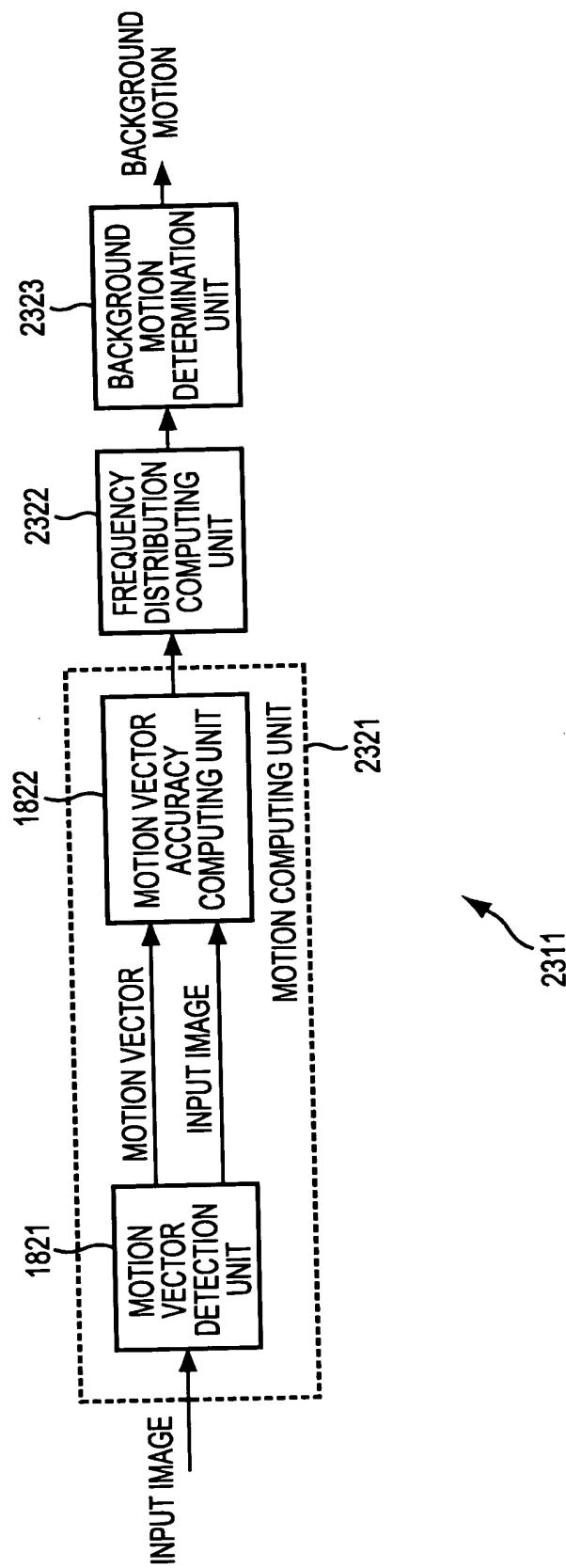


FIG. 71

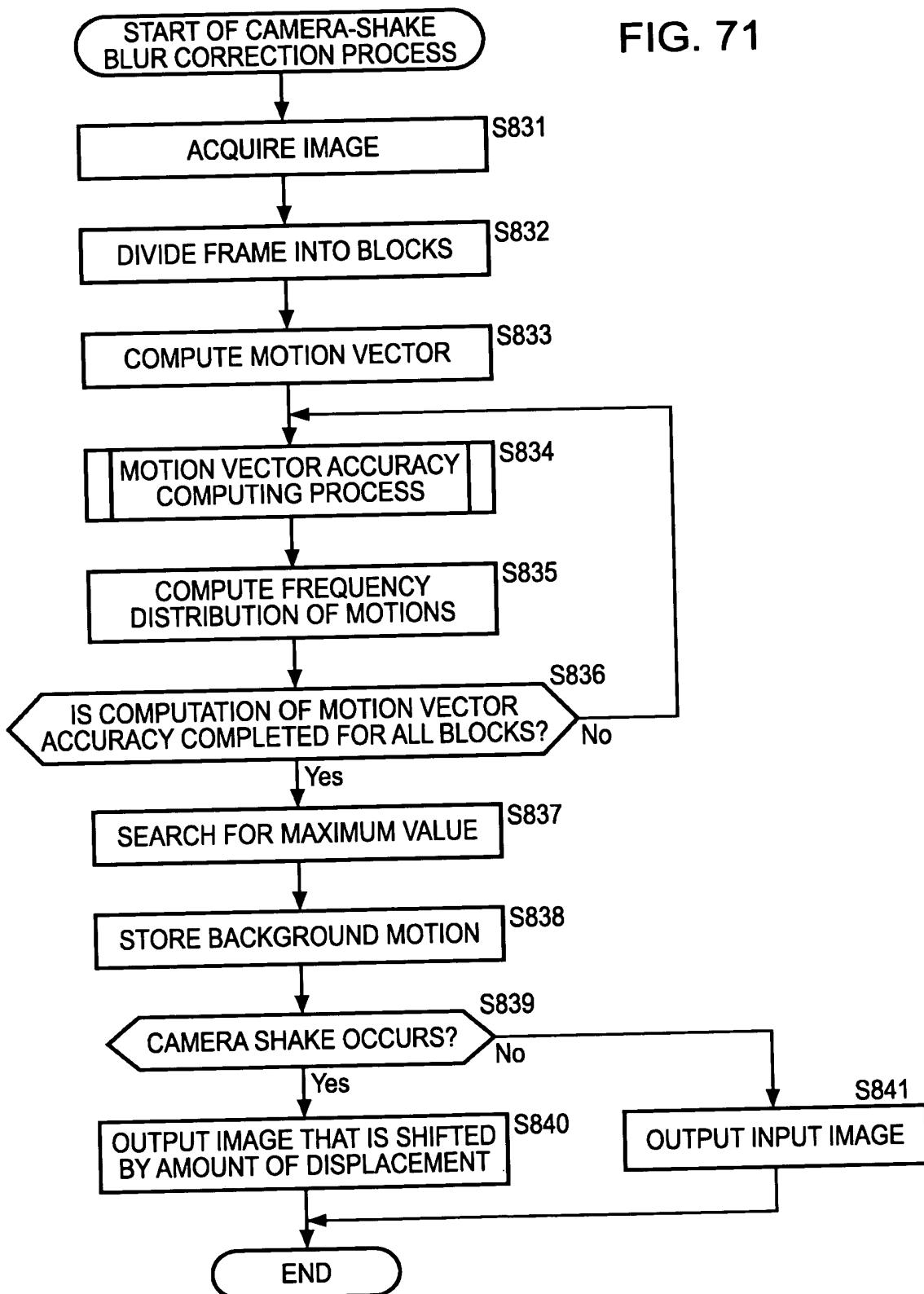


FIG. 72

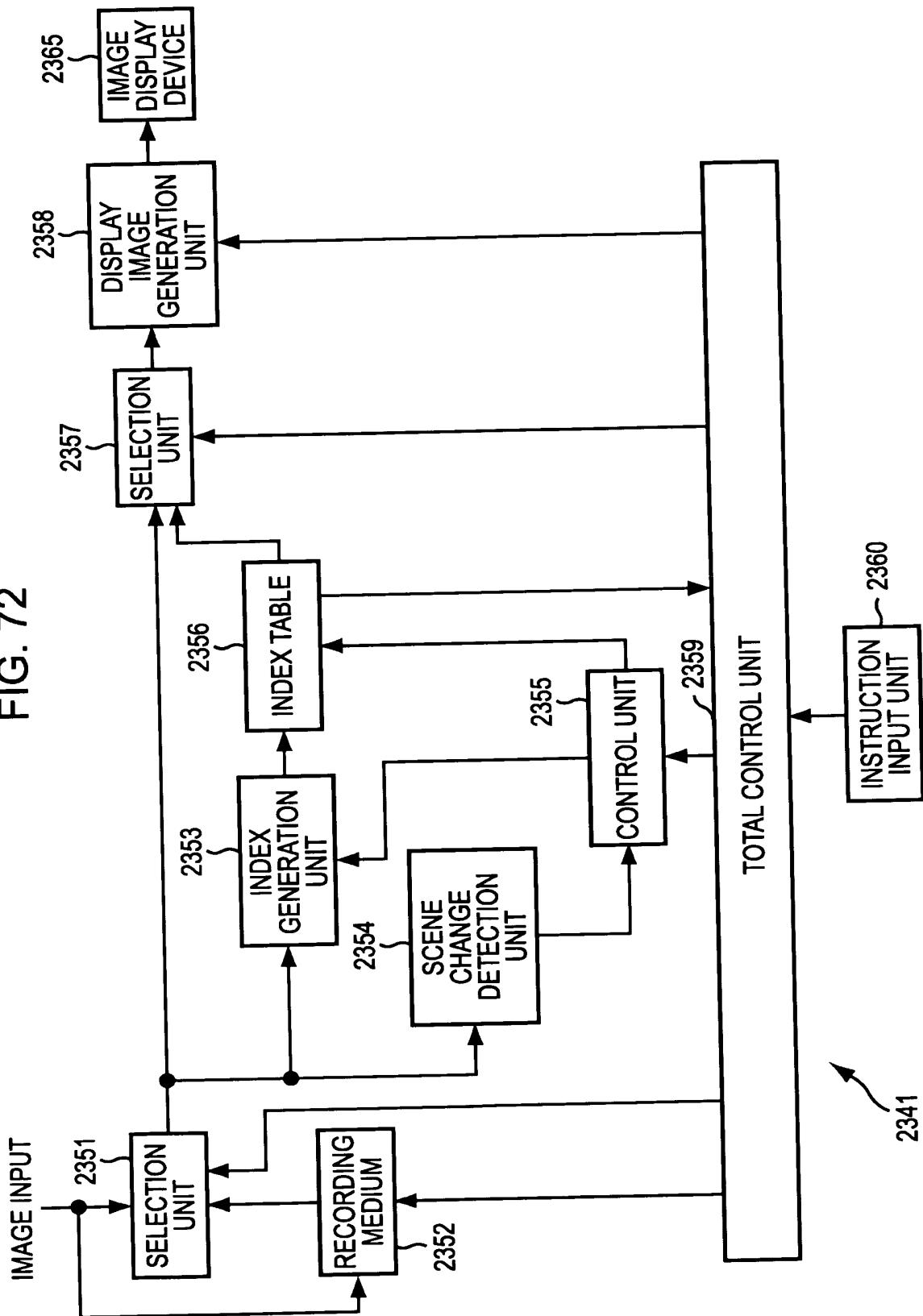


FIG. 73

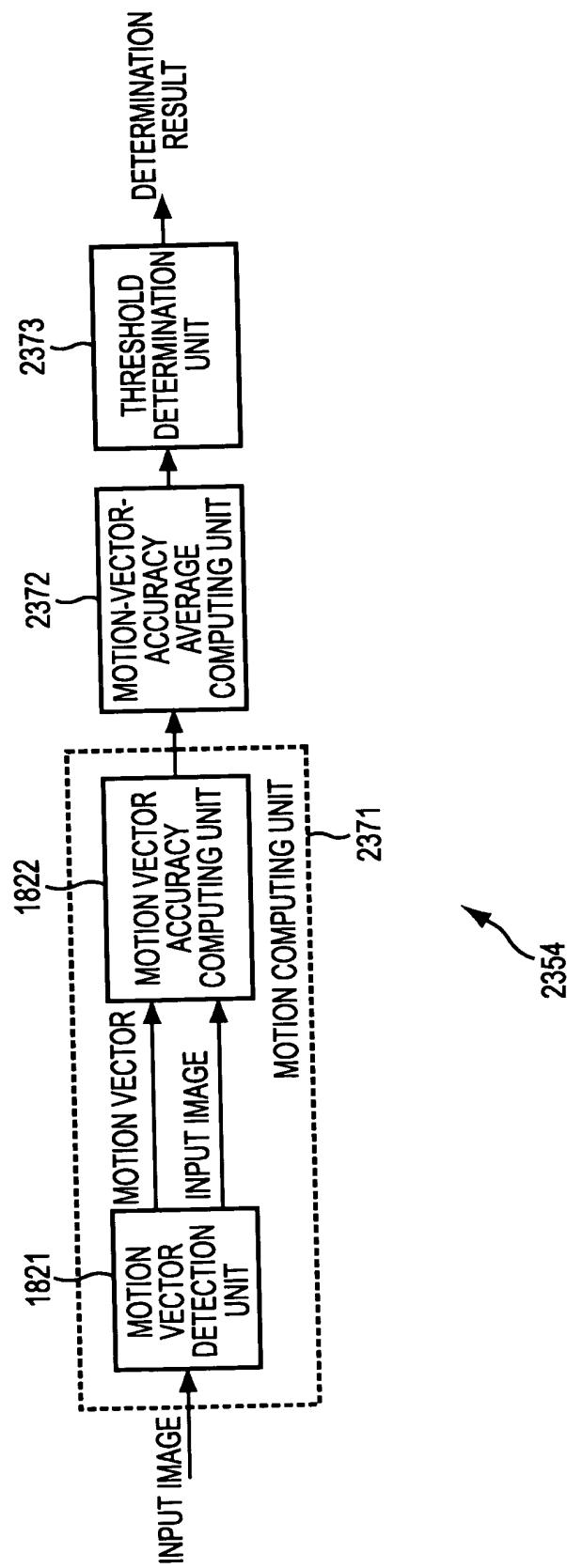


FIG. 74

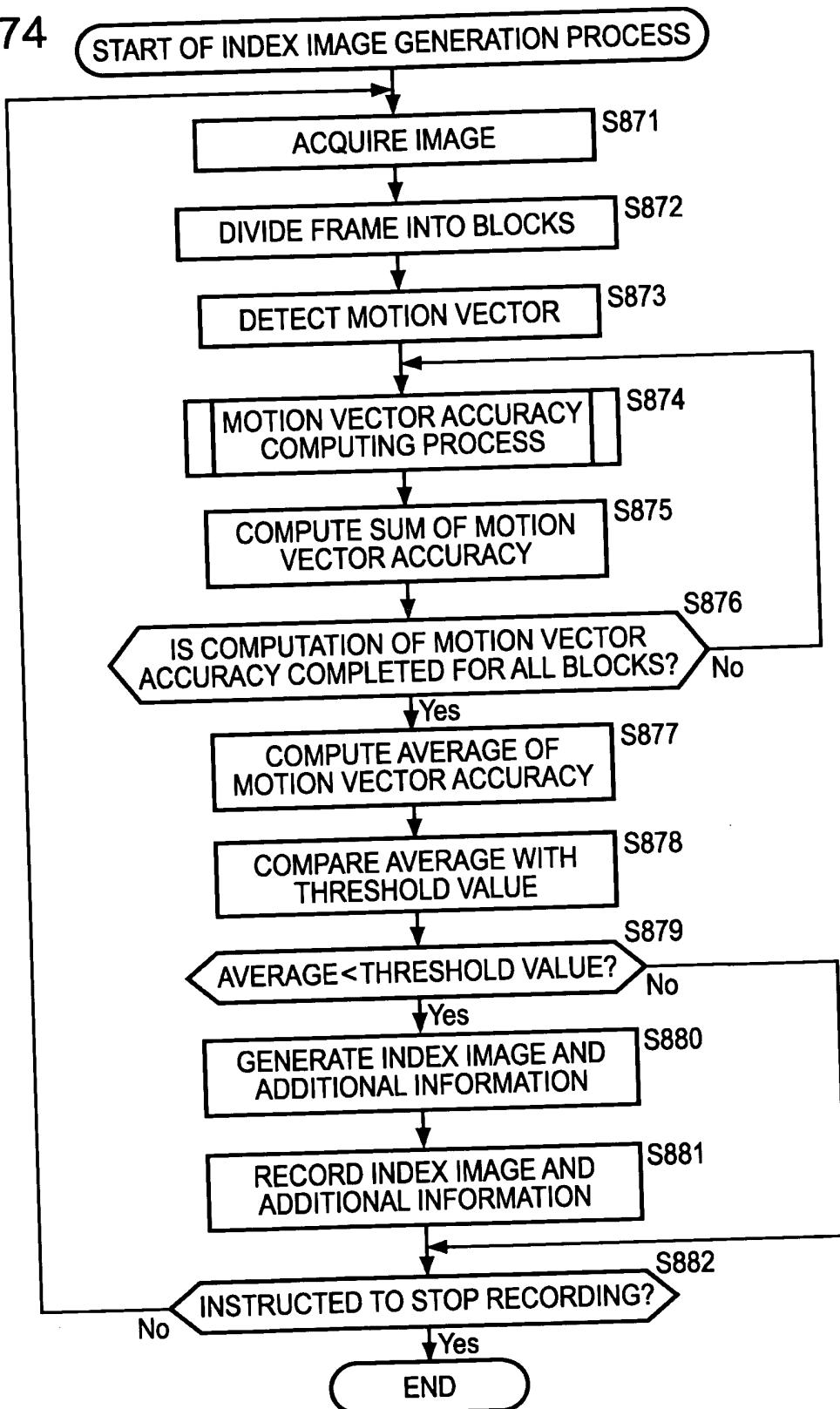


FIG. 75

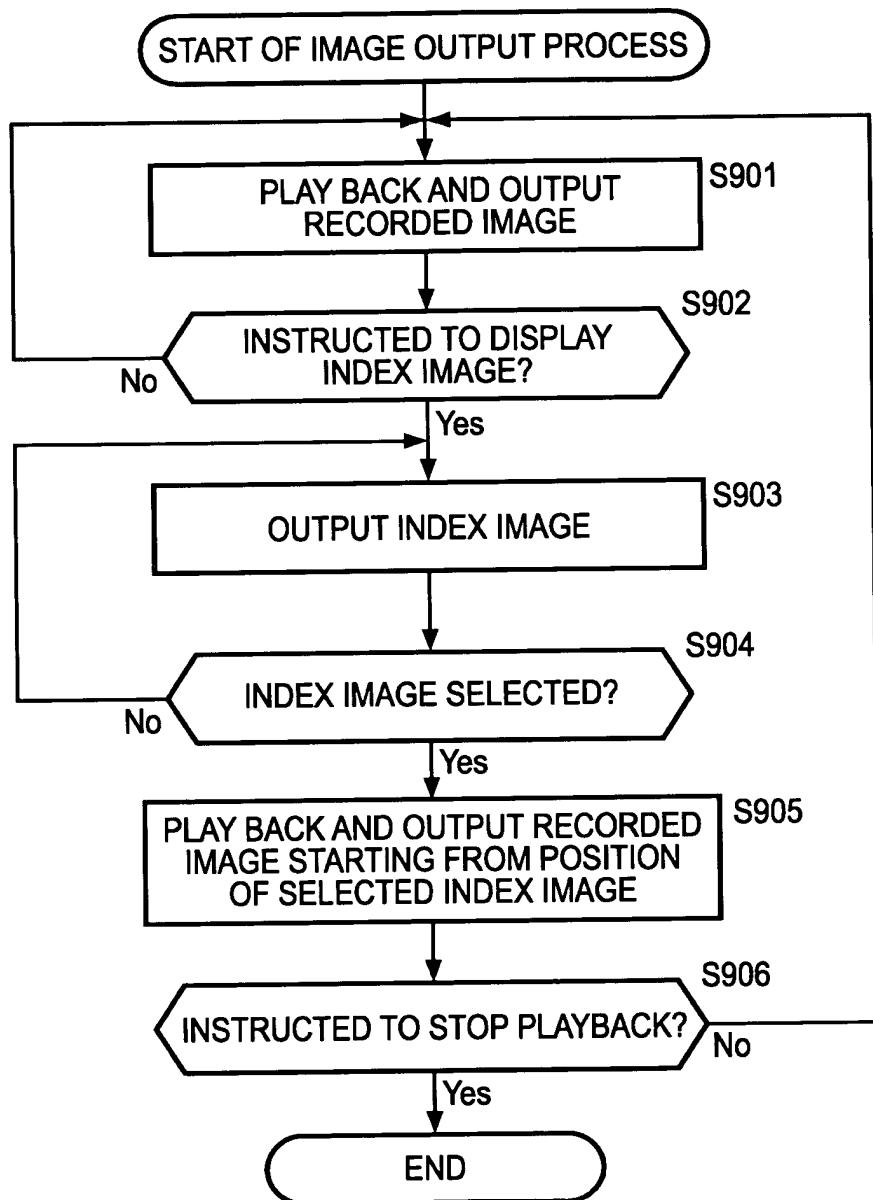


FIG. 76

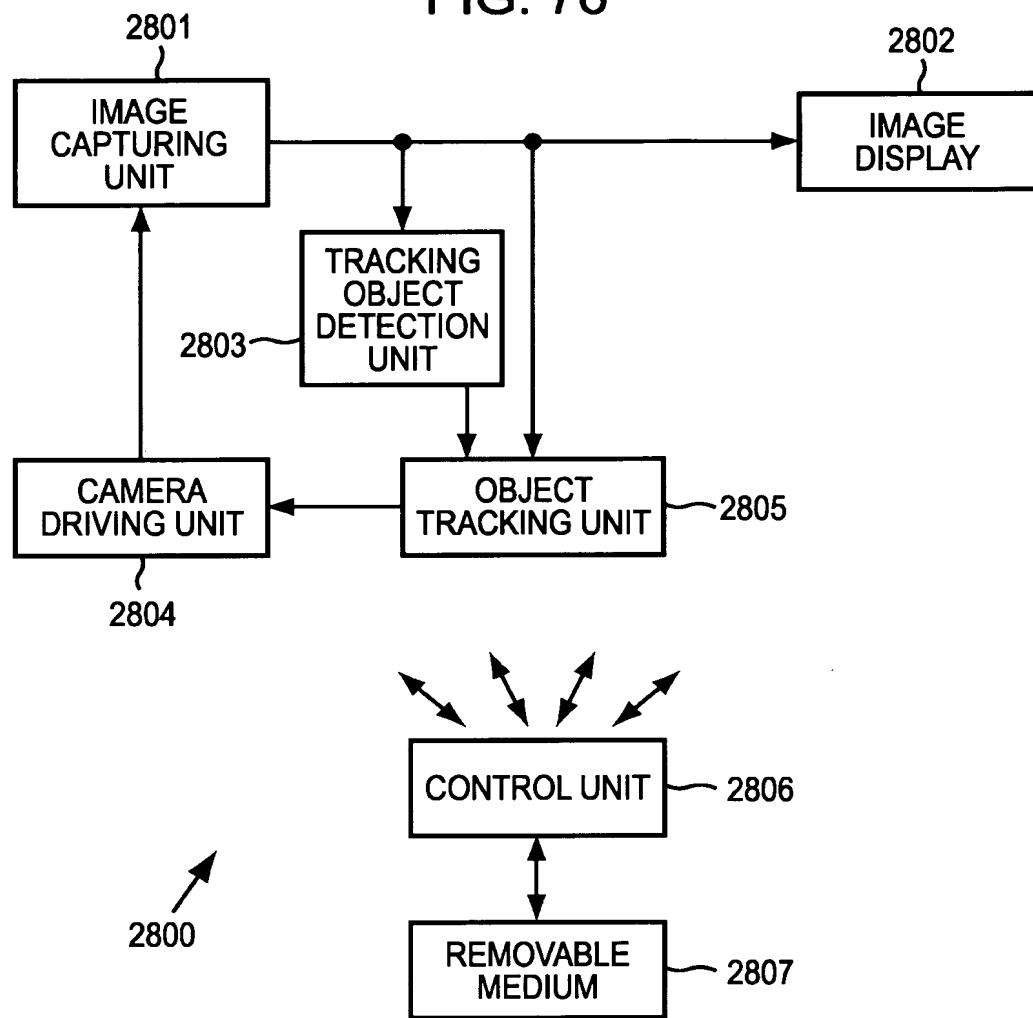


FIG. 77

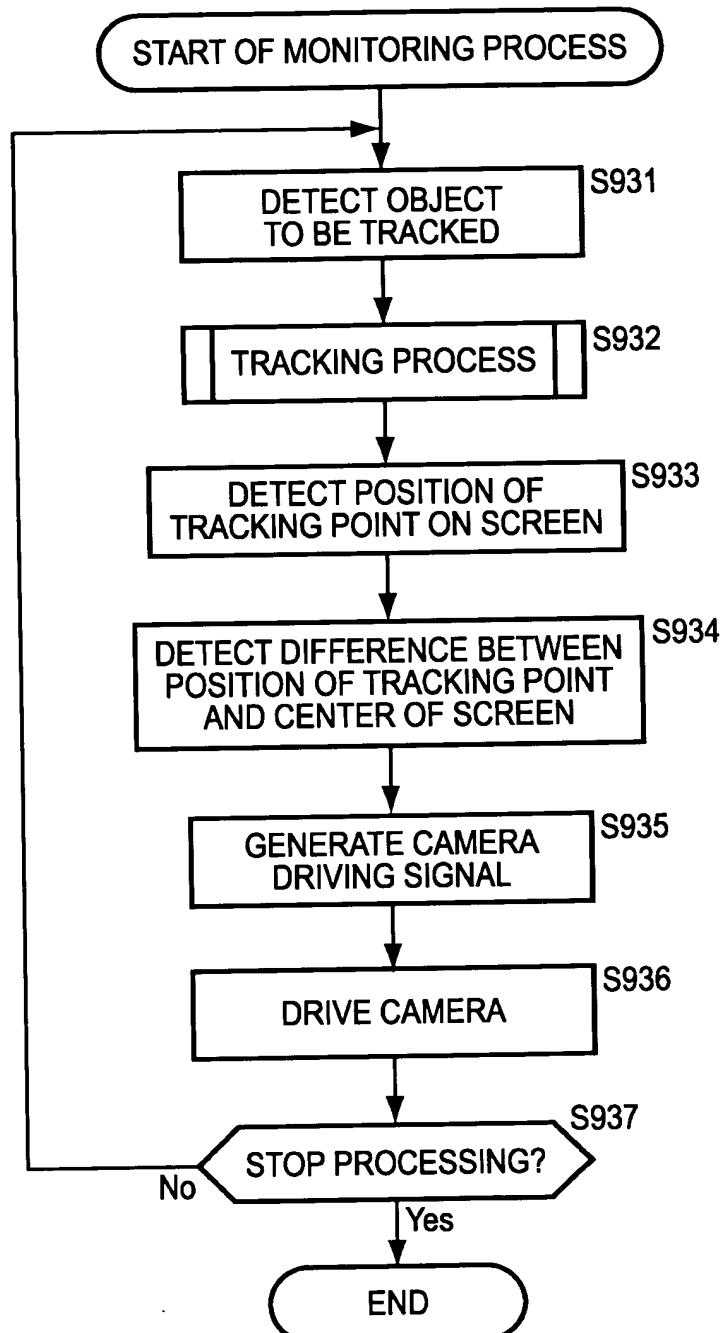


FIG. 78

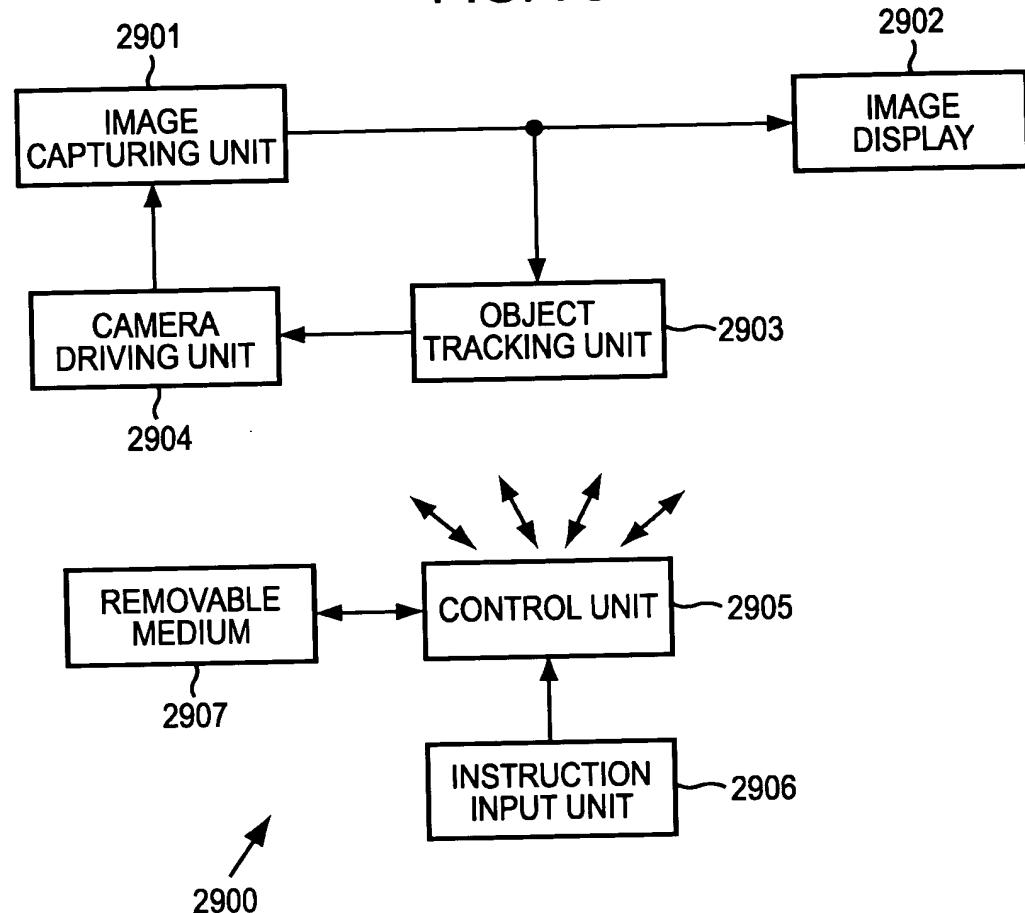


FIG. 79

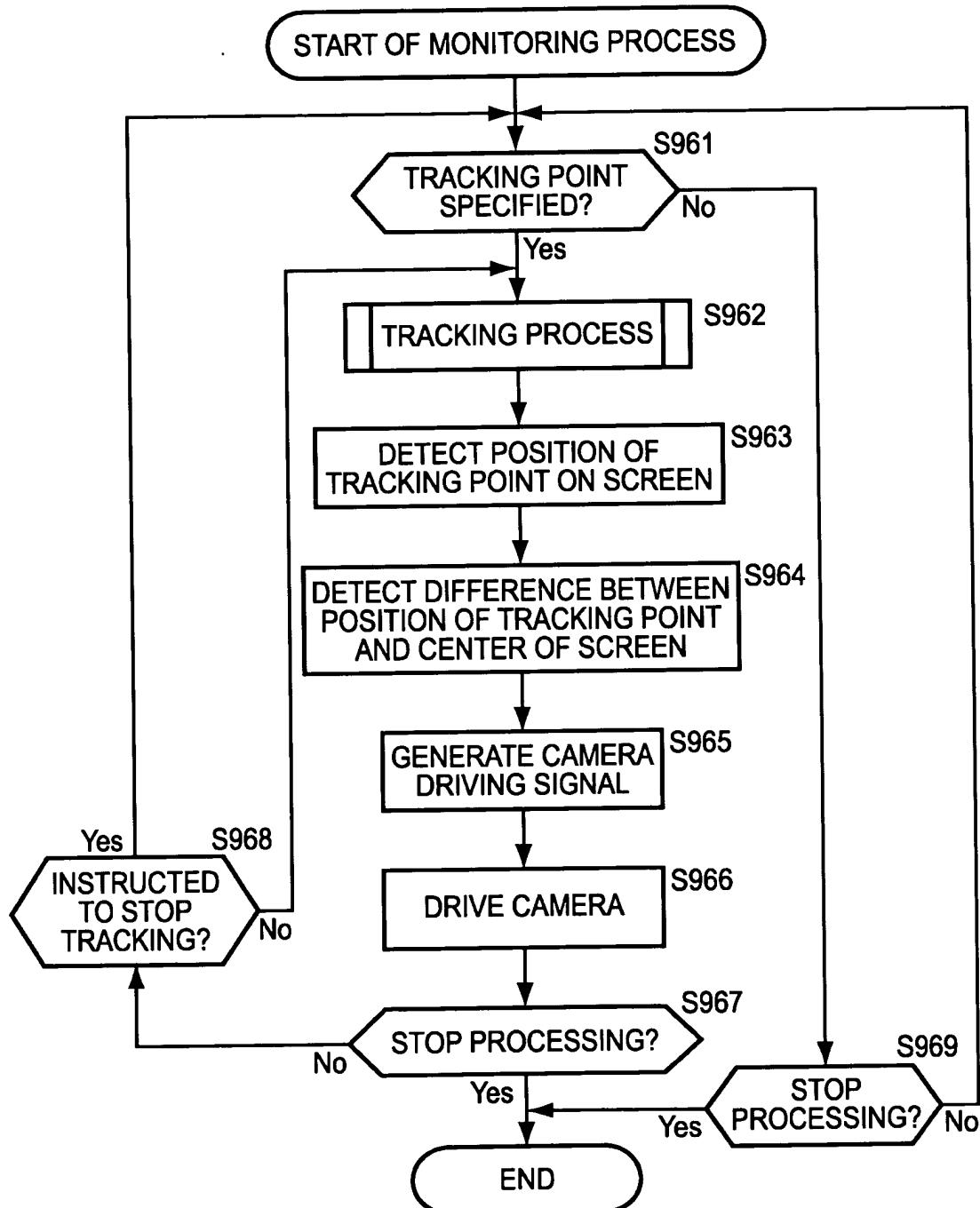
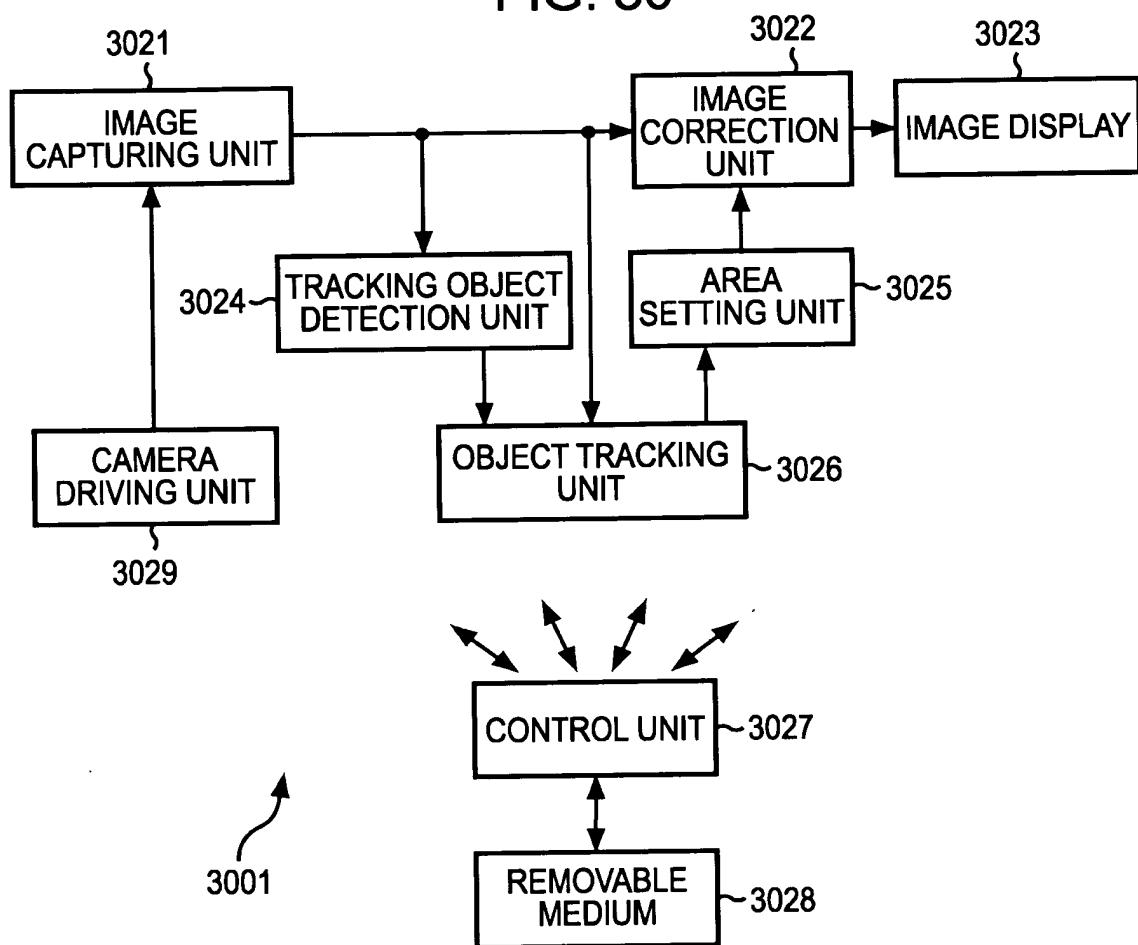
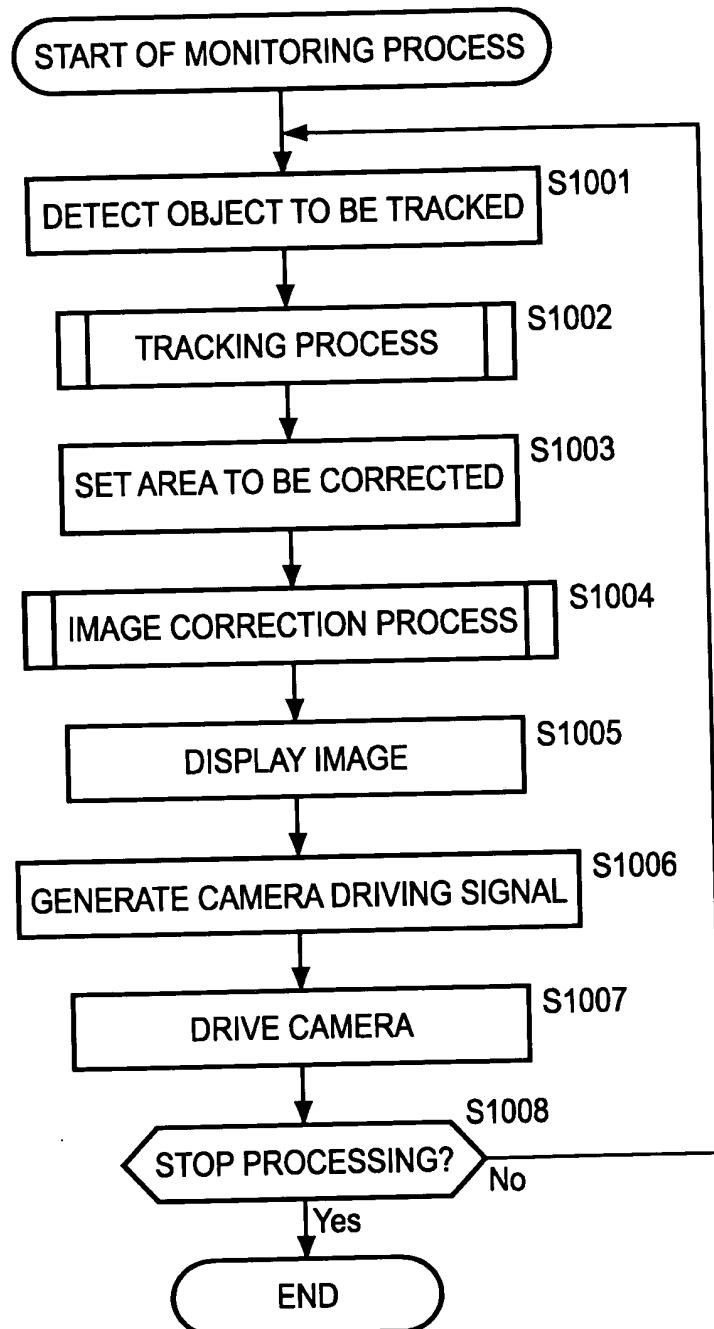


FIG. 80



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FIG. 81



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FIG. 82A

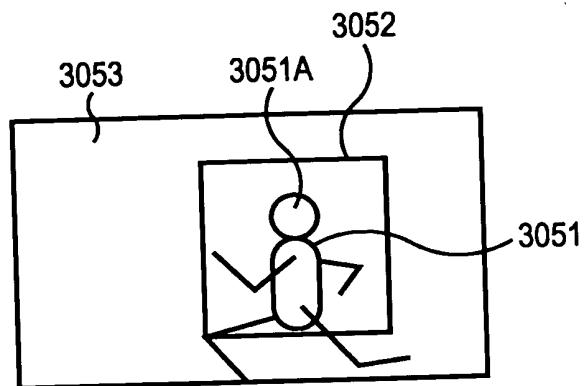


FIG. 82B

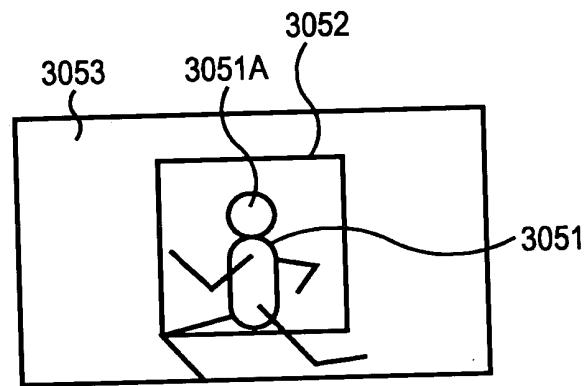


FIG. 82C

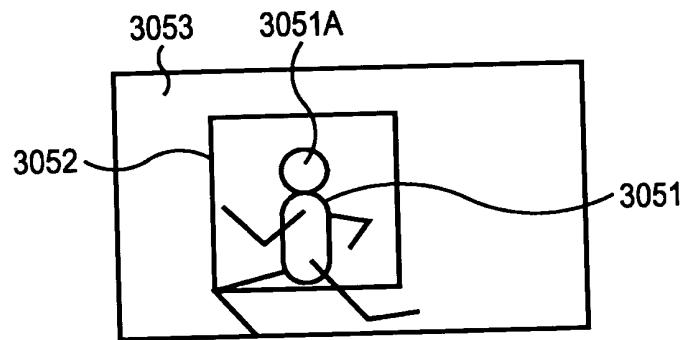


FIG. 83

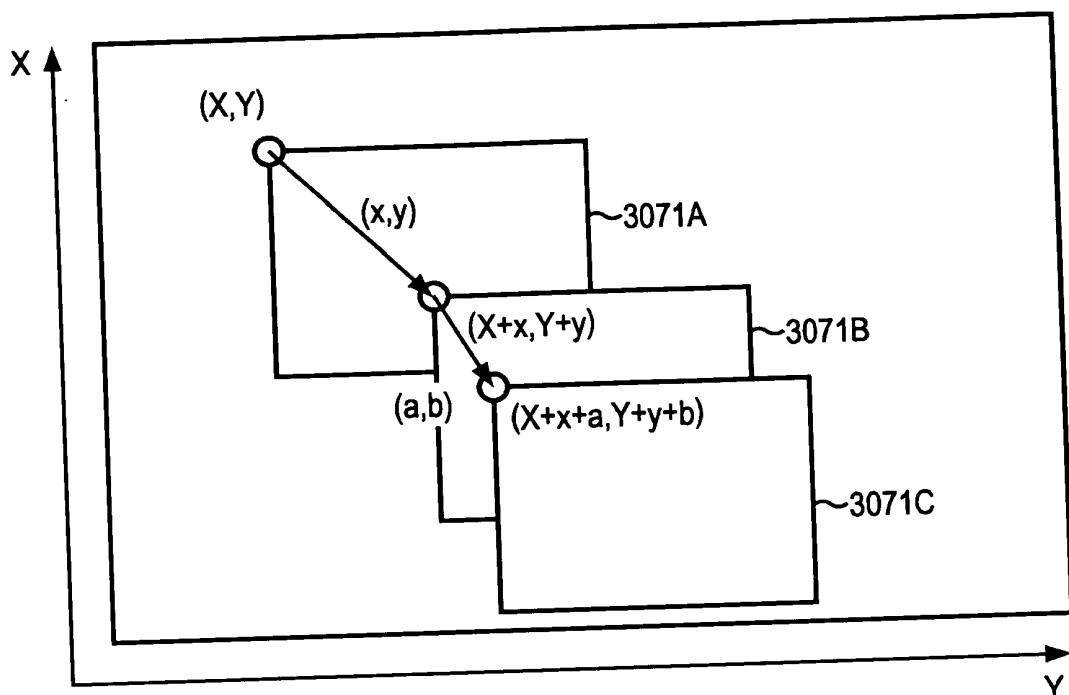
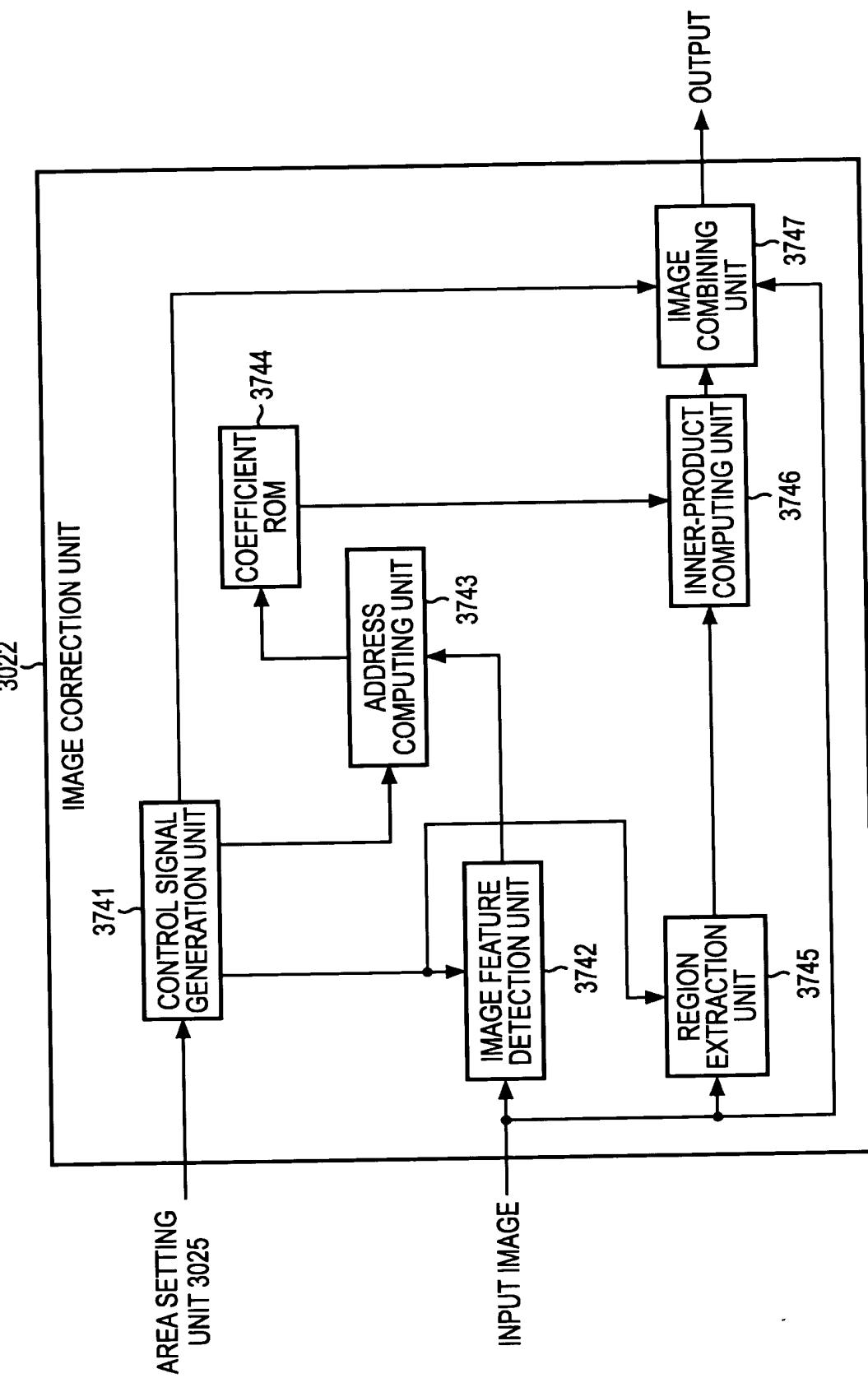


FIG. 84



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FIG. 85

NAME	RELATED BLOCK	DEFINITION
CONTROL SIGNAL A	IMAGE COMBINING UNIT, REGION EXTRACTION UNIT	PULSE FOR IDENTIFYING PROCESSING AREA
CONTROL SIGNAL B	ADDRESS COMPUTING UNIT	PARAMETER σ INDICATING LEVEL OF BLURRING
CONTROL SIGNAL C	ADDRESS COMPUTING UNIT	SWITCHING OF WEIGHT Wa OF RELATIONAL EXPRESSION
CONTROL SIGNAL D	IMAGE FEATURE DETECTION UNIT	SWITCHING OF THRESHOLD VALUE

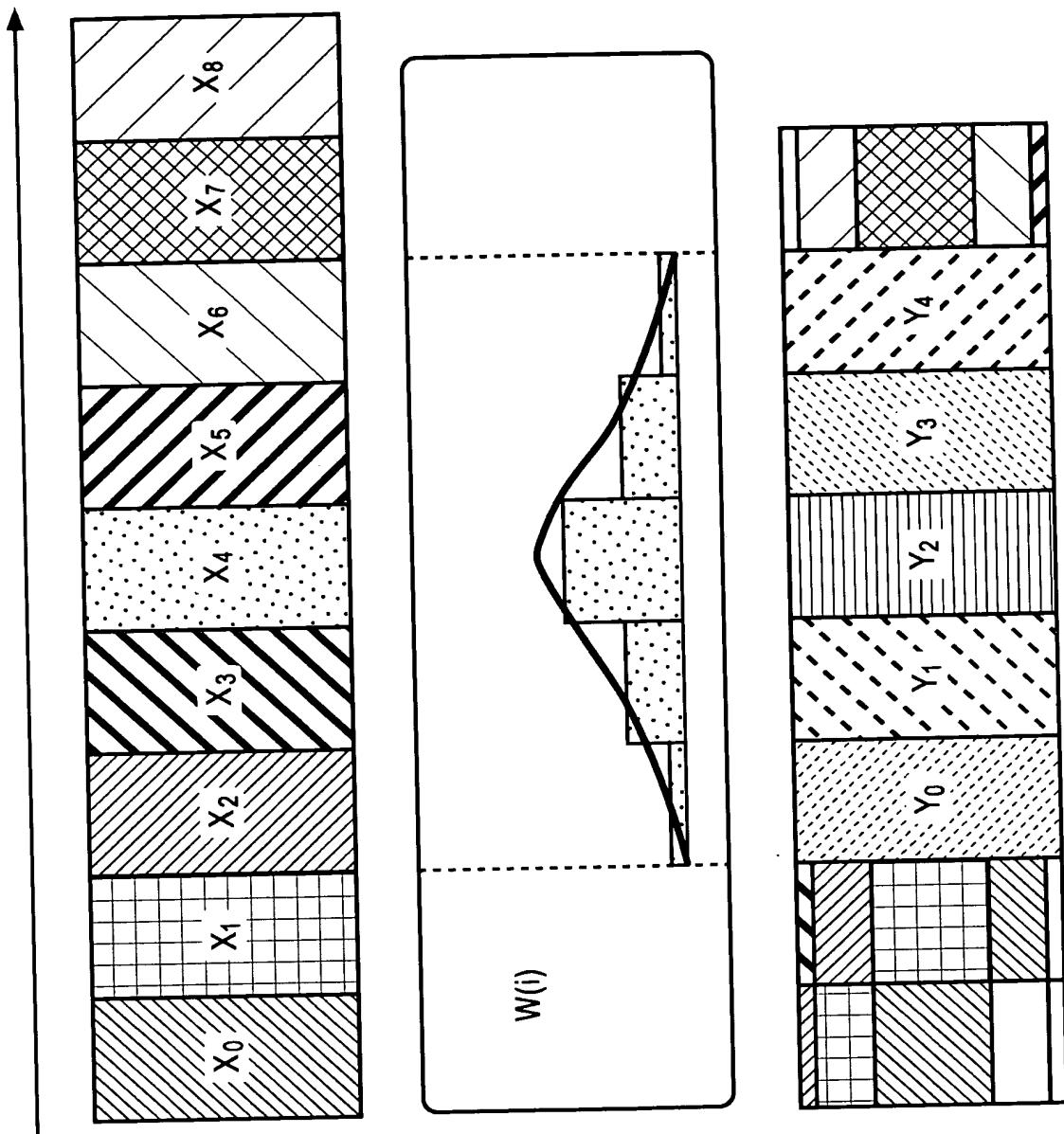


FIG. 86A

FIG. 86B

FIG. 86C

FIG. 87

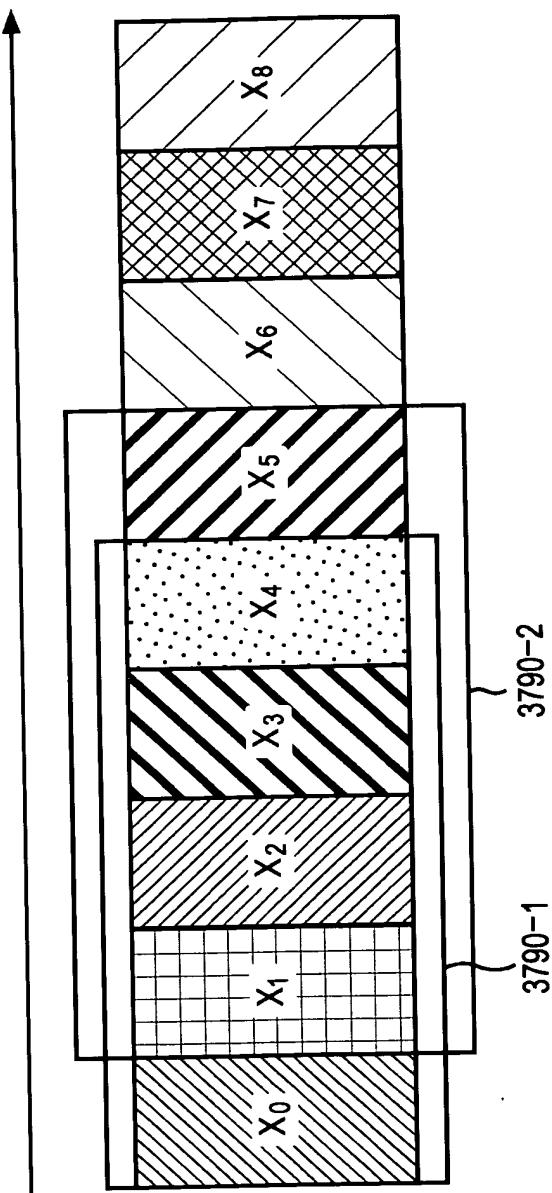


FIG. 88

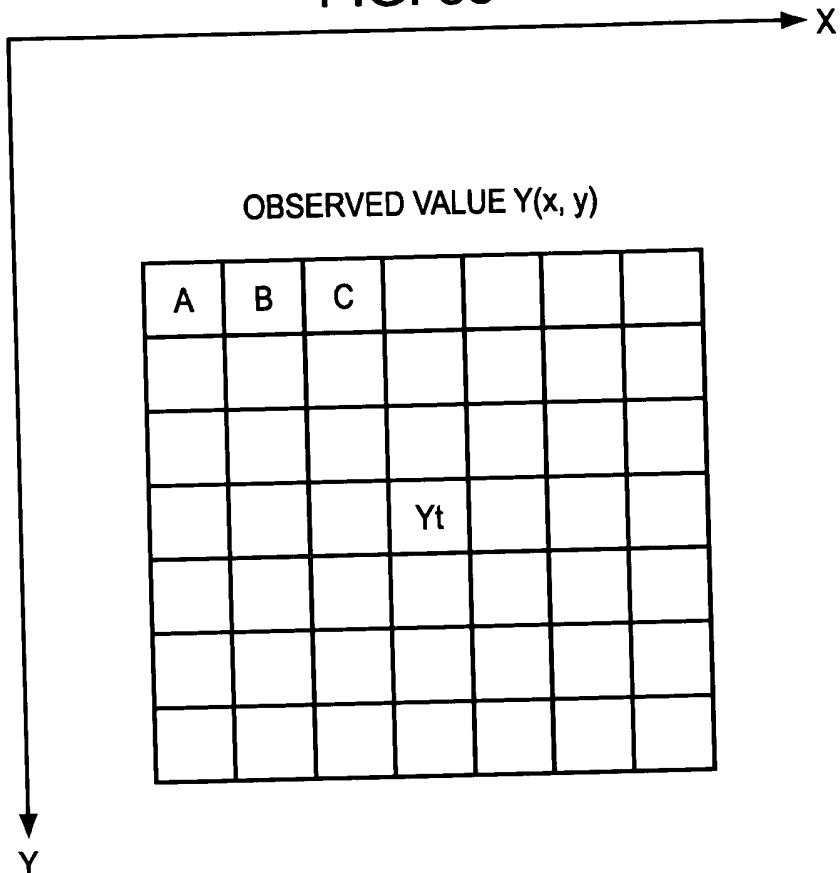
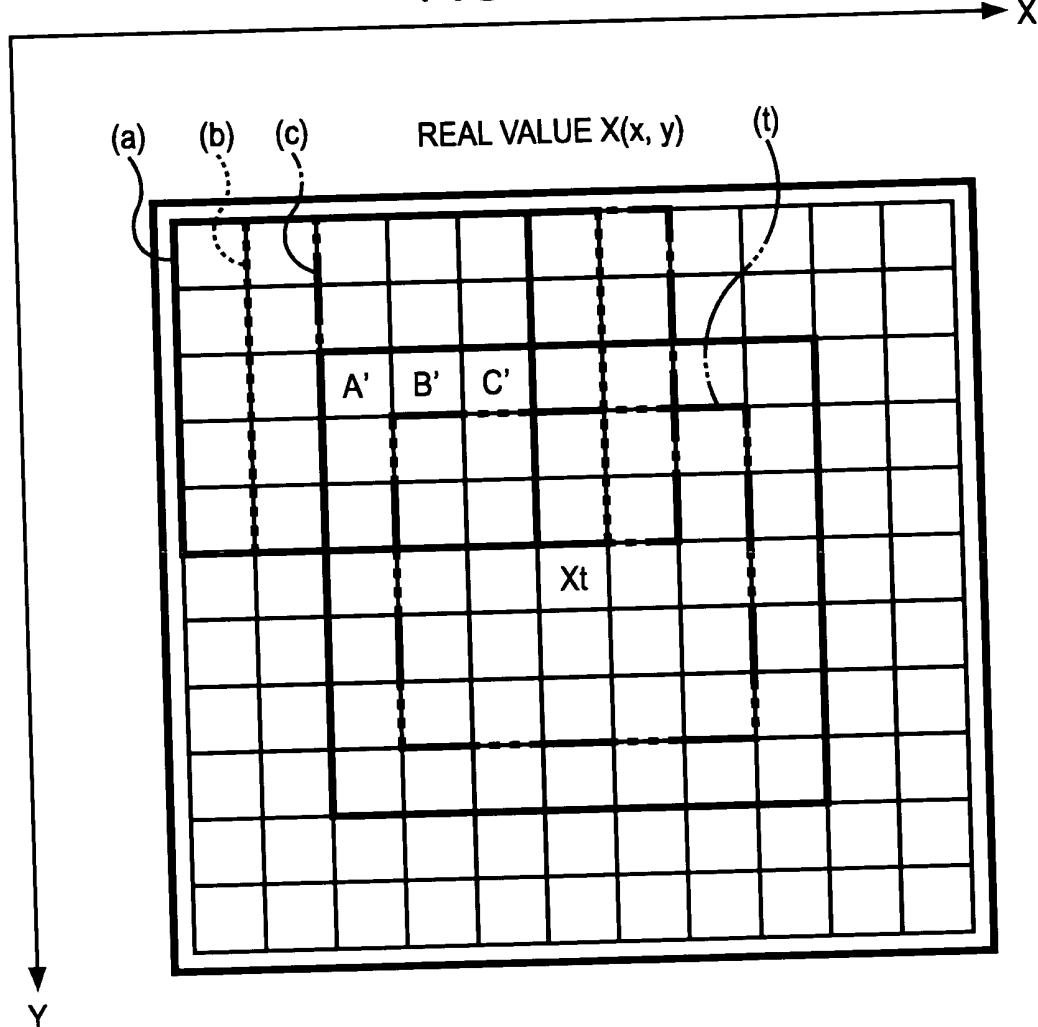


FIG. 89



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FIG. 90

PARAMETER CODE p_2	W_1	W_2	W_3	W_4
0001	0.1	0.1	0.1	5.4
0010	0.1	0.1	5.4	0.1
0100	0.1	5.4	0.1	0.1
1000	5.4	0.1	0.1	0.1
0011	0.1	0.1	2.6	2.6
0101	0.1	2.6	0.1	2.6
0110	0.1	2.6	2.6	0.1
1001	2.6	0.1	0.1	2.6
1010	2.6	0.1	2.6	0.1
1100	2.6	2.6	0.1	0.1
0111	0.3	1.7	1.7	1.7
1011	1.7	0.3	1.7	1.7
1101	1.7	1.7	0.3	1.7
1110	1.7	1.7	1.7	0.3
1111	1.3	1.7	1.3	1.3

FIG. 91

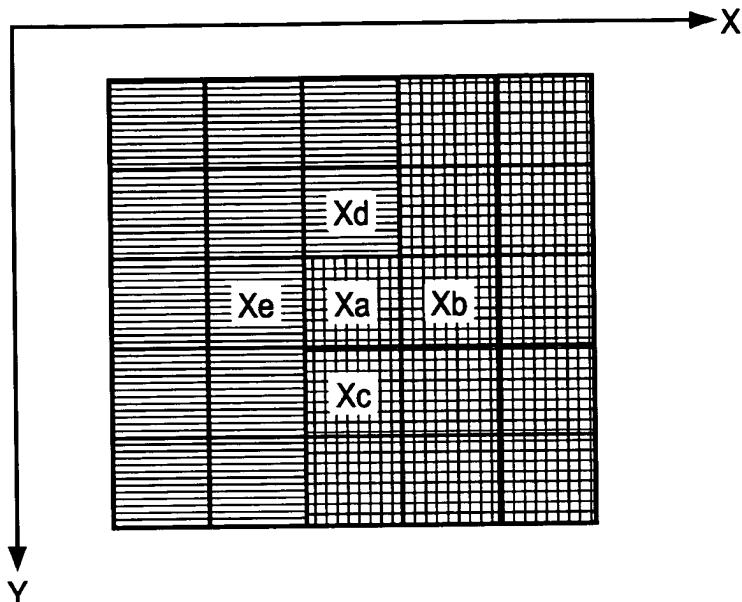


FIG. 92

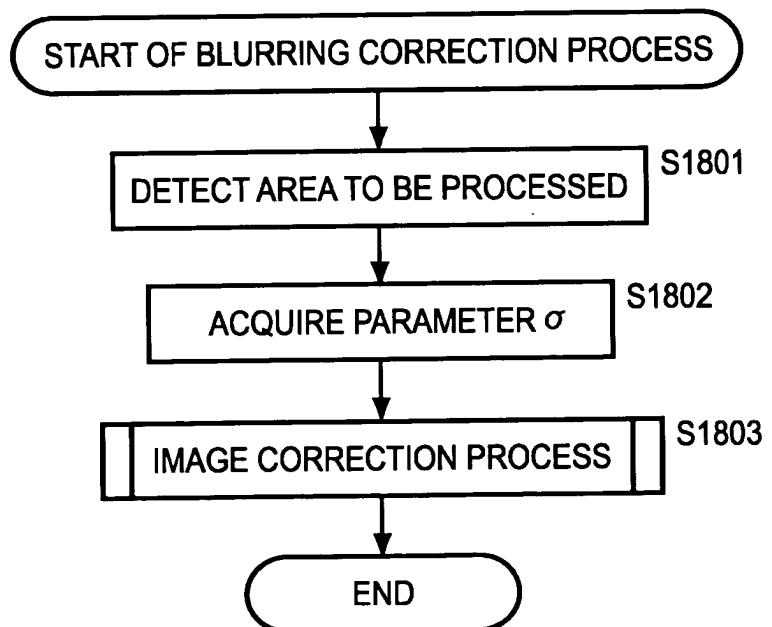


FIG. 93

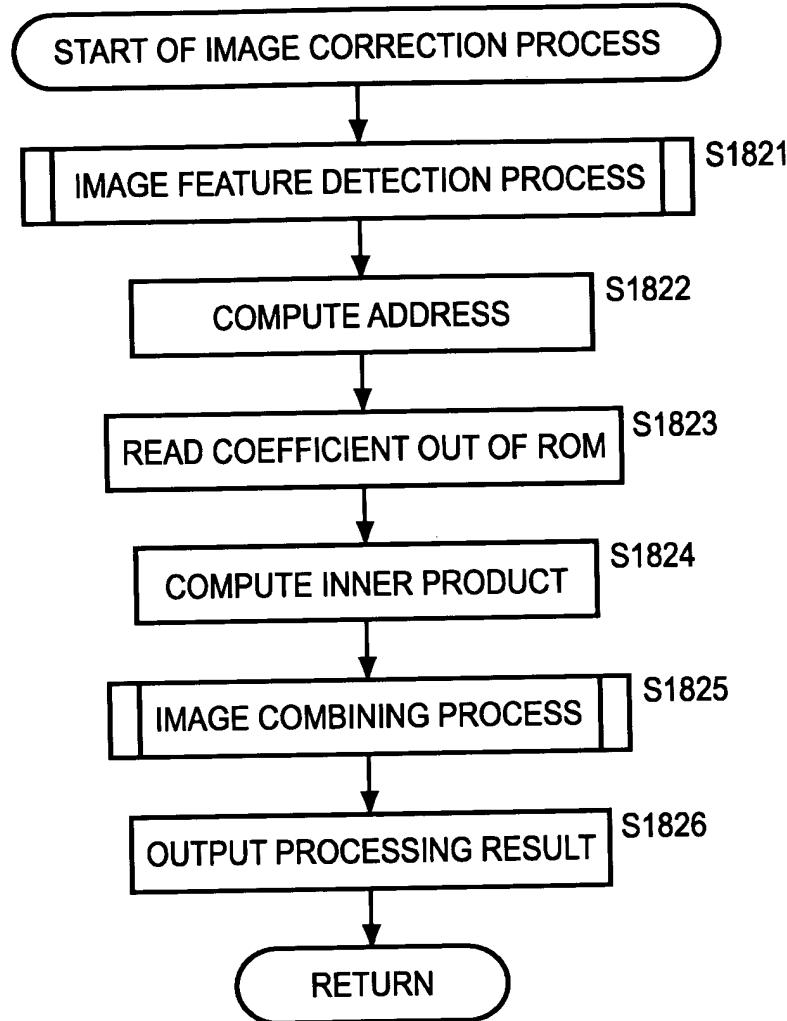


FIG. 94

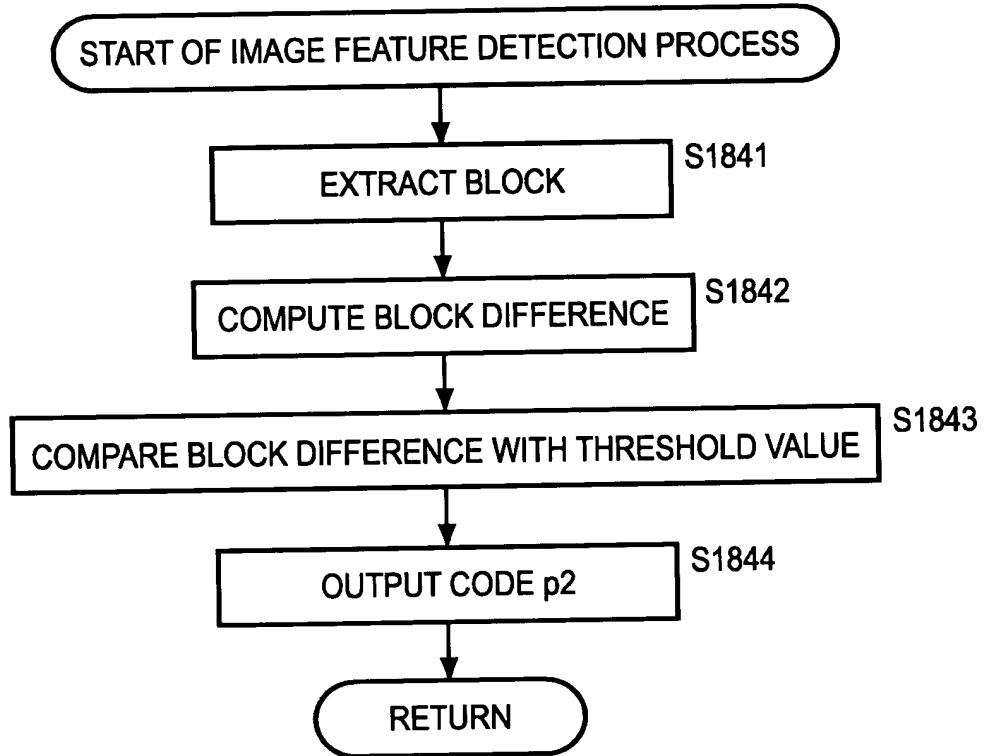


FIG. 95

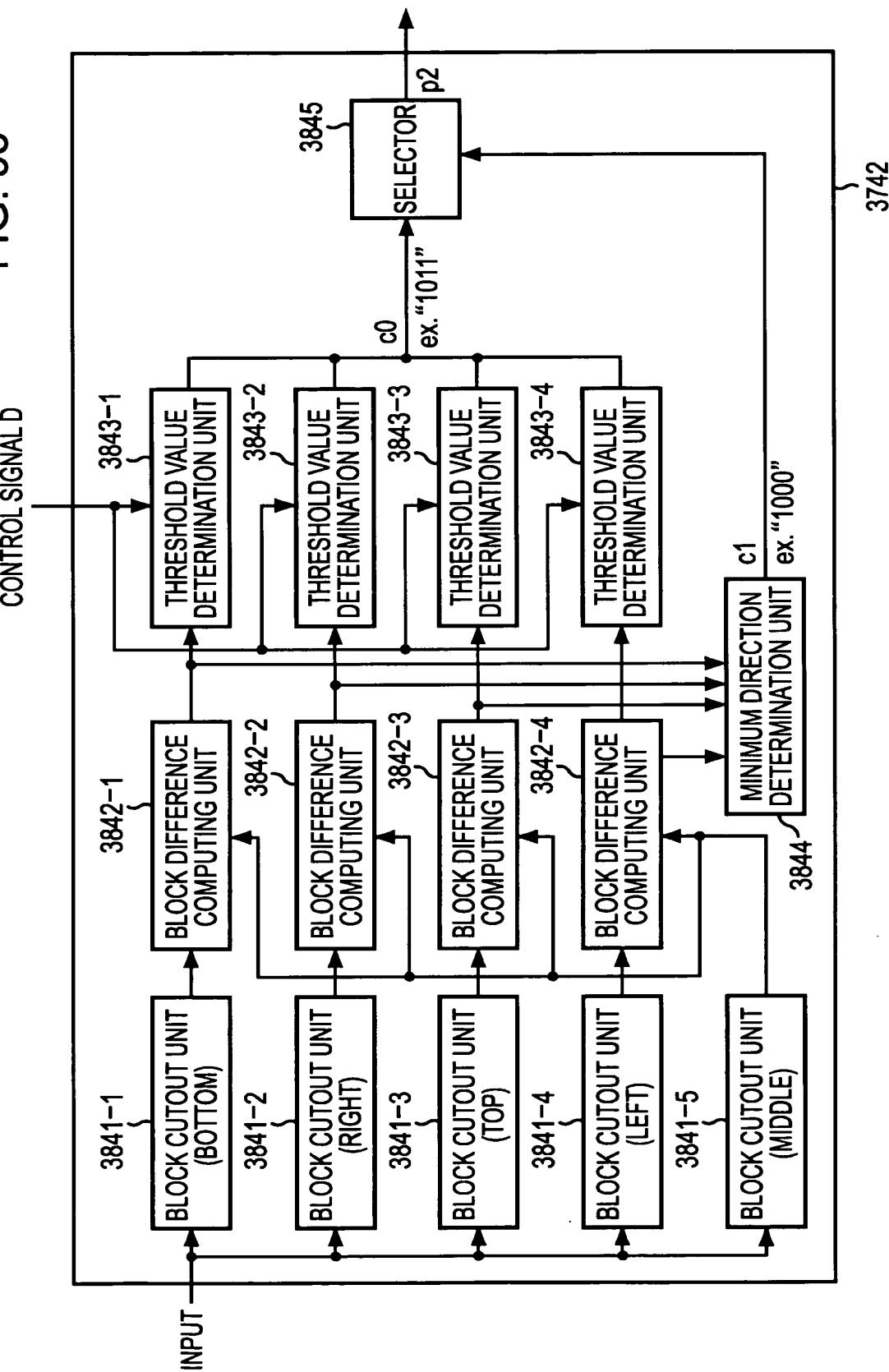


FIG. 96A

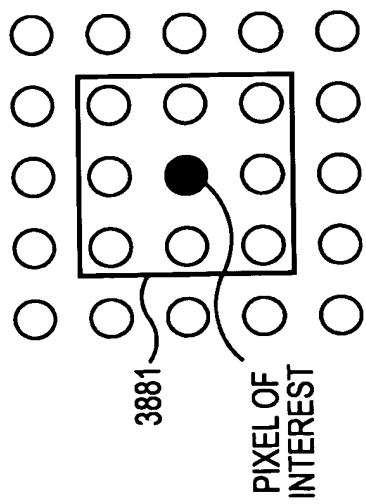


FIG. 96B

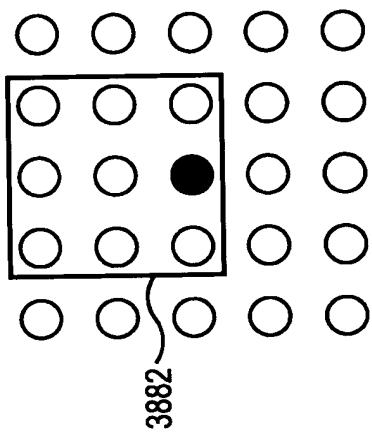


FIG. 96C

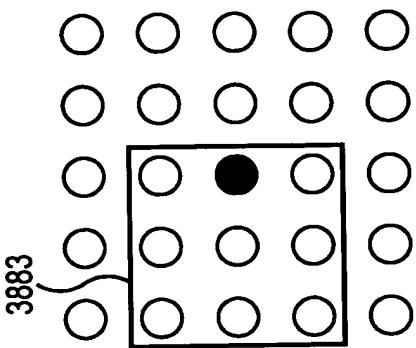


FIG. 96D

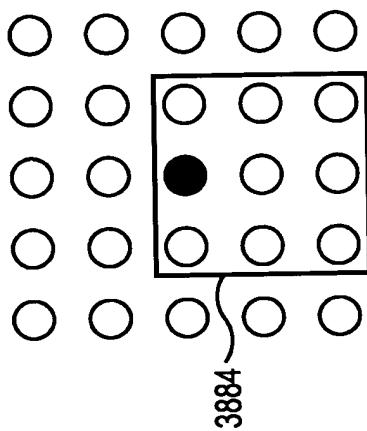


FIG. 96E

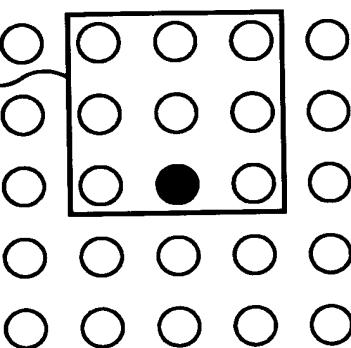
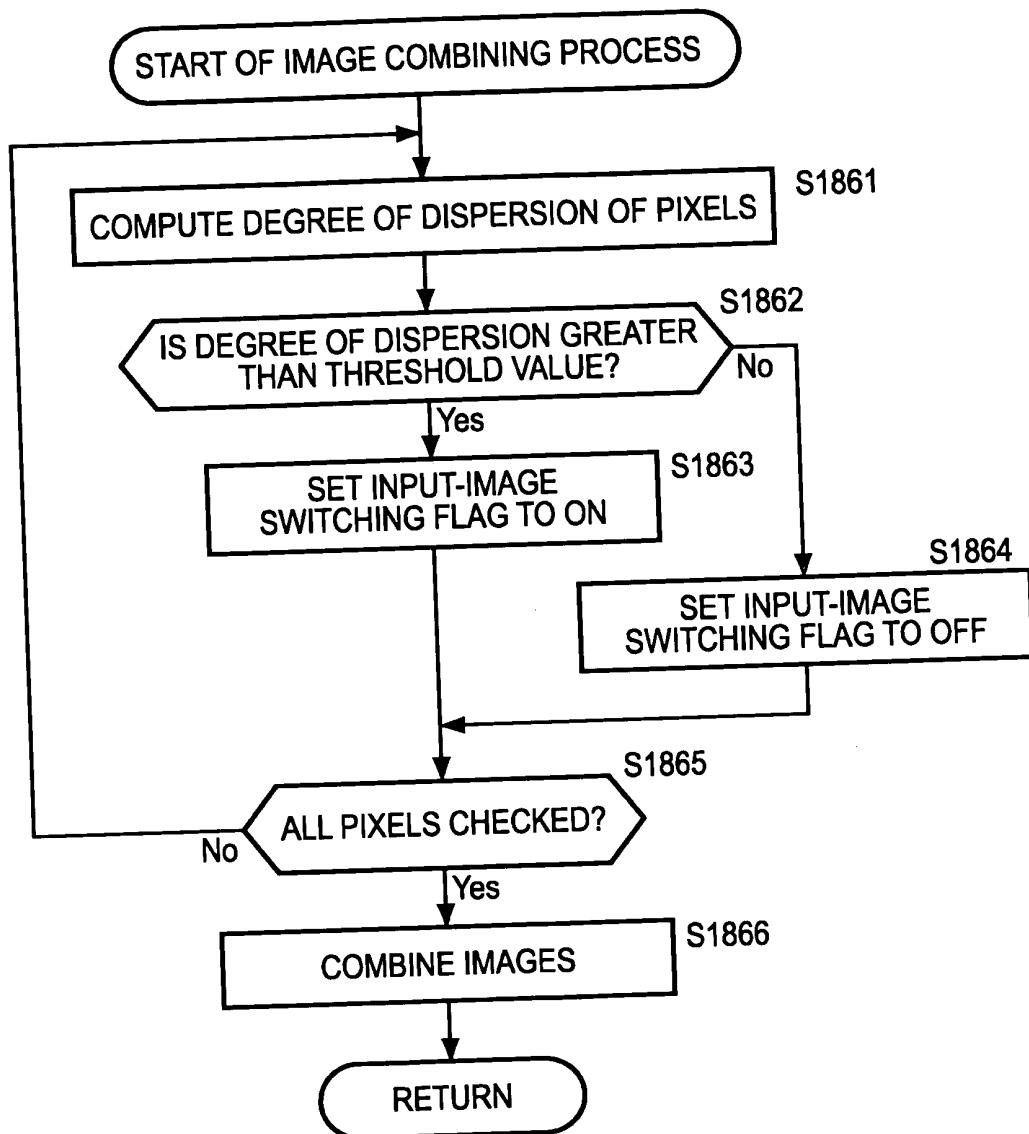
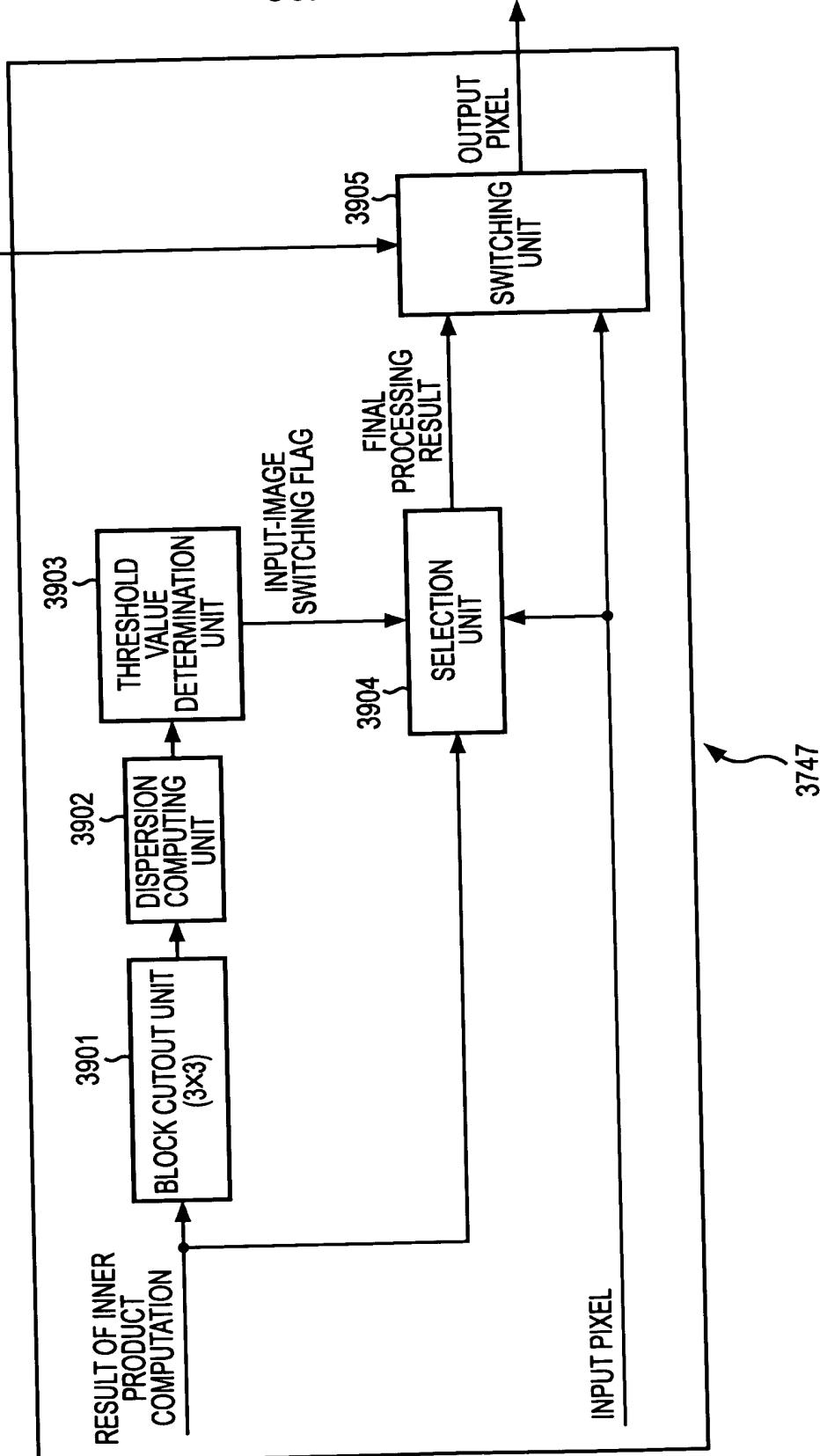


FIG. 97



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FIG. 98
CONTROL SIGNAL A
(PROCESSING AREA)



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FIG. 99

DISPERSION COMPUTING

